



BE1-11

Protection Systems

Modbus® Communication Protocol

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Preface

This instruction manual provides information about the BE1-11 Protection Systems with the Modbus® protocol. To accomplish this, the following information is provided:

- General information
- Register table

Conventions Used in this Manual

Important safety and procedural information is emphasized and presented in this manual through warning, caution, and note boxes. Each type is illustrated and defined as follows.

Warning!

Warning boxes call attention to conditions or actions that may cause personal injury or death.

Caution

Caution boxes call attention to operating conditions that may lead to equipment or property damage.

Note

Note boxes emphasize important information pertaining to installation or operation.



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Warning!

READ THIS MANUAL. Read this manual before installing, operating, or maintaining the BE1-11. Note all warnings, cautions, and notes in this manual as well as on the product. Keep this manual with the product for reference. Only qualified personnel should install, operate, or service this system. Failure to follow warning and cautionary labels may result in personal injury or property damage. Exercise caution at all times.

Caution

Installing previous versions of firmware may result in compatibility issues causing the inability to operate properly and may not have the enhancements and resolutions to issues that more recent versions provide. Basler Electric highly recommends using the latest version of firmware at all times. Using previous versions of firmware is at the user's risk and may void the warranty of the unit.

Basler Electric does not assume any responsibility to compliance or noncompliance with national code, local code, or any other applicable code. This manual serves as reference material that must be well understood prior to installation, operation, or maintenance.

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It is not the intention of this manual to cover all details and variations in equipment, nor does this manual provide data for every possible contingency regarding installation or operation. The availability and design of all features and options are subject to modification without notice. Over time, improvements and revisions may be made to this publication. Before performing any of the following procedures, contact Basler Electric for the latest revision of this manual.

The English-language version of this manual serves as the only approved manual version.

Revision History

A historical summary of the changes made to this instruction manual is provided below. Revisions are listed in reverse chronological order.

Visit www.basler.com to download the latest hardware, firmware, and BESTCOMSPPlus® revision histories.

Instruction Manual Revision History

Manual Revision and Date	Change
H, Jul-21	<ul style="list-style-type: none"> • Changed registers 2922, 2926, 2930, and 2934 to Reserved • Changed name of registers 10100 through 10192, 10232, 10234, and 10282 through 10308 • Changed registers 10242 through 10272 from “R” to “R W” • Removed Rev Letter from all pages • Changed sequential numbering to sectional numbering • Moved Instruction Manual Revision History into Preface • Minor text edits throughout manual
G, Feb-19	<ul style="list-style-type: none"> • Added registers for BE1-11<i>d</i> • Minor edits throughout manual
F1, Nov-18	<ul style="list-style-type: none"> • Added Prop 65 warning on back of cover page
F, May-17	<ul style="list-style-type: none"> • Added the following registers: <ul style="list-style-type: none"> • 399 through 417 • 984 bits 6 through 13 • 2580 through 2585 • 6608 through 6630 • 18717 through 18728 • Changed the following registers: <ul style="list-style-type: none"> • 2426 • 5178 through 5622 • 18126 • 22540 through 22984 • 26284 through 27338
E, Feb-17	<ul style="list-style-type: none"> • Added caution statement about nonvolatile memory
D, Jul-16	<ul style="list-style-type: none"> • Added registers 983 bit 9 through 984 bit 5, 18319, 18323, 26212 through 26282, and 35188 through 35540
C, Dec-15	<ul style="list-style-type: none"> • Added registers 500 through 577, 18695 through 18711, and 25884 through 26210
B, Aug-14	<ul style="list-style-type: none"> • Added <i>Alarms and Targets Reporting</i> chapter
A, Apr-14	<ul style="list-style-type: none"> • Initial release



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1 • General Information

This document describes the Modbus® communications protocol employed by BE1-11 Protection Systems and how to exchange information with these protection systems over a Modbus network. The BE1-11 communicates by emulating a subset of the Modicon 984 Programmable Controller.

Modbus communications use a master-slave technique in which only the master can initiate a transaction. This transaction is called a query. When appropriate, a slave (BE1-11) responds to the query. When a Modbus master communicates with a slave, information is provided or requested by the master. Information residing in the BE1-11 is grouped categorically as follows:

- Global Parameters
- Control Parameters (Select Before Operate)
- Setting Parameters
- Report Parameters
- Metering Parameters

All supported data can be read as specified in the register table. Abbreviations are used in the register table to indicate the register type. Register types are:

- Read/Write = RW
- Read Only = R

Select Before Operate (SBO) functions are used to change active settings groups and control outputs. There are four settings groups in the BE1-11, one of which may be selected as active using SBO commands.

When a slave receives a query, the slave responds by either supplying the requested data to the master or performing the requested action. A slave device never initiates communications on the Modbus network and will always generate a response to the query unless certain error conditions occur. The BE1-11 is designed to communicate on the Modbus network only as a slave device.

Caution

This product contains one or more *nonvolatile memory* devices. Nonvolatile memory is used to store information (such as settings) that needs to be preserved when the product is power-cycled or otherwise restarted. Established nonvolatile memory technologies have a physical limit on the number of times they can be erased and written. In this product, the limit is 100,000 erase/write cycles. During product application, consideration should be given to communications, logic, and other factors that may cause frequent/repeated writes of settings or other information that is retained by the product. Applications that result in such frequent/repeated writes may reduce the useable product life and result in loss of information and/or product inoperability.

Message Structure

Device Address Field

The device address field contains the unique Modbus address of the slave being queried. The addressed slave repeats the address in the device address field of the response message. This field is 1 byte.

Modbus protocol limits a device address from 1 - 247. The address is user-selectable at installation and can be altered during real-time operation.

Function Code Field

The function code field in the query message defines the action to be taken by the addressed slave. This field is echoed in the response message and is altered by setting the most significant bit (MSB) of the field to 1 if the response is an error response. This field is 1 byte in length.

The BE1-11 maps all available data into the Modicon 984 holding register address space and supports the following function codes:

- Function 03 (03 hex) - read holding registers
- Function 06 (06 hex) - preset single register
- Function 08 (08 hex), subfunction 00 - diagnostics: return query data
- Function 08 (08 hex), subfunction 01 - diagnostics: restart communications option
- Function 08 (08 hex), subfunction 04 - diagnostics: force listen only mode
- Function 16 (10 hex) - preset multiple registers

Data Block Field

The query data block contains additional information needed by the slave to perform the requested function. The response data block contains data collected by the slave for the queried function. An error response will substitute an exception response code for the data block. The length of this field varies with each query. See the paragraphs on *Register Definitions* in this manual for interpretation of data.

Error Check Field

The error check field provides a method for the slave to validate the integrity of the query message contents and allows the master to confirm the validity of response message contents. This field is 2 bytes.

Modbus® Modes of Operation

A standard Modbus network offers the remote terminal unit (RTU) transmission mode for communication. BE1-11 Protection Systems support the Modbus/TCP or RS-485 modes depending on communication options for the protection system. For example, the Modbus/TCP mode is employed when Ethernet Protocol Option "2" (Modbus/TCP with BESTnet™*Plus*) or Ethernet Protocol Option "4" (Modbus/TCP and DNP3 with BESTnet*Plus*) is ordered. See the style chart in the *Introduction* chapter of the appropriate instruction manual for the BE1-11. The BE1-11 also supports the Modbus over RS-485 protocol when the BE1-11 is ordered with the RS-485 Port Protocol Option "M". The BE1-11 supports Modbus/TCP and RS-485 at the same time. These two optional modes of operation are described below.

A master can query slaves individually or universally. A universal ("broadcast") query, when allowed, evokes no response from any slave device. If a query to an individual slave device requests actions unable to be performed by the slave, the slave response message contains an exception response code defining the error detected. Exception response codes are quite often enhanced by the information found in the "Error Details" block of holding registers.

The Modbus protocol defines a simple Protocol Data Unit (PDU) independent of the underlying communication layers. The mapping of the Modbus protocol on specific buses or networks can introduce some additional fields on the Application Data Unit (ADU). See Figure 1-1.

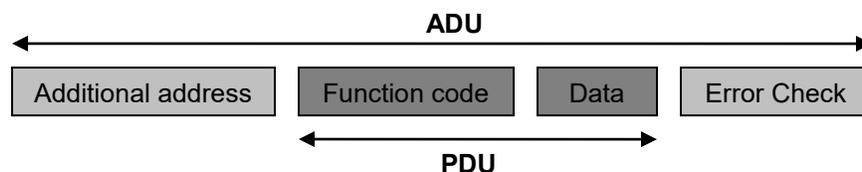


Figure 1-1. General Modbus Frame

The client that initiates a Modbus transaction builds the Modbus Application Data Unit. The function code indicates to the server which kind of action to perform.

Modbus® Over Serial Line

Message Structure

Master initiated queries and BE1-11 responses share the same message structure. Each message is comprised of four message fields. They are:

- Device Address (1 byte)
- Function Code (1 byte)
- Data Block (n bytes)
- Error Check field (2 bytes)

Each 8-bit byte in a message contains two 4-bit hexadecimal characters. The message is transmitted in a continuous stream with the LSB of each byte of data transmitted first. Transmission of each 8-bit data byte occurs with one start bit and either one or two stop bits. Parity checking is performed, when enabled, and can be either odd or even. The transmission baud rate is user-selectable, and can be set at installation and altered during real-time operation. The BE1-11 Modbus supports baud rates up to 115200. The factory default baud rate is 19200.

The BE1-11 supports RS-485 compatible serial interfaces. This interface is accessible from the rear panel of the BE1-11. The RS-485 interface is configured for Modbus communication when Option “M” is ordered.

Message Framing and Timing Considerations

When receiving a message via the RS-485 communication port, the BE1-11 requires an inter-byte latency of 3.5 character times before considering the message complete.

Once a valid query is received, the BE1-11 waits a specified amount of time before responding. This time delay is set on the Miscellaneous Modbus Settings screen under Communications in BESTCOMSPi.us®. This parameter contains a value from 10 - 10,000 milliseconds. The default value is 10 milliseconds.

Table 1-1 provides the response message transmission time (in seconds) and 3.5 character times (in milliseconds) for various message lengths and baud rates.

Table 1-1. Timing Considerations

Baud Rate	3.5 Character Time (ms)	Message Tx Time (s)	
		128 Bytes	256 Bytes
2400	16.04	0.59	1.17
4800	8.021	0.29	0.59
9600	4.0104	0.15	0.29
19200	2.0052	0.07	0.15

Modbus® on TCP/IP

Application Data Unit

The following describes the encapsulation of a Modbus request or response when it is carried on a Modbus TCP/IP network. See Figure 1-2.

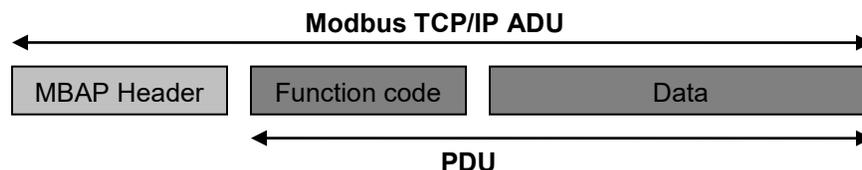


Figure 1-2. Modbus Request/Response Over TCP/IP

A dedicated header is used on TCP/IP to identify the Modbus Application Data Unit. It is called the MBAP header (Modbus Application Protocol header).

This header provides some differences compared to the Modbus RTU application data unit used on a serial line:

- The Modbus 'slave address' field usually used on Modbus Serial Line is replaced by a single byte 'Unit Identifier' within the MBAP header. The 'Unit Identifier' is used to communicate via devices such as bridges, routers, and gateways that use a single IP address to support multiple independent Modbus end units.
- All Modbus requests and responses are designed in such a way that the recipient can verify that a message is finished. For function codes where the Modbus PDU has a fixed length, the function code alone is sufficient. For function codes carrying a variable amount of data in the request or response, the data field includes a byte count.
- When Modbus is carried over TCP, additional length information is carried in the MBAP header to allow the recipient to recognize message boundaries even if the message has been split into multiple packets for transmission. The existence of explicit and implicit length rules and use of a CRC-32 error check code (on Ethernet) results in an infinitesimal chance of undetected corruption to a request or response message.

MBAP Header Description

The MBAP Header contains the fields listed in Table 1-2.

Table 1-2. MBAP Header Fields

Fields	Length	Description	Client	Server
Transaction Identifier	2 Bytes	Identification of a Modbus request/response transaction.	Initialized by the client.	Recopied by the server from the received request.
Protocol Identifier	2 Bytes	0 = Modbus protocol.	Initialized by the client.	Recopied by the server from the received request.
Length	2 Bytes	Number of following bytes.	Initialized by the client (request).	Initialized by the server (response).
Unit Identifier	1 Byte	Identification of a remote slave connected on a serial line or on other buses.	Initialized by the client.	Recopied by the server from the received request.

The header is 7 bytes long:

- Transaction Identifier – Used for transaction pairing, the Modbus server copies in the response the transaction identifier of the request.
- Protocol Identifier – Used for intra-system multiplexing. The Modbus protocol is identified by the value 0.
- Length – A byte count of the following fields, including the Unit Identifier and data fields.
- Unit Identifier – Used for intra-system routing purpose. It is typically used to communicate to a Modbus or a Modbus serial line slave through a gateway between an Ethernet TCP/IP network and a Modbus serial line. This field is set by the Modbus Client in the request and must be returned with the same value in the response by the server.

Note: All Modbus/TCP ADU are sent via TCP on registered port 502.

Error Handling and Exception Responses

Any query received that contains a non-existent device address, a framing error, or CRC error is ignored. No response is transmitted. Queries addressed to the BE1-11 with an unsupported function or illegal

values in the data block result in an error response message with an exception response code. The exception response codes supported by the BE1-11 are provided in Table 1-3.

Table 1-3. Supported Exception Response Codes

Code	Name	Description
01	Illegal Function	The query Function/Subfunction Code is unsupported; query read of more than 125 registers; query preset of more than 100 registers.
02	Illegal Data Address	A register referenced in the data block does not support queried read/write; query preset of a subset of a numerical register group.
03	Illegal Data Value	A preset register data block contains an incorrect number of bytes or one or more data values out of range.

BE1-11 Modbus® via Ethernet

Modbus can communicate through Ethernet if the IP address of the BE1-11 is configured as described in the *Communication* chapter of the appropriate BE1-11 instruction manual.

Communications Hardware Requirements

Modbus® Over RS-485 Communication Requirements

The BE1-11 optional RS-485 physical interface is three positions of a terminal strip with locations for Send/Receive A (A), Send/Receive B (B) and Signal Ground (C). Refer to the instruction manuals for the BE1-11 protection systems for further details.

Modbus® Over Ethernet TCP/IP Communication Requirements

The BE1-11 Ethernet port (RJ-45 or fiber optic) is used with the Ethernet option. The protection system supports 10/100BASE-T using Cat 5 / Cat 5e shielded twisted pair on the RJ-45 port or 100MB/s on the fiber optic port. Refer to the appropriate instruction manual for the BE1-11 for further details.

Detailed Message Query and Response for RTU Transmission Mode

A detailed description of BE1-11 supported message queries and responses is provided in the following paragraphs.

Read Holding Registers

Query

This query message requests a register or block of registers to be read. The data block contains the starting register address and the quantity of registers to be read. A register address of N will read holding register N+1. If the query is a broadcast (device address = 0), no response message is returned.

Device Address

Function Code = 03 (hex)

Starting Address Hi

Starting Address Lo

No. of Registers Hi

No. of Registers Lo

CRC Hi error check

CRC Lo error check

The number of registers cannot exceed 125 without causing an error response with the exception code for an illegal function.

Response

The response message contains the data queried. The data block contains the block length in bytes followed by the data (one Data Hi byte and one Data Lo byte) for each requested register.

Reading an unassigned holding register returns a value of zero.

Device Address
 Function Code = 03 (hex)
 Byte Count
 Data Hi (For each requested register, there is one Data Hi and one Data Lo.)
 Data Lo
 .
 .
 Data Hi
 Data Lo
 CRC Hi error check
 CRC Lo error check

Return Query Data

This query contains data to be returned (looped back) in the response. The response and query messages should be identical. If the query is a broadcast (device address = 0), no response message is returned.

Device Address
 Function Code = 08 (hex)
 Subfunction Hi = 00 (hex)
 Subfunction Lo = 00 (hex)
 Data Hi = xx (don't care)
 Data Lo = xx (don't care)
 CRC Hi error check
 CRC Lo error check

Restart Communications Option

This query causes the remote communications function of the BE1-11 to restart, terminating an active listen only mode of operation. No effect is made upon primary BE1-11 operations. Only the remote communications function is affected. If the query is a broadcast (device address = 0), no response message is returned.

If the BE1-11 receives this query while in the listen only mode, no response message is generated. Otherwise, a response message identical to the query message is transmitted prior to the communications restart.

Device Address
 Function Code = 08 (hex)
 Subfunction Hi = 00 (hex)
 Subfunction Lo = 01 (hex)
 Data Hi = xx (don't care)
 Data Lo = xx (don't care)
 CRC Hi error check
 CRC Lo error check

Listen Only Mode

This query forces the addressed BE1-11 to the listen only mode for Modbus communications, isolating it from other devices on the network. No responses are returned.

While in the listen only mode, the BE1-11 continues to monitor all queries. The BE1-11 does not respond to any other query until the listen only mode is removed. All write requests with a query to Preset Multiple Registers (Function Code = 16) are also ignored. When the BE1-11 receives the restart communications query, the listen only mode is removed.

Device Address
 Function Code = 08 (hex)
 Subfunction Hi = 00 (hex)
 Subfunction Lo = 04 (hex)
 Data Hi = xx (don't care)
 Data Lo = xx (don't care)
 CRC Hi error check
 CRC Lo error check

Preset Multiple Registers

A preset multiple registers query could address multiple registers in one slave or multiple slaves. If the query is a broadcast (device address = 0), no response message is returned.

Query

A Preset Multiple Register query message requests a register or block of registers to be written. The data block contains the starting address and the quantity of registers to be written, followed by the Data Block byte count and data. The BE1-11 will perform the write when the device address in query is a broadcast address or the same as the BE1-11 Modbus Unit ID (device address).

A register address of N will write Holding Register N+1.

Data will cease to be written if any of the following exceptions occur.

- Queries to write to Read Only registers result in an error response with Exception Code of "Illegal Data Address".
- Queries attempting to write more than 100 registers cause an error response with Exception Code "Illegal Function".
- An incorrect Byte Count will result in an error response with Exception Code of "Illegal Data Value".
- There are several instances of registers that are grouped together to collectively represent a single numerical BE1-11 data value (i.e. - floating point data, 32-bit integer data, and strings). A query to write a subset of such a register group will result in an error response with Exception Code "Illegal Data Address".
- A query to write a not allowed value (out of range) to a register results in an error response with Exception Code of "Illegal Data Value".

Device Address
 Function Code = 10 (hex)
 Starting Address Hi
 Starting Address Lo
 No. of Registers Hi
 No. of Registers Lo
 Byte Count
 Data Hi
 Data Lo
 .
 .
 Data Hi
 Data Lo
 CRC Hi error check
 CRC Lo error check

Response

The response message echoes the starting address and the number of registers. There is no response message when the query is a broadcast (device address = 0).

Device Address
 Function Code = 10 (hex)
 Starting Address Hi
 Starting Address Lo
 No. of Registers Hi
 No. of Registers Lo
 CRC Hi Error Check
 CRC Lo Error Check

Preset Single Register

A Preset Single Register query message requests a single register to be written. If the query is a broadcast (device address = 0), no response message is returned.

Note: Only data types INT16, INT8, UINT16, UINT8, and String (not longer than 2 bytes), can be preset by this function.

Query

Data will cease to be written if any of the following exceptions occur.

- Queries to write to Read Only registers result in an error response with Exception Code of “Illegal Data Address”.
- A query to write a disallowed value (out of range) to a register results in an error response with Exception Code of “Illegal Data Value”.

Device Address
 Function Code = 06 (hex)
 Address Hi
 Address Lo
 Data Hi
 Data Lo
 CRC Hi error check
 CRC Lo error check

Response

The response message echoes the Query message after the register has been altered.

Data Formats

BE1-11 Protection Systems support the following data types:

- Data types mapped to 2 registers
 - Unsigned Integer 32 (Uint32)
 - Signed Integer 32 (Int32)
 - Floating Point (Float)
 - IP Address (IP Address)
 - Strings maximum 4 characters long (String)
- Data types mapped to 1 register
 - Unsigned Integer 16 (Uint16) (If this type is mapped to section Binary Points, then 1 register contains up to 16 bit-mapped variables as listed in the *Register Table* chapter under Binary Points.)
 - Signed Integer 16 (Int16)
 - Unsigned Integer 8 (Uint8)
 - Signed Integer 8 (Int8)
 - Strings maximum 2 characters long (String)
- Data types mapped to more than 2 registers
 - Strings longer than 4 characters (String)

Floating Point Data Format (Float)

The Modbus floating point data format uses two consecutive holding registers to represent a data value. The first register contains the low-order 16 bits of the following 32-bit format:

- MSB is the sign bit for the floating-point value (0 = positive).
- The next 8 bits are the exponent biased by 127 decimal.
- The 23 LSBs comprise the normalized mantissa. The most-significant bit of the mantissa is always assumed to be 1 and is not explicitly stored, yielding an effective precision of 24 bits.

The value of the floating-point number is obtained by multiplying the binary mantissa times two raised to the power of the unbiased exponent. The assumed bit of the binary mantissa has the value of 1.0, with the remaining 23 bits providing a fractional value. Table 1-4 shows the floating-point format.

Table 1-4. Floating Point Format

Sign	Exponent + 127	Mantissa
1 Bit	8 Bits	23 Bits

The floating-point format allows for values ranging from approximately 8.43×10^{-37} to 3.38×10^{38} . A floating-point value of all zeroes is the value zero. A floating-point value of all ones (not a number) signifies a value currently not applicable or disabled.

Example: The value 95,800 represented in floating-point format is hexadecimal 47BB1C00. This number will read from two consecutive holding registers as follows:

<u>Holding Register</u>	<u>Value</u>
K (Hi Byte)	hex 1C
K (Lo Byte)	hex 00
K+1(Hi Byte)	hex 47
K+1(Lo Byte)	hex BB

The same byte alignments are required to write.

Long Integer Data Format (Uint32, Int32, and IP Address)

The Modbus long integer data format uses two consecutive holding registers to represent a 32-bit data value. The first register contains the low-order 16 bits and the second register contains the high-order 16 bits.

Example: The value 95,800 represented in long integer format is hexadecimal 0x00017638. This number will read from two consecutive holding registers as follows:

<u>Holding Register</u>	<u>Value</u>
K (Hi Byte)	hex 76
K (Lo Byte)	hex 38
K+1(Hi Byte)	hex 00
K+1(Lo Byte)	hex 01

The same byte alignments are required to write.

Integer Data Format (Uint16 and Int16) or Bit-Mapped Variables in Uint16 Format

The Modbus integer data format uses a single holding register to represent a 16-bit data value.

Example: The value 4660 represented in integer format is hexadecimal 0x1234. This number will read from a holding register as follows:

<u>Holding Register</u>	<u>Value</u>
K (Hi Byte)	hex 12
K (Lo Byte)	hex 34

The same byte alignments are required to write.

If Uint16 Data Format is listed in the Binary Points Section only, then each register contains up to 16 bit-mapped variables as listed in the *Register Table* chapter under Binary Points.

Example: Register 900 occupies 16 rows in the Register Table where each row gives the name of specific bit-mapped data such as 900-0 indicates bit 0 of register 900 is mapped to 60FL/60FL ALARM, bit 1 is mapped to 27P/BLOCK, and so forth until 900-15 is mapped to 27P-2/BLOCK.

Short Integer Data Format/Byte Character Data Format (Uint8 and Int8)

The Modbus short integer data format uses a single holding register to represent an 8-bit data value. The holding register high byte will always be zero.

Example: The value 132 represented in short integer format is hexadecimal 0x84. This number will read from a holding register as follows:

Holding Register	Value
K (Hi Byte)	hex 00
K (Lo Byte)	hex 84

The same byte alignments are required to write.

String Data Format (String)

The Modbus string data format uses one or more holding registers to represent a sequence, or string, of character values. If the string contains a single character, the holding register high byte will contain the ASCII character code and the low byte will be zero.

Example: The string "PASSWORD" represented in string format will read as follows:

Holding Register	Value
K (Hi Byte)	'P'
K (Lo Byte)	'A'
K+1(Hi Byte)	'S'
K+1(Lo Byte)	'S'
K+2(Hi Byte)	'W'
K+2(Lo Byte)	'O'
K+3(Hi Byte)	'R'
K+3(Lo Byte)	'D'

Example: If the above string is changed to "P", the new string will read as follows:

Holding Register	Value
K (Hi Byte)	'P'
K (Lo Byte)	hex 00
K+1(Hi Byte)	hex 00
K+1(Lo Byte)	hex 00
K+2(Hi Byte)	hex 00
K+2(Lo Byte)	hex 00
K+3(Hi Byte)	hex 00
K+3(Lo Byte)	hex 00

The same byte alignments are required to write.

CRC Error Check

This field contains a 2-byte CRC value for transmission error detection. The master first calculates the CRC and appends it to the query message. The BE1-11 Protection System recalculates the CRC value for the received query and performs a comparison to the query CRC value to determine if a transmission error has occurred. If so, no response message is generated. If no transmission error has occurred, the slave calculates a new CRC value for the response message and appends it to the message for transmission.

The CRC calculation is performed using all bytes of the device address, function code, and data block fields. A 16-bit CRC register is initialized to all 1's. Then each 8-bit byte of the message is used in the following algorithm:

First, exclusive-OR the message byte with the low-order byte of the CRC register. The result, stored in the CRC register, will then be right-shifted eight times. The CRC register MSB is zero-filled with each shift. After each shift, the CRC register LSB is examined. If the LSB is a 1, the CRC register is exclusive-ORed with the fixed polynomial value A001 (hex) prior to the next shift. Once all bytes of the message have undergone the above algorithm, the CRC register will contain the message CRC value to be placed in the error check field.

Reading Fault Record Data

Fault record data is obtained by reading registers from the session fault record. The fault record to be reported is selected by writing the fault number to register "Fault Report Selection". See Fault Record Setup in the *Register Table* chapter. The selected fault may be from 1 to 255. Entering a value of -1, will select the most recent fault record.

Contiguous Poll Block Registers

The user may allocate up to 125 holding registers to the Contiguous Poll Block (9875-9999). This allocation allows dispersed registers that are frequently read to be polled via a single read query. Mapping can be done via BESTCOMSP^{Plus} or via Modbus registers in the Poll Block Settings session. A register is assigned to a position in the Poll Block by writing its address value into the corresponding position in the Contiguous Poll Block Assignments registers (9676-9800) in the Poll Block Settings session. Unassigned position(s) must be mapped to Modbus Dummy Data register (9874), which always returns a value of 0.

Once assignments are made, the values of the assigned registers may be read by polling the Contiguous Poll Block. For example, if you wanted to continuously monitor VA Primary (10100), IA Primary (10124), IA Angle (10126), and Total PF (10192) Holding Registers, you would first configure the Contiguous Poll Block Registers by writing the desired register address values 10100, 10101, 10124, 10125, 10126, 10127, 10192, and 10193 into the Contiguous Poll Block Assignment registers 9676 thru 9683, respectively. You may now begin monitoring the specified registers by reading the first 8 locations in the Contiguous Poll Block; ie, reading registers 9875/9876 for VA Primary (as specified in its corresponding assignment registers 9676/9677), reading registers 9877/9878 for IA Primary (as specified in its corresponding assignment registers 9678/9679), reading registers 9879/9880 for IA Angle (as specified in its corresponding assignment registers 9680/9681), and reading registers 9881/9882 for Total PF (as specified in its corresponding assignment registers 9682/9683).

Register Table

Parameters are mapped into the holding register address space in blocks according to access type. Registers 9875 through 9999 are reserved for user configurable polling block, and register 9874 is reserved for system use only. By default, all registers in the polling block are mapped to register 9874 when their value is zero. Use BESTCOMSP^{Plus}® to map desired data to poll block registers to be read by one Modbus® read request.

Any holding register not listed in the register table is an unassigned holding register. Reading or writing to unassigned holding registers is not allowed and error code "Illegal Data Address" will be reported.

Reading/Writing any data (variable) as its complete (atomic) value is the only legal operation. Partially requested data will return an error. For example, request for reading/writing of/to only register 620 will return an error because this register is part of data Settings Group-Direct Operate that is mapped to 2 registers. Reading of register 620 is correct only if reading/writing of both 620 and 621 are requested in the same query.

Conventions

The Type column uses the following abbreviations:

- String - ASCII string
- Float - Floating point
- Int32 - Integer (32-bit integer)
- Int16 - Integer (16-bit integer)
- Int8 - Integer (8-bit integer)
- UInt32 - Unsigned Integer (32-bit integer)
- UInt16 - Unsigned Integer (16-bit integer)
- UInt8 - Unsigned Integer (8-bit integer)
- IP Address - IP Address

The Style column uses the following abbreviations:

- F - Feeder Protection System
- G - Generator Protection System
- I - Intertie Protection System
- M - Motor Protection System
- T - Transformer Protection System
- D - DC Power Protection System

2 • General Register Table

Name	Description	Register	Type	Bytes	Writable	Range
System Data	Default Graphics Module Version	1	String	13	R	0–13
System Data	Alternate Graphics Module Version	8	String	13	R	0–13
System Data	Model Number	15	String	64	R	0–64
Reserved		47–110				
System Data	Boot Version Information	111	String	64	R	0–64
Reserved		143–206				
System Data	Firmware Part Number	207	String	64	R	0–64
Unit Info	Style Number	239	String	32	R	0–32
Unit Info	Model Number	255	String	64	R	0–64
Unit Info	App Part Number	287	String	64	R	0–64
Unit Info	Serial Number	319	String	32	R	0–32
Reserved		399–417				
Security Center	Username	500	String	64	R W	0–64
Security Center	Password	532	String	64	R W	0–64
Security Center	Attempt Login	564	Uint32	4	R W	Write any value
Security Center	Log out	566	String	5	R W	Write any value
Security Center	Current Access (Refer to the <i>Security</i> chapter in the BE1-11 manual for detailed information on security levels.)	569	Uint32	4	R	None=0 Read=1 Control=2 Operator=3 Settings=4 Design=5 Admin=6
Security Center	Save Changes	571	Uint32	4	R W	Write any value
Security Center	Restore Changes	573	Uint32	4	R W	Write any value
Security Center	Activate Changes	575	Uint32	4	R W	Write any value
Security Center	Settings Activated	577	Uint32	4	R	No=0 Yes=1



3 • SBO Select Register Table

Name	Description	Register	Type	Bytes	Writable	Range
43-1	Select	600	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-2	Select	602	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-3	Select	604	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-4	Select	606	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-5	Select	608	Uint32	4	R W	Set=1 Reset=2 Pulse=3
101	Select	610	Uint32	4	R W	Trip=1 Close=2
Local Contacts	Contact Output 1 Override State Select	612	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 2 Override State Select	613	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 3 Override State Select	614	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 4 Override State Select	615	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 5 Override State Select	616	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output A Override State Select	617	Uint8	1	R W	Off=0 On=1
Settings Group	Select	618	Int32	4	R W	Settings Group 0=0 Settings Group 1=1 Settings Group 2=2 Settings Group 3=3
Settings Group	Direct Operate	620	Int32	4	R W	Settings Group 0=0 Settings Group 1=1 Settings Group 2=2 Settings Group 3=3
Local Contacts	Contact Output 6 Override State Select	622	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 7 Override State Select	623	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 8 Override State Select	624	Uint8	1	R W	Off=0 On=1
Security Center	Save Changes	571	Uint32	4	R W	Write any value
Security Center	Restore Changes	573	Uint32	4	R W	Write any value
Security Center	Activate Changes	575	Uint32	4	R W	Write any value
Security Center	Settings Activated	577	Uint32	4	R	No=0 Yes=1



4 • SBO Operate Register Table

Name	Description	Register	Type	Bytes	Writable	Range
43-1	Operate	700	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-2	Operate	702	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-3	Operate	704	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-4	Operate	706	Uint32	4	R W	Set=1 Reset=2 Pulse=3
43-5	Operate	708	Uint32	4	R W	Set=1 Reset=2 Pulse=3
101	Operate	710	Uint32	4	R W	Trip=1 Close=2
Local Contacts	Contact Output 1 Override State Operate	712	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 2 Override State Operate	713	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 3 Override State Operate	714	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 4 Override State Operate	715	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 5 Override State Operate	716	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output A Override State Operate	717	Uint8	1	R W	Off=0 On=1
Settings Group	Operate	718	Int32	4	R W	Settings Group 0=0 Settings Group 1=1 Settings Group 2=2 Settings Group 3=3
Local Contacts	Contact Output 6 Override State Operate	720	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 7 Override State Operate	721	Uint8	1	R W	Off=0 On=1
Local Contacts	Contact Output 8 Override State Operate	722	Uint8	1	R W	Off=0 On=1



5 • Direct Operate Register Table

Name	Description	Register	Type	Bytes	Writable	Range	Style
101	Untag	800	Uint32	4	R W	Disable Tag=0 Info Tag=1 Blocking Tag=2	DFGIMT
101	Tag	802	Uint32	4	R W	Disable Tag=0 Info Tag=1 Blocking Tag=2	DFGIMT
Demand Meter Circuit 1	Reset Demands	804	Uint32	4	R W	Operate=1	FGIMT
Current Demand Circuit 2	Reset Demands	806	Uint32	4	R W	Operate=1	FGIMT
G Current Demand Circuit 2	Reset Demands	808	Uint32	4	R W	Operate=1	FGIMT
DC Demand Meter 1	Reset Demands	810	Uint32	4	R W	Operate=1	D



6 • Binary Points Register Table

Name	Description	Register	Type	Bytes	Writable	Range	Style
60FL	60FL Alarm	900 bit 0	Uint16	2	R	True=1 False=0	FGIMT
24	Block	900 bit 1	Uint16	2	R	True=1 False=0	FGIT
24	Pickup	900 bit 2	Uint16	2	R	True=1 False=0	FGIT
24	Trip	900 bit 3	Uint16	2	R	True=1 False=0	FGIT
24	Target	900 bit 4	Uint16	2	R	True=1 False=0	FGIT
24	Alarm	900 bit 5	Uint16	2	R	True=1 False=0	FGIT
25	Block	900 bit 6	Uint16	2	R	True=1 False=0	FGIT
25	Status	900 bit 7	Uint16	2	R	True=1 False=0	FGIT
25	VM1 Status	900 bit 8	Uint16	2	R	True=1 False=0	FGIT
27P-1	Block	900 bit 9	Uint16	2	R	True=1 False=0	FGIMT
27P-1	Pickup	900 bit 10	Uint16	2	R	True=1 False=0	FGIMT
27P-1	Trip	900 bit 11	Uint16	2	R	True=1 False=0	FGIMT
27P-1	Target A	900 bit 12	Uint16	2	R	True=1 False=0	FGIMT
27P-1	Target B	900 bit 13	Uint16	2	R	True=1 False=0	FGIMT
27P-1	Target C	900 bit 14	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Block	900 bit 15	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Pickup	901 bit 0	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Trip	901 bit 1	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Target A	901 bit 2	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Target B	901 bit 3	Uint16	2	R	True=1 False=0	FGIMT
27P-2	Target C	901 bit 4	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Block	901 bit 5	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Pickup	901 bit 6	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Trip	901 bit 7	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Target A	901 bit 8	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Target B	901 bit 9	Uint16	2	R	True=1 False=0	FGIMT
27P-3	Target C	901 bit 10	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Block	901 bit 11	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Pickup	901 bit 12	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Trip	901 bit 13	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Target A	901 bit 14	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Target B	901 bit 15	Uint16	2	R	True=1 False=0	FGIMT
27P-4	Target C	902 bit 0	Uint16	2	R	True=1 False=0	FGIMT
27P-5	Block	902 bit 1	Uint16	2	R	True=1 False=0	FGIT
27P-5	Pickup	902 bit 2	Uint16	2	R	True=1 False=0	FGIT
27P-5	Trip	902 bit 3	Uint16	2	R	True=1 False=0	FGIT
27P-5	Target A	902 bit 4	Uint16	2	R	True=1 False=0	FGIT
27P-5	Target B	902 bit 5	Uint16	2	R	True=1 False=0	FGIT
27P-5	Target C	902 bit 6	Uint16	2	R	True=1 False=0	FGIT
27X-1	Block	902 bit 7	Uint16	2	R	True=1 False=0	FGIT
27-1	Block	902 bit 7	Uint16	2	R	True=1 False=0	D
27X-1	Pickup	902 bit 8	Uint16	2	R	True=1 False=0	FGIT
27-1	Pickup	902 bit 8	Uint16	2	R	True=1 False=0	D
27X-1	Trip	902 bit 9	Uint16	2	R	True=1 False=0	FGIT
27-1	Trip	902 bit 9	Uint16	2	R	True=1 False=0	D
27X-1	Target 3V0	902 bit 10	Uint16	2	R	True=1 False=0	FGIT
27X-1	Target V2	902 bit 11	Uint16	2	R	True=1 False=0	FGIT
27X-1	Target AUX	902 bit 12	Uint16	2	R	True=1 False=0	FGIT
27X-1	Target 3RD	902 bit 13	Uint16	2	R	True=1 False=0	FGIT
27X-1	Target V1	902 bit 14	Uint16	2	R	True=1 False=0	FGIT
27X-2	Block	902 bit 15	Uint16	2	R	True=1 False=0	FGIT
27-2	Block	902 bit 15	Uint16	2	R	True=1 False=0	D
27X-2	Pickup	903 bit 0	Uint16	2	R	True=1 False=0	FGIT
27-2	Pickup	903 bit 0	Unit16	2	R	True=1 False=0	D
27X-2	Trip	903 bit 1	Uint16	2	R	True=1 False=0	FGIT
27-2	Trip	903 bit 1	Uint16	2	R	True=1 False=0	D
27X-2	Target 3V0	903 bit 2	Uint16	2	R	True=1 False=0	FGIT
27X-2	Target V2	903 bit 3	Uint16	2	R	True=1 False=0	FGIT
27X-2	Target AUX	903 bit 4	Uint16	2	R	True=1 False=0	FGIT
27X-2	Target 3RD	903 bit 5	Uint16	2	R	True=1 False=0	FGIT
27X-2	Target V1	903 bit 6	Uint16	2	R	True=1 False=0	FGIT
27X-3	Block	903 bit 7	Uint16	2	R	True=1 False=0	FGIT
27-3	Block	903 bit 7	Uint16	2	R	True=1 False=0	D
27X-3	Pickup	903 bit 8	Uint16	2	R	True=1 False=0	FGIT
27-3	Pickup	903 bit 8	Uint16	2	R	True=1 False=0	D
27X-3	Trip	903 bit 9	Uint16	2	R	True=1 False=0	FGIT
27-3	Trip	903 bit 9	Uint16	2	R	True=1 False=0	D
27X-3	Target 3V0	903 bit 10	Uint16	2	R	True=1 False=0	FGIT
27X-3	Target V2	903 bit 11	Uint16	2	R	True=1 False=0	FGIT
27X-3	Target AUX	903 bit 12	Uint16	2	R	True=1 False=0	FGIT
27X-3	Target 3RD	903 bit 13	Uint16	2	R	True=1 False=0	FGIT
27X-3	Target V1	903 bit 14	Uint16	2	R	True=1 False=0	FGIT
27X-4	Block	903 bit 15	Uint16	2	R	True=1 False=0	FGIT

Name	Description	Register	Type	Bytes	Writable	Range	Style
27-4	Block	903 bit 15	Uint16	2	R	True=1 False=0	D
27X-4	Pickup	904 bit 0	Uint16	2	R	True=1 False=0	FGIT
27-4	Pickup	904 bit 0	Uint16	2	R	True=1 False=0	D
27X-4	Trip	904 bit 1	Uint16	2	R	True=1 False=0	FGIT
27-4	Trip	904 bit 1	Uint16	2	R	True=1 False=0	D
27X-4	Target 3V0	904 bit 2	Uint16	2	R	True=1 False=0	FGIT
27X-4	Target V2	904 bit 3	Uint16	2	R	True=1 False=0	FGIT
27X-4	Target AUX	904 bit 4	Uint16	2	R	True=1 False=0	FGIT
27X-4	Target 3RD	904 bit 5	Uint16	2	R	True=1 False=0	FGIT
27X-4	Target V1	904 bit 6	Uint16	2	R	True=1 False=0	FGIT
59P-1	Block	904 bit 7	Uint16	2	R	True=1 False=0	FGIMT
59P-1	Pickup	904 bit 8	Uint16	2	R	True=1 False=0	FGIMT
59P-1	Trip	904 bit 9	Uint16	2	R	True=1 False=0	FGIMT
59P-1	Target A	904 bit 10	Uint16	2	R	True=1 False=0	FGIMT
59P-1	Target B	904 bit 11	Uint16	2	R	True=1 False=0	FGIMT
59P-1	Target C	904 bit 12	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Block	904 bit 13	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Pickup	904 bit 14	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Trip	904 bit 15	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Target A	905 bit 0	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Target B	905 bit 1	Uint16	2	R	True=1 False=0	FGIMT
59P-2	Target C	905 bit 2	Uint16	2	R	True=1 False=0	FGIMT
59P-3	Block	905 bit 3	Uint16	2	R	True=1 False=0	FGIT
59P-3	Pickup	905 bit 4	Uint16	2	R	True=1 False=0	FGIT
59P-3	Trip	905 bit 5	Uint16	2	R	True=1 False=0	FGIT
59P-3	Target A	905 bit 6	Uint16	2	R	True=1 False=0	FGIT
59P-3	Target B	905 bit 7	Uint16	2	R	True=1 False=0	FGIT
59P-3	Target C	905 bit 8	Uint16	2	R	True=1 False=0	FGIT
59P-4	Block	905 bit 9	Uint16	2	R	True=1 False=0	FGIT
59P-4	PICKUP	905 bit 10	Uint16	2	R	True=1 False=0	FGIT
59P-4	Trip	905 bit 11	Uint16	2	R	True=1 False=0	FGIT
59P-4	Target A	905 bit 12	Uint16	2	R	True=1 False=0	FGIT
59P-4	Target B	905 bit 13	Uint16	2	R	True=1 False=0	FGIT
59P-4	Target C	905 bit 14	Uint16	2	R	True=1 False=0	FGIT
59X-1	Block	905 bit 15	Uint16	2	R	True=1 False=0	FGIMT
59-1	Block	905 bit 15	Uint16	2	R	True=1 False=0	D
59X-1	Pickup	906 bit 0	Uint16	2	R	True=1 False=0	FGIMT
59-1	Pickup	906 bit 0	Uint16	2	R	True=1 False=0	D
59X-1	Trip	906 bit 1	Uint16	2	R	True=1 False=0	FGIMT
59-1	Trip	906 bit 1	Uint16	2	R	True=1 False=0	D
59X-1	Target 3V0	906 bit 2	Uint16	2	R	True=1 False=0	FGIMT
59X-1	Target V2	906 bit 3	Uint16	2	R	True=1 False=0	FGIMT
59X-1	Target AUX	906 bit 4	Uint16	2	R	True=1 False=0	FGIMT
59X-1	Target 3RD	906 bit 5	Uint16	2	R	True=1 False=0	FGIMT
59X-1	Target V1	906 bit 6	Uint16	2	R	True=1 False=0	FGIMT
59X-2	Block	906 bit 7	Uint16	2	R	True=1 False=0	FGIMT
59-2	Block	906 bit 7	Uint16	2	R	True=1 False=0	D
59X-2	Pickup	906 bit 8	Uint16	2	R	True=1 False=0	FGIMT
59-2	Pickup	906 bit 8	Uint16	2	R	True=1 False=0	D
59X-2	Trip	906 bit 9	Uint16	2	R	True=1 False=0	FGIMT
59-2	Trip	906 bit 9	Uint16	2	R	True=1 False=0	D
59X-2	Target 3V0	906 bit 10	Uint16	2	R	True=1 False=0	FGIMT
59X-2	Target V2	906 bit 11	Uint16	2	R	True=1 False=0	FGIMT
59X-2	Target AUX	906 bit 12	Uint16	2	R	True=1 False=0	FGIMT
59X-2	Target 3RD	906 bit 13	Uint16	2	R	True=1 False=0	FGIMT
59X-2	Target V1	906 bit 14	Uint16	2	R	True=1 False=0	FGIMT
59X-3	Block	906 bit 15	Uint16	2	R	True=1 False=0	FGIT
59-3	Block	906 bit 15	Uint16	2	R	True=1 False=0	D
59X-3	Pickup	907 bit 0	Uint16	2	R	True=1 False=0	FGIT
59-3	Pickup	907 bit 0	Uint16	2	R	True=1 False=0	D
59X-3	Trip	907 bit 1	Uint16	2	R	True=1 False=0	FGIT
59-3	Trip	907 bit 1	Uint16	2	R	True=1 False=0	D
59X-3	Target 3V0	907 bit 2	Uint16	2	R	True=1 False=0	FGIT
59X-3	Target V2	907 bit 3	Uint16	2	R	True=1 False=0	FGIT
59X-3	Target AUX	907 bit 4	Uint16	2	R	True=1 False=0	FGIT
59X-3	Target 3RD	907 bit 5	Uint16	2	R	True=1 False=0	FGIT
59X-3	Target V1	907 bit 6	Uint16	2	R	True=1 False=0	FGIT
59X-4	Block	907 bit 7	Uint16	2	R	True=1 False=0	FGIT
59-4	Block	907 bit 7	Uint16	2	R	True=1 False=0	D
59X-4	Pickup	907 bit 8	Uint16	2	R	True=1 False=0	FGIT
59-4	Pickup	907 bit 8	Uint16	2	R	True=1 False=0	D
59X-4	Trip	907 bit 9	Uint16	2	R	True=1 False=0	FGIT
59-4	Trip	907 bit 9	Uint16	2	R	True=1 False=0	D
59X-4	Target 3V0	907 bit 10	Uint16	2	R	True=1 False=0	FGIT
59X-4	Target V2	907 bit 11	Uint16	2	R	True=1 False=0	FGIT
59X-4	Target AUX	907 bit 12	Uint16	2	R	True=1 False=0	FGIT
59X-4	Target 3RD	907 bit 13	Uint16	2	R	True=1 False=0	FGIT
59X-4	Target V1	907 bit 14	Uint16	2	R	True=1 False=0	FGIT

Name	Description	Register	Type	Bytes	Writable	Range	Style
50-1	Block	907 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-1	Pickup	908 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-1	Trip	908 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target A	908 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target B	908 bit 3	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target C	908 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target Negative Sequence	908 bit 5	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target Residual	908 bit 6	Uint16	2	R	True=1 False=0	FGIMT
50-1	Target Independent Ground	908 bit 7	Uint16	2	R	True=1 False=0	FGIMT
50-1	67 Target A	908 bit 8	Uint16	2	R	True=1 False=0	FGIT
50-1	67 Target B	908 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-1	67 Target C	908 bit 10	Uint16	2	R	True=1 False=0	FGIT
50-1	67 Target Negative Sequence	908 bit 11	Uint16	2	R	True=1 False=0	FGIT
50-1	67 Target Residual	908 bit 12	Uint16	2	R	True=1 False=0	FGIT
50-1	67 Target Independent Ground	908 bit 13	Uint16	2	R	True=1 False=0	FGIT
50-2	Block	908 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-2	Pickup	908 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-2	Trip	909 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target A	909 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target B	909 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target C	909 bit 3	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target Negative Sequence	909 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target Residual	909 bit 5	Uint16	2	R	True=1 False=0	FGIMT
50-2	Target Independent Ground	909 bit 6	Uint16	2	R	True=1 False=0	FGIMT
50-2	67 Target A	909 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-2	67 Target B	909 bit 8	Uint16	2	R	True=1 False=0	FGIT
50-2	67 Target C	909 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-2	67 Target Negative Sequence	909 bit 10	Uint16	2	R	True=1 False=0	FGIT
50-2	67 Target Residual	909 bit 11	Uint16	2	R	True=1 False=0	FGIT
50-2	67 Target Independent Ground	909 bit 12	Uint16	2	R	True=1 False=0	FGIT
50-3	Block	909 bit 13	Uint16	2	R	True=1 False=0	FGIMT
50-3	Pickup	909 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-3	Trip	909 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target A	910 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target B	910 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target C	910 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target Negative Sequence	910 bit 3	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target Residual	910 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-3	Target Independent Ground	910 bit 5	Uint16	2	R	True=1 False=0	FGIMT
50-3	67 Target A	910 bit 6	Uint16	2	R	True=1 False=0	FGIT
50-3	67 Target B	910 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-3	67 Target C	910 bit 8	Uint16	2	R	True=1 False=0	FGIT
50-3	67 Target Negative Sequence	910 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-3	67 Target Residual	910 bit 10	Uint16	2	R	True=1 False=0	FGIT
50-3	67 Target Independent Ground	910 bit 11	Uint16	2	R	True=1 False=0	FGIT
50-4	Block	910 bit 12	Uint16	2	R	True=1 False=0	FGIMT
50-4	Pickup	910 bit 13	Uint16	2	R	True=1 False=0	FGIMT
50-4	Trip	910 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target A	910 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target B	911 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target C	911 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target Negative Sequence	911 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target Residual	911 bit 3	Uint16	2	R	True=1 False=0	FGIMT
50-4	Target Independent Ground	911 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-4	67 Target A	911 bit 5	Uint16	2	R	True=1 False=0	FGIT
50-4	67 Target B	911 bit 6	Uint16	2	R	True=1 False=0	FGIT
50-4	67 Target C	911 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-4	67 Target Negative Sequence	911 bit 8	Uint16	2	R	True=1 False=0	FGIT
50-4	67 Target Residual	911 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-4	67 Target Independent Ground	911 bit 10	Uint16	2	R	True=1 False=0	FGIT
50-5	Block	911 bit 11	Uint16	2	R	True=1 False=0	FGIMT
50-5	Pickup	911 bit 12	Uint16	2	R	True=1 False=0	FGIMT
50-5	Trip	911 bit 13	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target A	911 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target B	911 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target C	912 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target Negative Sequence	912 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target Residual	912 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-5	Target Independent Ground	912 bit 3	Uint16	2	R	True=1 False=0	FGIMT
50-5	67 Target A	912 bit 4	Uint16	2	R	True=1 False=0	FGIT
50-5	67 Target B	912 bit 5	Uint16	2	R	True=1 False=0	FGIT
50-5	67 Target C	912 bit 6	Uint16	2	R	True=1 False=0	FGIT
50-5	67 Target Negative Sequence	912 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-5	67 Target Residual	912 bit 8	Uint16	2	R	True=1 False=0	FGIT
50-5	67 Target Independent Ground	912 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-6	Block	912 bit 10	Uint16	2	R	True=1 False=0	FGIMT
50-6	Pickup	912 bit 11	Uint16	2	R	True=1 False=0	FGIMT
50-6	Trip	912 bit 12	Uint16	2	R	True=1 False=0	FGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
50-6	Target A	912 bit 13	Uint16	2	R	True=1 False=0	FGIMT
50-6	Target B	912 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-6	Target C	912 bit 15	Uint16	2	R	True=1 False=0	FGIMT
50-6	Target Negative Sequence	913 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-6	Target Residual	913 bit 1	Uint16	2	R	True=1 False=0	FGIMT
50-6	Target Independent Ground	913 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-6	67 Target A	913 bit 3	Uint16	2	R	True=1 False=0	FGIT
50-6	67 Target B	913 bit 4	Uint16	2	R	True=1 False=0	FGIT
50-6	67 Target C	913 bit 5	Uint16	2	R	True=1 False=0	FGIT
50-6	67 Target Negative Sequence	913 bit 6	Uint16	2	R	True=1 False=0	FGIT
50-6	67 Target Residual	913 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-6	67 Target Independent Ground	913 bit 8	Uint16	2	R	True=1 False=0	FGIT
51-1	Block	913 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-1	Pickup	913 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-1	Trip	913 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target A	913 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target B	913 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target C	913 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target Negative Sequence	913 bit 15	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target Residual	914 bit 0	Uint16	2	R	True=1 False=0	FGIMT
51-1	Target Independent Ground	914 bit 1	Uint16	2	R	True=1 False=0	FGIMT
51-1	67 Target A	914 bit 2	Uint16	2	R	True=1 False=0	FGIT
51-1	67 Target B	914 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-1	67 Target C	914 bit 4	Uint16	2	R	True=1 False=0	FGIT
51-1	67 Target NEG SEQ	914 bit 5	Uint16	2	R	True=1 False=0	FGIT
51-1	67 Target Residual	914 bit 6	Uint16	2	R	True=1 False=0	FGIT
51-1	67 Target Independent Ground	914 bit 7	Uint16	2	R	True=1 False=0	FGIT
51-2	Block	914 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-2	Pickup	914 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-2	Trip	914 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target A	914 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target B	914 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target C	914 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target Negative Sequence	914 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target Residual	914 bit 15	Uint16	2	R	True=1 False=0	FGIMT
51-2	Target Independent Ground	915 bit 0	Uint16	2	R	True=1 False=0	FGIMT
51-2	67 Target A	915 bit 1	Uint16	2	R	True=1 False=0	FGIT
51-2	67 Target B	915 bit 2	Uint16	2	R	True=1 False=0	FGIT
51-2	67 Target C	915 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-2	67 Target NEG SEQ	915 bit 4	Uint16	2	R	True=1 False=0	FGIT
51-2	67 Target Residual	915 bit 5	Uint16	2	R	True=1 False=0	FGIT
51-2	67 Target Independent Ground	915 bit 6	Uint16	2	R	True=1 False=0	FGIT
51-3	Block	915 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-3	Pickup	915 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-3	Trip	915 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target A	915 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target B	915 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target C	915 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target Negative Sequence	915 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target Residual	915 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-3	Target Independent Ground	915 bit 15	Uint16	2	R	True=1 False=0	FGIMT
51-3	67 Target A	916 bit 0	Uint16	2	R	True=1 False=0	FGIT
51-3	67 Target B	916 bit 1	Uint16	2	R	True=1 False=0	FGIT
51-3	67 Target C	916 bit 2	Uint16	2	R	True=1 False=0	FGIT
51-3	67 Target Negative Sequence	916 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-3	67 Target Residual	916 bit 4	Uint16	2	R	True=1 False=0	FGIT
51-3	67 Target Independent Ground	916 bit 5	Uint16	2	R	True=1 False=0	FGIT
51-4	Block	916 bit 6	Uint16	2	R	True=1 False=0	FGIMT
51-4	Pickup	916 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-4	Trip	916 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target A	916 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target B	916 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target C	916 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target Negative Sequence	916 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target Residual	916 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-4	Target Independent Ground	916 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-4	67 Target A	916 bit 15	Uint16	2	R	True=1 False=0	FGIT
51-4	67 Target B	917 bit 0	Uint16	2	R	True=1 False=0	FGIT
51-4	67 Target C	917 bit 1	Uint16	2	R	True=1 False=0	FGIT
51-4	67 Target Negative Sequence	917 bit 2	Uint16	2	R	True=1 False=0	FGIT
51-4	67 Target Residual	917 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-4	67 Target Independent Ground	917 bit 4	Uint16	2	R	True=1 False=0	FGIT
51-5	Block	917 bit 5	Uint16	2	R	True=1 False=0	FGIMT
51-5	Pickup	917 bit 6	Uint16	2	R	True=1 False=0	FGIMT
51-5	Trip	917 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-5	Target A	917 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-5	Target B	917 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-5	Target C	917 bit 10	Uint16	2	R	True=1 False=0	FGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
51-5	Target Negative Sequence	917 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-5	Target Residual	917 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-5	Target Independent Ground	917 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-5	67 Target A	917 bit 14	Uint16	2	R	True=1 False=0	FGIT
51-5	67 Target B	917 bit 15	Uint16	2	R	True=1 False=0	FGIT
51-5	67 Target C	918 bit 0	Uint16	2	R	True=1 False=0	FGIT
51-5	67 Target Negative Sequence	918 bit 1	Uint16	2	R	True=1 False=0	FGIT
51-5	67 Target Residual	918 bit 2	Uint16	2	R	True=1 False=0	FGIT
51-5	67 Target Independent Ground	918 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-6	Block	918 bit 4	Uint16	2	R	True=1 False=0	FGIMT
51-6	Pickup	918 bit 5	Uint16	2	R	True=1 False=0	FGIMT
51-6	Trip	918 bit 6	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target A	918 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target B	918 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target C	918 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target Negative Sequence	918 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target Residual	918 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-6	Target Independent Ground	918 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target A	918 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target B	918 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target C	918 bit 15	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target Negative Sequence	919 bit 0	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target Residual	919 bit 1	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target Independent Ground	919 bit 2	Uint16	2	R	True=1 False=0	FGIMT
51-7	Block	919 bit 3	Uint16	2	R	True=1 False=0	FGIMT
51-7	Pickup	919 bit 4	Uint16	2	R	True=1 False=0	FGIMT
51-7	Trip	919 bit 5	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target A	919 bit 6	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target B	919 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target C	919 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target Negative Sequence	919 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target Residual	919 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-7	Target Independent Ground	919 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target A	919 bit 12	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target B	919 bit 13	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target C	919 bit 14	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target Negative Sequence	919 bit 15	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target Residual	920 bit 0	Uint16	2	R	True=1 False=0	FGIMT
51-7	67 Target Independent Ground	920 bit 1	Uint16	2	R	True=1 False=0	FGIMT
32-1	Block	920 bit 2	Uint16	2	R	True=1 False=0	DFGIM
32-1	Pickup	920 bit 3	Uint16	2	R	True=1 False=0	DFGIM
32-1	Trip	920 bit 4	Uint16	2	R	True=1 False=0	DFGIM
32-1	Target A Over	920 bit 5	Uint16	2	R	True=1 False=0	FGIM
32-1	Target B Over	920 bit 6	Uint16	2	R	True=1 False=0	FGIM
32-1	Target C Over	920 bit 7	Uint16	2	R	True=1 False=0	FGIM
32-1	Target T Over	920 bit 8	Uint16	2	R	True=1 False=0	FGIM
32-1	Target A Under	920 bit 9	Uint16	2	R	True=1 False=0	FGIM
32-1	Target B Under	920 bit 10	Uint16	2	R	True=1 False=0	FGIM
32-1	Target C Under	920 bit 11	Uint16	2	R	True=1 False=0	FGIM
32-1	Target T Under	920 bit 12	Uint16	2	R	True=1 False=0	FGIM
32-2	Block	920 bit 13	Uint16	2	R	True=1 False=0	DFGI
32-2	Pickup	920 bit 14	Uint16	2	R	True=1 False=0	DFGI
32-2	Trip	920 bit 15	Uint16	2	R	True=1 False=0	DFGI
32-2	Target A Over	921 bit 0	Uint16	2	R	True=1 False=0	FGI
32-2	Target B Over	921 bit 1	Uint16	2	R	True=1 False=0	FGI
32-2	Target C Over	921 bit 2	Uint16	2	R	True=1 False=0	FGI
32-2	Target T Over	921 bit 3	Uint16	2	R	True=1 False=0	FGI
32-2	Target A Under	921 bit 4	Uint16	2	R	True=1 False=0	FGI
32-2	Target B Under	921 bit 5	Uint16	2	R	True=1 False=0	FGI
32-2	Target C Under	921 bit 6	Uint16	2	R	True=1 False=0	FGI
32-2	Target T Under	921 bit 7	Uint16	2	R	True=1 False=0	FGI
40Z	Block	921 bit 8	Uint16	2	R	True=1 False=0	G
40Z	Pickup	921 bit 9	Uint16	2	R	True=1 False=0	G
40Z	Trip	921 bit 10	Uint16	2	R	True=1 False=0	G
40Z	VC Pickup	921 bit 11	Uint16	2	R	True=1 False=0	G
40Z	VC Trip	921 bit 12	Uint16	2	R	True=1 False=0	G
40Z	Z1 Pickup	921 bit 13	Uint16	2	R	True=1 False=0	G
40Z	Z1 Trip	921 bit 14	Uint16	2	R	True=1 False=0	G
40Z	Z1 Target	921 bit 15	Uint16	2	R	True=1 False=0	G
40Z	Z1 VC Pickup	922 bit 0	Uint16	2	R	True=1 False=0	G
40Z	Z1 VC Trip	922 bit 1	Uint16	2	R	True=1 False=0	G
40Z	Z1 VC Target	922 bit 2	Uint16	2	R	True=1 False=0	G
40Z	Z2 Pickup	922 bit 3	Uint16	2	R	True=1 False=0	G
40Z	Z2 Trip	922 bit 4	Uint16	2	R	True=1 False=0	G
40Z	Z2 Target	922 bit 5	Uint16	2	R	True=1 False=0	G
40Z	Z2 VC Pickup	922 bit 6	Uint16	2	R	True=1 False=0	G
40Z	Z2 VC Trip	922 bit 7	Uint16	2	R	True=1 False=0	G
40Z	Z2 VC Target	922 bit 8	Uint16	2	R	True=1 False=0	G

Name	Description	Register	Type	Bytes	Writable	Range	Style
40Q	Block	922 bit 9	Uint16	2	R	True=1 False=0	GM
40Q	Pickup	922 bit 10	Uint16	2	R	True=1 False=0	GM
40Q	Trip	922 bit 11	Uint16	2	R	True=1 False=0	GM
40Q	Target	922 bit 12	Uint16	2	R	True=1 False=0	GM
81-1	Block	922 bit 13	Uint16	2	R	True=1 False=0	FGIMT
81-1	Pickup	922 bit 14	Uint16	2	R	True=1 False=0	FGIMT
81-1	Trip	922 bit 15	Uint16	2	R	True=1 False=0	FGIMT
81-1	Target Over	923 bit 0	Uint16	2	R	True=1 False=0	FGIMT
81-1	Target Under	923 bit 1	Uint16	2	R	True=1 False=0	FGIMT
81-1	Target Rate of Change	923 bit 2	Uint16	2	R	True=1 False=0	FGIMT
81-2	Block	923 bit 3	Uint16	2	R	True=1 False=0	FGIMT
81-2	Pickup	923 bit 4	Uint16	2	R	True=1 False=0	FGIMT
81-2	Trip	923 bit 5	Uint16	2	R	True=1 False=0	FGIMT
81-2	Target Over	923 bit 6	Uint16	2	R	True=1 False=0	FGIMT
81-2	Target Under	923 bit 7	Uint16	2	R	True=1 False=0	FGIMT
81-2	Target Rate of Change	923 bit 8	Uint16	2	R	True=1 False=0	FGIMT
81-3	Block	923 bit 9	Uint16	2	R	True=1 False=0	FGIMT
81-3	Pickup	923 bit 10	Uint16	2	R	True=1 False=0	FGIMT
81-3	Trip	923 bit 11	Uint16	2	R	True=1 False=0	FGIMT
81-3	Target Over	923 bit 12	Uint16	2	R	True=1 False=0	FGIMT
81-3	Target Under	923 bit 13	Uint16	2	R	True=1 False=0	FGIMT
81-3	Target Rate of Change	923 bit 14	Uint16	2	R	True=1 False=0	FGIMT
81-4	Block	923 bit 15	Uint16	2	R	True=1 False=0	FGIMT
81-4	Pickup	924 bit 0	Uint16	2	R	True=1 False=0	FGIMT
81-4	Trip	924 bit 1	Uint16	2	R	True=1 False=0	FGIMT
81-4	Target Over	924 bit 2	Uint16	2	R	True=1 False=0	FGIMT
81-4	Target Under	924 bit 3	Uint16	2	R	True=1 False=0	FGIMT
81-4	Target Rate of Change	924 bit 4	Uint16	2	R	True=1 False=0	FGIMT
81-5	Block	924 bit 5	Uint16	2	R	True=1 False=0	FGIT
81-5	Pickup	924 bit 6	Uint16	2	R	True=1 False=0	FGIT
81-5	Trip	924 bit 7	Uint16	2	R	True=1 False=0	FGIT
81-5	Target Over	924 bit 8	Uint16	2	R	True=1 False=0	FGIT
81-5	Target Under	924 bit 9	Uint16	2	R	True=1 False=0	FGIT
81-5	Target Rate of Change	924 bit 10	Uint16	2	R	True=1 False=0	FGIT
81-6	Block	924 bit 11	Uint16	2	R	True=1 False=0	FGIT
81-6	Pickup	924 bit 12	Uint16	2	R	True=1 False=0	FGIT
81-6	Trip	924 bit 13	Uint16	2	R	True=1 False=0	FGIT
81-6	Target Over	924 bit 14	Uint16	2	R	True=1 False=0	FGIT
81-6	Target Under	924 bit 15	Uint16	2	R	True=1 False=0	FGIT
81-6	Target Rate of Change	925 bit 0	Uint16	2	R	True=1 False=0	FGIT
81-7	Block	925 bit 1	Uint16	2	R	True=1 False=0	FGIT
81-7	Pickup	925 bit 2	Uint16	2	R	True=1 False=0	FGIT
81-7	Trip	925 bit 3	Uint16	2	R	True=1 False=0	FGIT
81-7	Target Over	925 bit 4	Uint16	2	R	True=1 False=0	FGIT
81-7	Target Under	925 bit 5	Uint16	2	R	True=1 False=0	FGIT
81-7	Target Rate of Change	925 bit 6	Uint16	2	R	True=1 False=0	FGIT
81-8	Block	925 bit 7	Uint16	2	R	True=1 False=0	FGIT
81-8	Pickup	925 bit 8	Uint16	2	R	True=1 False=0	FGIT
81-8	Trip	925 bit 9	Uint16	2	R	True=1 False=0	FGIT
81-8	Target Over	925 bit 10	Uint16	2	R	True=1 False=0	FGIT
81-8	Target Under	925 bit 11	Uint16	2	R	True=1 False=0	FGIT
81-8	Target Rate of Change	925 bit 12	Uint16	2	R	True=1 False=0	FGIT
43-1	Pulse	925 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Reset	925 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Set	925 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Output	926 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		926 bit 1					
43-1	Tag Blocking	926 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Tag Informational	926 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Untag Blocking	926 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Untag Informational	926 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Tag Blocking Status	926 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
43-1	Tag Informational Status	926 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Pulse	926 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Reset	926 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Set	926 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Output	926 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		926 bit 12					
43-2	Tag Blocking	926 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Tag Informational	926 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Untag Blocking	926 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Untag Informational	927 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Tag Blocking Status	927 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
43-2	Tag Informational Status	927 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
43-3	Pulse	927 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
43-3	Reset	927 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
43-3	Set	927 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
43-3	Output	927 bit 6	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
Reserved		927 bit 7					
43-3	Tag Blocking	927 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
43-3	Tag Informational	927 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
43-3	Untag Blocking	927 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
43-3	Untag Informational	927 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
43-3	Tag Blocking Status	927 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
43-3	Tag Informational Status	927 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Pulse	927 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Reset	927 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Set	928 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Output	928 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
Reserved		928 bit 2					
43-4	Tag Blocking	928 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Tag Informational	928 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Untag Blocking	928 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Untag Informational	928 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Tag Blocking Status	928 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
43-4	Tag Informational Status	928 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Pulse	928 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Reset	928 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Set	928 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Output	928 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
Reserved		928 bit 13					
43-5	Tag Blocking	928 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Tag Informational	928 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Untag Blocking	929 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Untag Informational	929 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Tag Blocking Status	929 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
43-5	Tag Informational Status	929 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
86-1	Reset	929 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
86-1	Set	929 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
86-1	Out	929 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
86-2	Reset	929 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
86-2	Set	929 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
86-2	Out	929 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
101	Trip	929 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
101	Close	929 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
101	Trip Out	929 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
101	Close Out	929 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
101	TSC Out	929 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
101	CSC Out	929 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
Reserved		930 bit 0					
101	Tag Blocking	930 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
101	Tag Informational	930 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
101	Untag Blocking	930 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
101	Untag Informational	930 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
101	Tag Blocking Status	930 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
101	Tag Informational Status	930 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
62-1	Block	930 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
62-1	Initiate	930 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
62-1	Output	930 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
62-2	Block	930 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
62-2	Initiate	930 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
62-2	Output	930 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
62-3	Block	930 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
62-3	Initiate	930 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
62-3	Output	930 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
62-4	Block	931 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
62-4	Initiate	931 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
62-4	Output	931 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
62-5	Block	931 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
62-5	Initiate	931 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
62-5	Output	931 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
62-6	Block	931 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
62-6	Initiate	931 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
62-6	Output	931 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
62-7	Block	931 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
62-7	Initiate	931 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
62-7	Output	931 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
62-8	Block	931 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
62-8	Initiate	931 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
62-8	Output	931 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
79	Recloser Fail Alarm	931 bit 15	UInt16	2	R	True=1 False=0	FIT
79	Reclosing	932 bit 0	UInt16	2	R	True=1 False=0	FIT
79	Recloser Running	932 bit 1	UInt16	2	R	True=1 False=0	FIT
79	Recloser Reset	932 bit 2	UInt16	2	R	True=1 False=0	FIT
79	Recloser Lockout	932 bit 3	UInt16	2	R	True=1 False=0	FIT
79	Recloser SCB	932 bit 4	UInt16	2	R	True=1 False=0	FIT

Name	Description	Register	Type	Bytes	Writable	Range	Style
79	Shot 1	932 bit 5	UInt16	2	R	True=1 False=0	FIT
79	Shot 2	932 bit 6	UInt16	2	R	True=1 False=0	FIT
79	Shot 3	932 bit 7	UInt16	2	R	True=1 False=0	FIT
79	Shot 4	932 bit 8	UInt16	2	R	True=1 False=0	FIT
79	Reclose Initiate	932 bit 9	UInt16	2	R	True=1 False=0	FIT
79	Wait	932 bit 10	UInt16	2	R	True=1 False=0	FIT
79	Drive to Lockout	932 bit 11	UInt16	2	R	True=1 False=0	FIT
79	Zone Pick Up	932 bit 12	UInt16	2	R	True=1 False=0	FIT
79	Zone Trip	932 bit 13	UInt16	2	R	True=1 False=0	FIT
50BF	Block	932 bit 14	UInt16	2	R	True=1 False=0	FGIMT
50BF	BF152	932 bit 15	UInt16	2	R	True=1 False=0	FGIMT
50BF	BF150	933 bit 0	UInt16	2	R	True=1 False=0	FGIMT
50BF	BFR1	933 bit 1	UInt16	2	R	True=1 False=0	FGIMT
50BF	BFT	933 bit 2	UInt16	2	R	True=1 False=0	FGIMT
50BF	50BF Target	933 bit 3	UInt16	2	R	True=1 False=0	FGIMT
50BF	BFI Alarm	933 bit 4	UInt16	2	R	True=1 False=0	FGIMT
50BF	Current Detected	933 bit 5	UInt16	2	R	True=1 False=0	FGIMT
Reserved		933 bit 6					
52	52 Trip Coil Monitor	933 bit 7	UInt16	2	R	True=1 False=0	FGIMT
Breaker Monitor	Breaker Monitor Block	933 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
Demand Meter	IG Demand Alarm	933 bit 9	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	IN Demand Alarm	933 bit 10	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	IP Demand Alarm	933 bit 11	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	IQ Demand Alarm	933 bit 12	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	Var Positive Demand Alarm	933 bit 13	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	Var Negative Demand Alarm	933 bit 14	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	Watt Forward Demand Alarm	933 bit 15	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	Watt Reverse Demand Alarm	934 bit 0	UInt16	2	R	True=1 False=0	FGIMT
Demand Meter	S Demand Alarm	934 bit 1	UInt16	2	R	True=1 False=0	FGIMT
Local Contacts	Contact Input 1 State	934 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 2 State	934 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 3 State	934 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 4 State	934 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact 52 TCM	934 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 1 State	934 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 2 State	934 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 3 State	934 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 4 State	934 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 5 State	934 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output A State	934 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
System Data	RF Trigger	934 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
System Data	Pickup Logic	934 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
System Data	Trip Logic	934 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
System Data	Logic Trigger	935 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
System Data	Breaker Status	935 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Flash Failure Alarm	935 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Micro Failure Alarm	935 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Calibration Error Alarm	935 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Calibration Defaults Alarm	935 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Defaults Loaded Alarm	935 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	System Overload Alarm	935 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Power Supply Alarm	935 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Changes Lost Alarm	935 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Real-Time Clock Alarm	935 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Date/Time Set Alarm	935 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Firmware Changed Alarm	935 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Frequency Out of Range Alarm	935 bit 13	UInt16	2	R	True=1 False=0	FGIMT
Alarms	Ethernet Link Lost Alarm	935 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
Reserved		935 bit 15					
Alarms	IRIG Sync Lost Alarm	936 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Logic = None Alarm	936 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	No User Setting Alarm	936 bit 2	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	NTP Sync Lost Alarm	936 bit 3	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	DNP Polls Error	936 bit 4	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Setting Change Alarm	936 bit 5	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Output Override Alarm	936 bit 6	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Analog Alarm	936 bit 7	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Microprocessor Reset Alarm	936 bit 8	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Breaker Monitor 1	936 bit 9	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Breaker Monitor 2	936 bit 10	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Breaker Monitor 3	936 bit 11	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Fault Report Timeout	936 bit 12	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 1	936 bit 13	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 2	936 bit 14	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 3	936 bit 15	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 4	937 bit 0	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 5	937 bit 1	UInt16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 6	937 bit 2	UInt16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
Alarms	Programmable Alarm 7	937 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Alarms	Programmable Alarm 8	937 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Major Alarm	Alarm Output	937 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
Minor Alarm	Alarm Output	937 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
Targets	Targets Active	937 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
Relay Alarms	Alarm Output	937 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC Auto	937 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC D0	937 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC D1	937 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC D2	937 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC D3	937 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC Active Alarm	937 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC Logic Override	937 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC SG0	938 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC SG1	938 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC SG2	938 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
Settings Group	SGC SG3	938 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
50-1	Target Positive Sequence	938 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-1	67 Target Positive Sequence	938 bit 5	Uint16	2	R	True=1 False=0	FGIT
50-2	Target Positive Sequence	938 bit 6	Uint16	2	R	True=1 False=0	FGIMT
50-2	67 Target Positive Sequence	938 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-3	Target Positive Sequence	938 bit 8	Uint16	2	R	True=1 False=0	FGIMT
50-3	67 Target Positive Sequence	938 bit 9	Uint16	2	R	True=1 False=0	FGIT
50-4	Target Positive Sequence	938 bit 10	Uint16	2	R	True=1 False=0	FGIMT
50-4	67 Target Positive Sequence	938 bit 11	Uint16	2	R	True=1 False=0	FGIT
50-5	Target Positive Sequence	938 bit 12	Uint16	2	R	True=1 False=0	FGIMT
50-5	67 Target Positive Sequence	938 bit 13	Uint16	2	R	True=1 False=0	FGIT
50-6	Target Positive Sequence	938 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-6	67 Target Positive Sequence	938 bit 15	Uint16	2	R	True=1 False=0	FGIT
51-1	Target Positive Sequence	939 bit 0	Uint16	2	R	True=1 False=0	FGIMT
51-1	67 Target Positive Sequence	939 bit 1	Uint16	2	R	True=1 False=0	FGIT
51-2	Target Positive Sequence	939 bit 2	Uint16	2	R	True=1 False=0	FGIMT
51-2	67 Target Positive Sequence	939 bit 3	Uint16	2	R	True=1 False=0	FGIT
51-3	Target Positive Sequence	939 bit 4	Uint16	2	R	True=1 False=0	FGIMT
51-3	67 Target Positive Sequence	939 bit 5	Uint16	2	R	True=1 False=0	FGIT
51-4	Target Positive Sequence	939 bit 6	Uint16	2	R	True=1 False=0	FGIMT
51-4	67 Target Positive Sequence	939 bit 7	Uint16	2	R	True=1 False=0	FGIT
51-5	Target Positive Sequence	939 bit 8	Uint16	2	R	True=1 False=0	FGIMT
51-5	67 Target Positive Sequence	939 bit 9	Uint16	2	R	True=1 False=0	FGIT
51-6	Target Positive Sequence	939 bit 10	Uint16	2	R	True=1 False=0	FGIMT
51-6	67 Target Positive Sequence	939 bit 11	Uint16	2	R	True=1 False=0	FGIT
51-7	Target Positive Sequence	939 bit 12	Uint16	2	R	True=1 False=0	FGIT
51-7	67 Target Positive Sequence	939 bit 13	Uint16	2	R	True=1 False=0	FGIT
Alarms	Programmable Alarm 9	939 bit 14	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 10	939 bit 15	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 11	940 bit 0	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 12	940 bit 1	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 13	940 bit 2	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 14	940 bit 3	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 15	940 bit 4	Uint16	2	R	True=1 False=0	FGIMT
Alarms	Programmable Alarm 16	940 bit 5	Uint16	2	R	True=1 False=0	FGIMT
87N-1	Block	940 bit 6	Uint16	2	R	True=1 False=0	GT
87N-1	Pickup	940 bit 7	Uint16	2	R	True=1 False=0	GT
87N-1	Trip	940 bit 8	Uint16	2	R	True=1 False=0	GT
87N-1	Target	940 bit 9	Uint16	2	R	True=1 False=0	GT
51TF	Block	940 bit 10	Uint16	2	R	True=1 False=0	T
51TF	Pickup	940 bit 11	Uint16	2	R	True=1 False=0	T
51TF	Trip	940 bit 12	Uint16	2	R	True=1 False=0	T
51TF	Target	940 bit 13	Uint16	2	R	True=1 False=0	T
Reserved		940 bit 14					
Current Demand Circuit 2	3I0 Demand Alarm	940 bit 15	Uint16	2	R	True=1 False=0	FGIMT
Current Demand Circuit 2	IP Demand Alarm	941 bit 0	Uint16	2	R	True=1 False=0	FGIMT
Current Demand Circuit 2	I2 Demand Alarm	941 bit 1	Uint16	2	R	True=1 False=0	FGIMT
Ground Current Demand Circuit 2	IG Demand Alarm	941 bit 2	Uint16	2	R	True=1 False=0	FGIMT
Local Contacts	Contact Output 6 State	941 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 7 State	941 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Output 8 State	941 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		941 bit 6-8					
Local Contacts	Contact Input 5 State	941 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 6 State	941 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 7 State	941 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 8 State	941 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 9 State	941 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Local Contacts	Contact Input 10 State	941 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		941 bit 15-942 bit 5					
Analog Input Protection 1	Block	942 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 1	Pickup	942 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 1	Trip	942 bit 8	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
Analog Input Protection 1	Target	942 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 2	Block	942 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 2	Pickup	942 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 2	Trip	942 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 2	Target	942 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 3	Block	942 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 3	Pickup	942 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 3	Trip	943 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 3	Target	943 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 4	Block	943 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 4	Pickup	943 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 4	Trip	943 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 4	Target	943 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 5	Block	943 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 5	Pickup	943 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 5	Trip	943 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 5	Target	943 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 6	Block	943 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 6	Pickup	943 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 6	Trip	943 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 6	Target	943 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 7	Block	943 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 7	Pickup	943 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 7	Trip	944 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 7	Target	944 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 8	Block	944 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 8	Pickup	944 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 8	Trip	944 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Analog Input Protection 8	Target	944 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		944 bit 6-10					
49RTD-1	Block	944 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	Pickup	944 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	Trip	944 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-1	944 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-2	944 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-3	945 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-4	945 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-5	945 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-6	945 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-7	945 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-8	945 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-9	945 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-10	945 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-11	945 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD1-12	945 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-1	945 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-2	945 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-3	945 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-4	945 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-5	945 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-6	945 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-7	946 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-8	946 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-9	946 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-10	946 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-11	946 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-1	RTD2-12	946 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	Block	946 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	Pickup	946 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	Trip	946 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-1	946 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-2	946 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-3	946 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-4	946 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-5	946 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-6	946 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-7	946 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-8	947 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-9	947 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-10	947 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-11	947 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD1-12	947 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-1	947 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-2	947 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-3	947 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-4	947 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-5	947 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-6	947 bit 10	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
49RTD-2	RTD2-7	947 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-8	947 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-9	947 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-10	947 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-11	947 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-2	RTD2-12	948 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	Block	948 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	Pickup	948 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	Trip	948 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-1	948 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-2	948 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-3	948 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-4	948 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-5	948 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-6	948 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-7	948 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-8	948 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-9	948 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-10	948 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-11	948 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD1-12	948 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-1	949 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-2	949 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-3	949 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-4	949 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-5	949 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-6	949 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-7	949 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-8	949 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-9	949 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-10	949 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-11	949 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-3	RTD2-12	949 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	Block	949 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	Pickup	949 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	Trip	949 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-1	949 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-2	950 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-3	950 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-4	950 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-5	950 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-6	950 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-7	950 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-8	950 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-9	950 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-10	950 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-11	950 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD1-12	950 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-1	950 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-2	950 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-3	950 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-4	950 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-5	950 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-6	951 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-7	951 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-8	951 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-9	951 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-10	951 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-11	951 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-4	RTD2-12	951 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	Block	951 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	Pickup	951 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	Trip	951 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-1	951 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-2	951 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-3	951 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-4	951 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-5	951 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-6	951 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-7	952 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-8	952 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-9	952 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-10	952 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-11	952 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD1-12	952 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-1	952 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-2	952 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-3	952 bit 8	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
49RTD-5	RTD2-4	952 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-5	952 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-6	952 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-7	952 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-8	952 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-9	952 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-10	952 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-11	953 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-5	RTD2-12	953 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	Block	953 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	Pickup	953 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	Trip	953 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-1	953 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-2	953 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-3	953 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-4	953 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-5	953 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-6	953 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-7	953 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-8	953 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-9	953 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-10	953 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-11	953 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD1-12	954 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-1	954 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-2	954 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-3	954 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-4	954 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-5	954 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-6	954 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-7	954 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-8	954 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-9	954 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-10	954 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-11	954 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-6	RTD2-12	954 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
87	Block	954 bit 13	Uint16	2	R	True=1 False=0	GMT
87	Restrained Pickup	954 bit 14	Uint16	2	R	True=1 False=0	GMT
87	Restrained Trip	954 bit 15	Uint16	2	R	True=1 False=0	GMT
87	Restrained Target A	955 bit 0	Uint16	2	R	True=1 False=0	GMT
87	Restrained Target B	955 bit 1	Uint16	2	R	True=1 False=0	GMT
87	Restrained Target C	955 bit 2	Uint16	2	R	True=1 False=0	GMT
87	87 Alarm	955 bit 3	Uint16	2	R	True=1 False=0	GMT
Logic Labels	Logic Label 1	955 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 2	955 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 3	955 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 4	955 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 5	955 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 6	955 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 7	955 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 8	955 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 9	955 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 10	955 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 11	955 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Logic Labels	Logic Label 12	955 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		956 bit 0-7					
Remote Module 1	RTD Out of Range	956 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 1	Default Calibration Loaded	956 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 1	Defaults Loaded	956 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 1	Flash Failure	956 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 1	RTD Comm Send Fail	956 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 1	RTD Comm Receive Fail	956 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		956 bit 14					
Remote Module 2	RTD Out of Range	956 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 2	Default Calibration Loaded	957 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 2	Defaults Loaded	957 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 2	Flash Failure	957 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 2	RTD Comm Send Fail	957 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Remote Module 2	RTD Comm Receive Fail	957 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
Reserved		957 bit 5-958 bit 9					
Alarms	Ethernet Excess Traffic	958 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
25	Voltage Difference	958 bit 11	Uint16	2	R	True=1 False=0	FGIT
25	Angle Difference	958 bit 12	Uint16	2	R	True=1 False=0	FGIT
25	Slip Difference	958 bit 13	Uint16	2	R	True=1 False=0	FGIT
50-1	Target Unbalance	958 bit 14	Uint16	2	R	True=1 False=0	FGIMT
50-1	67 Target Unbalance	958 bit 15	Uint16	2	R	True=1 False=0	FGIT
50-2	Target Unbalance	959 bit 0	Uint16	2	R	True=1 False=0	FGIMT
50-2	67 Target Unbalance	959 bit 1	Uint16	2	R	True=1 False=0	FGIT

Name	Description	Register	Type	Bytes	Writable	Range	Style
50-3	Target Unbalance	959 bit 2	Uint16	2	R	True=1 False=0	FGIMT
50-3	67 Target Unbalance	959 bit 3	Uint16	2	R	True=1 False=0	FGIT
50-4	Target Unbalance	959 bit 4	Uint16	2	R	True=1 False=0	FGIMT
50-5	67 Target Unbalance	959 bit 5	Uint16	2	R	True=1 False=0	FGIT
50-5	Target Unbalance	959 bit 6	Uint16	2	R	True=1 False=0	FGIMT
50-5	67 Target Unbalance	959 bit 7	Uint16	2	R	True=1 False=0	FGIT
50-6	Target Unbalance	959 bit 8	Uint16	2	R	True=1 False=0	FGIMT
50-6	67 Target Unbalance	959 bit 9	Uint16	2	R	True=1 False=0	FGIT
Reserved		959 bit 10-963 bit 2					
51-1	Target Unbalance	963 bit 3	Uint16	2	R	True=1 False=0	FGIMT
51-1	67 Target Unbalance	963 bit 4	Uint16	2	R	True=1 False=0	FGIT
51-2	Target Unbalance	963 bit 5	Uint16	2	R	True=1 False=0	FGIMT
51-2	67 Target Unbalance	963 bit 6	Uint16	2	R	True=1 False=0	FGIT
51-3	Target Unbalance	963 bit 7	Uint16	2	R	True=1 False=0	FGIMT
51-3	67 Target Unbalance	963 bit 8	Uint16	2	R	True=1 False=0	FGIT
51-4	Target Unbalance	963 bit 9	Uint16	2	R	True=1 False=0	FGIMT
51-4	67 Target Unbalance	963 bit 10	Uint16	2	R	True=1 False=0	FGIT
51-5	Target Unbalance	963 bit 11	Uint16	2	R	True=1 False=0	FGIMT
51-5	67 Target Unbalance	963 bit 12	Uint16	2	R	True=1 False=0	FGIT
51-6	Target Unbalance	963 bit 13	Uint16	2	R	True=1 False=0	FGIT
51-6	67 Target Unbalance	963 bit 14	Uint16	2	R	True=1 False=0	FGIT
51-7	Target Unbalance	963 bit 15	Uint16	2	R	True=1 False=0	FGIT
51-7	67 Target Unbalance	964 bit 0	Uint16	2	R	True=1 False=0	FGIT
51-8	Block	964 bit 1	Uint16	2	R	True=1 False=0	T
51-8	Pickup	964 bit 2	Uint16	2	R	True=1 False=0	T
51-8	Trip	964 bit 3	Uint16	2	R	True=1 False=0	T
51-8	Target A	964 bit 4	Uint16	2	R	True=1 False=0	T
51-8	Target B	964 bit 5	Uint16	2	R	True=1 False=0	T
51-8	Target C	964 bit 6	Uint16	2	R	True=1 False=0	T
51-8	Target Negative Sequence	964 bit 7	Uint16	2	R	True=1 False=0	T
51-8	Target Residual	964 bit 8	Uint16	2	R	True=1 False=0	T
51-8	Target Independent Ground	964 bit 9	Uint16	2	R	True=1 False=0	T
51-8	67 Target A	964 bit 10	Uint16	2	R	True=1 False=0	T
51-8	67 Target B	964 bit 11	Uint16	2	R	True=1 False=0	T
51-8	67 Target C	964 bit 12	Uint16	2	R	True=1 False=0	T
51-8	67 Target Negative Sequence	964 bit 13	Uint16	2	R	True=1 False=0	T
51-8	67 Target Residual	964 bit 14	Uint16	2	R	True=1 False=0	T
51-8	67 Target Independent Ground	964 bit 15	Uint16	2	R	True=1 False=0	T
51-8	Target Positive Sequence	965 bit 0	Uint16	2	R	True=1 False=0	T
51-8	67 Target Positive Sequence	965 bit 1	Uint16	2	R	True=1 False=0	T
51-8	Target Unbalance	965 bit 2	Uint16	2	R	True=1 False=0	T
51-8	67 Target Unbalance	965 bit 3	Uint16	2	R	True=1 False=0	T
51-9	Block	965 bit 4	Uint16	2	R	True=1 False=0	T
51-9	Pickup	965 bit 5	Uint16	2	R	True=1 False=0	T
51-9	Trip	965 bit 6	Uint16	2	R	True=1 False=0	T
51-9	Target A	965 bit 7	Uint16	2	R	True=1 False=0	T
51-9	Target B	965 bit 8	Uint16	2	R	True=1 False=0	T
51-9	Target C	965 bit 9	Uint16	2	R	True=1 False=0	T
51-9	Target Negative Sequence	965 bit 10	Uint16	2	R	True=1 False=0	T
51-9	Target Residual	965 bit 11	Uint16	2	R	True=1 False=0	T
51-9	Target Independent Ground	965 bit 12	Uint16	2	R	True=1 False=0	T
51-9	67 Target A	965 bit 13	Uint16	2	R	True=1 False=0	T
51-9	67 Target B	965 bit 14	Uint16	2	R	True=1 False=0	T
51-9	67 Target C	965 bit 15	Uint16	2	R	True=1 False=0	T
51-9	67 Target Negative Sequence	966 bit 0	Uint16	2	R	True=1 False=0	T
51-9	67 Target Residual	966 bit 1	Uint16	2	R	True=1 False=0	T
51-9	67 Target Independent Ground	966 bit 2	Uint16	2	R	True=1 False=0	T
51-9	Target Positive Sequence	966 bit 3	Uint16	2	R	True=1 False=0	T
51-9	67 Target Positive Sequence	966 bit 4	Uint16	2	R	True=1 False=0	T
51-9	Target Unbalance	966 bit 5	Uint16	2	R	True=1 False=0	T
51-9	67 Target Unbalance	966 bit 6	Uint16	2	R	True=1 False=0	T
87	Unrestrained Trip	966 bit 7	Uint16	2	R	True=1 False=0	GMT
Reserved		966 bit 8	Uint16	2	R	True=1 False=0	GMT
87	Alarm A	966 bit 9	Uint16	2	R	True=1 False=0	GMT
87	Alarm B	966 bit 10	Uint16	2	R	True=1 False=0	GMT
87	Alarm C	966 bit 11	Uint16	2	R	True=1 False=0	GMT
87	Unrestrained Target A	966 bit 12	Uint16	2	R	True=1 False=0	GMT
87	Unrestrained Target B	966 bit 13	Uint16	2	R	True=1 False=0	GMT
87	Unrestrained Target C	966 bit 14	Uint16	2	R	True=1 False=0	GMT
87	2nd Harmonic Inhibit	966 bit 15	Uint16	2	R	True=1 False=0	GT
87	5th Harmonic Inhibit	967 bit 0	Uint16	2	R	True=1 False=0	GT
55	Block	967 bit 1	Uint16	2	R	True=1 False=0	M
55	Pickup	967 bit 2	Uint16	2	R	True=1 False=0	M
55	Trip	967 bit 3	Uint16	2	R	True=1 False=0	M
55	Target	967 bit 4	Uint16	2	R	True=1 False=0	M
21-1	Block	967 bit 5	Uint16	2	R	True=1 False=0	FGT
21-1	Pickup	967 bit 6	Uint16	2	R	True=1 False=0	FGT
21-1	Trip	967 bit 7	Uint16	2	R	True=1 False=0	FGT

Name	Description	Register	Type	Bytes	Writable	Range	Style
21-1	Target AB	967 bit 8	Uint16	2	R	True=1 False=0	FGT
21-1	Target BC	967 bit 9	Uint16	2	R	True=1 False=0	FGT
21-1	Target CA	967 bit 10	Uint16	2	R	True=1 False=0	FGT
21-2	Block	967 bit 11	Uint16	2	R	True=1 False=0	FGT
21-2	Pickup	967 bit 12	Uint16	2	R	True=1 False=0	FGT
21-2	Trip	967 bit 13	Uint16	2	R	True=1 False=0	FGT
21-2	Target AB	967 bit 14	Uint16	2	R	True=1 False=0	FGT
21-2	Target BC	967 bit 15	Uint16	2	R	True=1 False=0	FGT
21-2	Target CA	968 bit 0	Uint16	2	R	True=1 False=0	FGT
78V	Block	968 bit 1	Uint16	2	R	True=1 False=0	GI
78V	Pickup	968 bit 2	Uint16	2	R	True=1 False=0	GI
78V	Trip	968 bit 3	Uint16	2	R	True=1 False=0	GI
78V	Target	968 bit 4	Uint16	2	R	True=1 False=0	GI
78OOS	Block	968 bit 5	Uint16	2	R	True=1 False=0	G
78OOS	Pickup	968 bit 6	Uint16	2	R	True=1 False=0	G
78OOS	Trip	968 bit 7	Uint16	2	R	True=1 False=0	G
78OOS	Target	968 bit 8	Uint16	2	R	True=1 False=0	G
78OOS	MHO Pickup	968 bit 9	Uint16	2	R	True=1 False=0	G
78OOS	Blinder A Pickup	968 bit 10	Uint16	2	R	True=1 False=0	G
78OOS	Blinder B Pickup	968 bit 11	Uint16	2	R	True=1 False=0	G
25A	Block	968 bit 12	Uint16	2	R	True=1 False=0	G
25A	Status	968 bit 13	Uint16	2	R	True=1 False=0	G
25A	Voltage Difference	968 bit 14	Uint16	2	R	True=1 False=0	G
25A	Angle Difference	968 bit 15	Uint16	2	R	True=1 False=0	G
25A	Slip Difference	969 bit 0	Uint16	2	R	True=1 False=0	G
25A	Close Breaker	969 bit 1	Uint16	2	R	True=1 False=0	G
25A	Voltage Monitor 1 Status	969 bit 2	Uint16	2	R	True=1 False=0	G
25A	Initiate	969 bit 3	Uint16	2	R	True=1 False=0	G
25A	Sync Fail	969 bit 4	Uint16	2	R	True=1 False=0	G
25A	Raising Voltage	969 bit 5	Uint16	2	R	True=1 False=0	G
25A	Lowering Voltage	969 bit 6	Uint16	2	R	True=1 False=0	G
25A	Raising Frequency	969 bit 7	Uint16	2	R	True=1 False=0	G
25A	Lowering Frequency	969 bit 8	Uint16	2	R	True=1 False=0	G
25A	Sync In Progress	969 bit 9	Uint16	2	R	True=1 False=0	G
Alarms	Power Loss	969 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Alarms	Logic Alarm	969 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
87N-2	Block	969 bit 12	Uint16	2	R	True=1 False=0	T
87N-2	Pickup	969 bit 13	Uint16	2	R	True=1 False=0	T
87N-2	Trip	969 bit 14	Uint16	2	R	True=1 False=0	T
87N-2	Target	969 bit 15	Uint16	2	R	True=1 False=0	T
49RTD-7	Block	970 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	Pickup	970 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	Trip	970 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-1	970 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-2	970 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-3	970 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-4	970 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-5	970 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-6	970 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-7	970 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-8	970 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-9	970 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-10	970 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-11	970 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD1-12	970 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-1	970 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-2	971 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-3	971 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-4	971 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-5	971 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-6	971 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-7	971 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-8	971 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-9	971 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-10	971 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-11	971 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-7	RTD2-12	971 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	Block	971 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	Pickup	971 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	Trip	971 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-1	971 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-2	971 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-3	972 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-4	972 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-5	972 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-6	972 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-7	972 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-8	972 bit 5	Uint16	2	R	True=1 False=0	DFGIMT

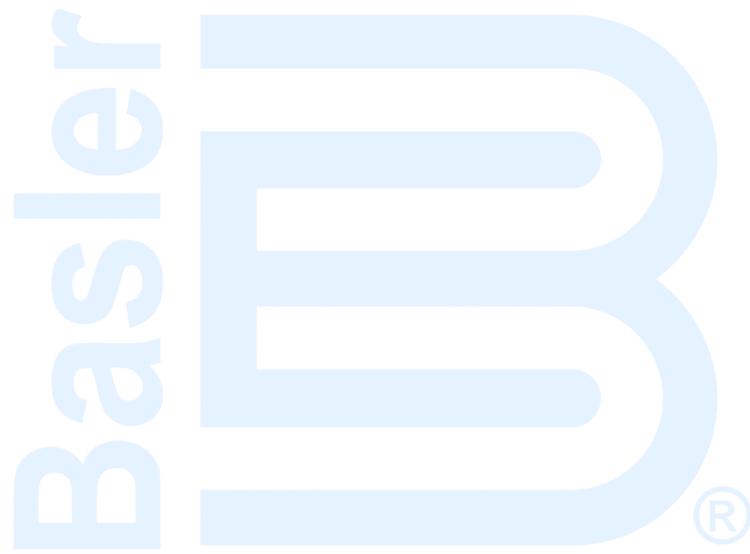
Name	Description	Register	Type	Bytes	Writable	Range	Style
49RTD-8	RTD1-9	972 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-10	972 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-11	972 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD1-12	972 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-1	972 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-2	972 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-3	972 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-4	972 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-5	972 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-6	972 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-7	973 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-8	973 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-9	973 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-10	973 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-11	973 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-8	RTD2-12	973 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	Block	973 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	Pickup	973 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	Trip	973 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-1	973 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-2	973 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-3	973 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-4	973 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-5	973 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-6	973 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-7	973 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-8	974 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-9	974 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-10	974 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-11	974 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD1-12	974 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-1	974 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-2	974 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-3	974 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-4	974 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-5	974 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-6	974 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-7	974 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-8	974 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-9	974 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-10	974 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-11	974 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-9	RTD2-12	975 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	Block	975 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	Pickup	975 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	Trip	975 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-1	975 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-2	975 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-3	975 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-4	975 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-5	975 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-6	975 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-7	975 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-8	975 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-9	975 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-10	975 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-11	975 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD1-12	975 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-1	976 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-2	976 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-3	976 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-4	976 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-5	976 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-6	976 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-7	976 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-8	976 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-9	976 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-10	976 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-11	976 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-10	RTD2-12	976 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	Block	976 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	Pickup	976 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	Trip	976 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-1	976 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-2	977 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-3	977 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-4	977 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-5	977 bit 3	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
49RTD-11	RTD1-6	977 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-7	977 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-8	977 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-9	977 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-10	977 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-11	977 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD1-12	977 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-1	977 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-2	977 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-3	977 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-4	977 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-5	977 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-6	978 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-7	978 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-8	978 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-9	978 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-10	978 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-11	978 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-11	RTD2-12	978 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	Block	978 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	Pickup	978 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	Trip	978 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-1	978 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-2	978 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-3	978 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-4	978 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-5	978 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-6	978 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-7	979 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-8	979 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-9	979 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-10	979 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-11	979 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD1-12	979 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-1	979 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-2	979 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-3	979 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-4	979 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-5	979 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-6	979 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-7	979 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-8	979 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-9	979 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-10	979 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-11	980 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-12	RTD2-12	980 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	Block	980 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	Pickup	980 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	Trip	980 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-1	980 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-2	980 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-3	980 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-4	980 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-5	980 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-6	980 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-7	980 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-8	980 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-9	980 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-10	980 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-11	980 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD1-12	981 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-1	981 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-2	981 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-3	981 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-4	981 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-5	981 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-6	981 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-7	981 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-8	981 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-9	981 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-10	981 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-11	981 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-13	RTD2-12	981 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	Block	981 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	Pickup	981 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	Trip	981 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-1	982 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-2	982 bit 1	Uint16	2	R	True=1 False=0	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Range	Style
49RTD-14	RTD1-3	982 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-4	982 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-5	982 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-6	982 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-7	982 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-8	982 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-9	982 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-10	982 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-11	982 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD1-12	982 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-1	982 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-2	982 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-3	982 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-4	982 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-5	983 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-6	983 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-7	983 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-8	983 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-9	983 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-10	983 bit 5	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-11	983 bit 6	Uint16	2	R	True=1 False=0	DFGIMT
49RTD-14	RTD2-12	983 bit 7	Uint16	2	R	True=1 False=0	DFGIMT
Alarms	Overrun Alarm	983 bit 8	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 1	983 bit 9	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 2	983 bit 10	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 3	983 bit 11	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 4	983 bit 12	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 5	983 bit 13	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 6	983 bit 14	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 7	983 bit 15	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 8	984 bit 0	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 9	984 bit 1	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 10	984 bit 2	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 11	984 bit 3	Uint16	2	R	True=1 False=0	DFGIMT
Programmable Targets	Programmable Target 12	984 bit 4	Uint16	2	R	True=1 False=0	DFGIMT
25A	Initiated	984 bit 5	Uint16	2	R	True=1 False=0	G
Grouped Targets	Any Phase A Target	984 bit 6	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Phase B Target	984 bit 7	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Phase C Target	984 bit 8	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Ground Target	984 bit 9	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Neutral Target	984 bit 10	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Ground/Neutral Target	984 bit 11	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Positive Sequence Target	984 bit 12	Uint16	2	R	True=1 False=0	FGIMT
Grouped Targets	Any Negative Sequence Target	984 bit 13	Uint16	2	R	True=1 False=0	FGIMT
DC Bus	Input I1 No IT-D Connected	984 bit 14	Uint16	2	R	True=1 False=0	D
DC Bus	Input V1 No IT-D Connected	984 bit 15	Uint16	2	R	True=1 False=0	D
DC Bus	Input V2 No IT-D Connected	985 bit 0	Uint16	2	R	True=1 False=0	D
DC Bus	Input V3 No IT-D Connected	985 bit 1	Uint16	2	R	True=1 False=0	D
27-1	Target	985 bit 2	Uint16	2	R	True=1 False=0	D
27-2	Target	985 bit 3	Uint16	2	R	True=1 False=0	D
27-3	Target	985 bit 4	Uint16	2	R	True=1 False=0	D
27-4	Target	985 bit 5	Uint16	2	R	True=1 False=0	D
59-1	Target	985 bit 6	Uint16	2	R	True=1 False=0	D
59-2	Target	985 bit 7	Uint16	2	R	True=1 False=0	D
59-3	Target	985 bit 8	Uint16	2	R	True=1 False=0	D
59-4	Target	985 bit 9	Uint16	2	R	True=1 False=0	D
76-1	Block	985 bit 10	Uint16	2	R	True=1 False=0	D
76-1	Pickup	985 bit 11	Uint16	2	R	True=1 False=0	D
76-1	Trip	985 bit 12	Uint16	2	R	True=1 False=0	D
76-1	Target	985 bit 13	Uint16	2	R	True=1 False=0	D
76-2	Block	985 bit 14	Uint16	2	R	True=1 False=0	D
76-2	Pickup	985 bit 15	Uint16	2	R	True=1 False=0	D
76-2	Trip	986 bit 0	Uint16	2	R	True=1 False=0	D
76-2	Target	986 bit 1	Uint16	2	R	True=1 False=0	D
76-3	Block	986 bit 2	Uint16	2	R	True=1 False=0	D
76-3	Pickup	986 bit 3	Uint16	2	R	True=1 False=0	D
76-3	Trip	986 bit 4	Uint16	2	R	True=1 False=0	D
76-3	Target	986 bit 5	Uint16	2	R	True=1 False=0	D
76-4	Block	986 bit 6	Uint16	2	R	True=1 False=0	D
76-4	Pickup	986 bit 7	Uint16	2	R	True=1 False=0	D
76-4	Trip	986 bit 8	Uint16	2	R	True=1 False=0	D
76-4	Target	986 bit 9	Uint16	2	R	True=1 False=0	D
76-5	Block	986 bit 10	Uint16	2	R	True=1 False=0	D
76-5	Pickup	986 bit 11	Uint16	2	R	True=1 False=0	D
76-5	Trip	986 bit 12	Uint16	2	R	True=1 False=0	D
76-5	Target	986 bit 13	Uint16	2	R	True=1 False=0	D
76-6	Block	986 bit 14	Uint16	2	R	True=1 False=0	D
76-6	Pickup	986 bit 15	Uint16	2	R	True=1 False=0	D

Name	Description	Register	Type	Bytes	Writable	Range	Style
76-6	Trip	987 bit 0	Uint16	2	R	True=1 False=0	D
76-6	Target	987 bit 1	Uint16	2	R	True=1 False=0	D
76-7	Block	987 bit 2	Uint16	2	R	True=1 False=0	D
76-7	Pickup	987 bit 3	Uint16	2	R	True=1 False=0	D
76-7	Trip	987 bit 4	Uint16	2	R	True=1 False=0	D
76-7	Target	987 bit 5	Uint16	2	R	True=1 False=0	D
76-8	Block	987 bit 6	Uint16	2	R	True=1 False=0	D
76-8	Pickup	987 bit 7	Uint16	2	R	True=1 False=0	D
76-8	Trip	987 bit 8	Uint16	2	R	True=1 False=0	D
76-8	Target	987 bit 9	Uint16	2	R	True=1 False=0	D
76-9	Block	987 bit 10	Uint16	2	R	True=1 False=0	D
76-9	Pickup	987 bit 11	Uint16	2	R	True=1 False=0	D
76-9	Trip	987 bit 12	Uint16	2	R	True=1 False=0	D
76-9	Target	987 bit 13	Uint16	2	R	True=1 False=0	D
76-10	Block	987 bit 14	Uint16	2	R	True=1 False=0	D
76-10	Pickup	987 bit 15	Uint16	2	R	True=1 False=0	D
76-10	Trip	988 bit 0	Uint16	2	R	True=1 False=0	D
76-10	Target	988 bit 1	Uint16	2	R	True=1 False=0	D
76-11	Block	988 bit 2	Uint16	2	R	True=1 False=0	D
76-11	Pickup	988 bit 3	Uint16	2	R	True=1 False=0	D
76-11	Trip	988 bit 4	Uint16	2	R	True=1 False=0	D
76-11	Target	988 bit 5	Uint16	2	R	True=1 False=0	D
76-12	Block	988 bit 6	Uint16	2	R	True=1 False=0	D
76-12	Pickup	988 bit 7	Uint16	2	R	True=1 False=0	D
76-12	Trip	988 bit 8	Uint16	2	R	True=1 False=0	D
76-12	Target	988 bit 9	Uint16	2	R	True=1 False=0	D
76-13	Block	988 bit 10	Uint16	2	R	True=1 False=0	D
76-13	Pickup	988 bit 11	Uint16	2	R	True=1 False=0	D
76-13	Trip	988 bit 12	Uint16	2	R	True=1 False=0	D
76-13	Target	988 bit 13	Uint16	2	R	True=1 False=0	D
ROR-1	Block	988 bit 14	Uint16	2	R	True=1 False=0	D
ROR-1	Trip	988 bit 15	Uint16	2	R	True=1 False=0	D
ROR-1	Pickup	989 bit 0	Uint16	2	R	True=1 False=0	D
ROR-1	Delta Current > Max	989 bit 1	Uint16	2	R	True=1 False=0	D
ROR-1	Delta Current Timer > Max	989 bit 2	Uint16	2	R	True=1 False=0	D
ROR-1	Delta Timer > Max	989 bit 3	Uint16	2	R	True=1 False=0	D
ROR-1	Delta Current > Min	989 bit 4	Uint16	2	R	True=1 False=0	D
ROR-1	Target	989 bit 5	Uint16	2	R	True=1 False=0	D
ROR-2	Block	989 bit 6	Uint16	2	R	True=1 False=0	D
ROR-2	Trip	989 bit 7	Uint16	2	R	True=1 False=0	D
ROR-2	Pickup	989 bit 8	Uint16	2	R	True=1 False=0	D
ROR-2	Delta Current > Max	989 bit 9	Uint16	2	R	True=1 False=0	D
ROR-2	Delta Current Timer > Max	989 bit 10	Uint16	2	R	True=1 False=0	D
ROR-2	Delta Timer > Max	989 bit 11	Uint16	2	R	True=1 False=0	D
ROR-2	Delta Current > Min	989 bit 12	Uint16	2	R	True=1 False=0	D
ROR-2	Target	989 bit 13	Uint16	2	R	True=1 False=0	D
32-1	Target Over	989 bit 14	Uint16	2	R	True=1 False=0	D
32-1	Target Under	989 bit 15	Uint16	2	R	True=1 False=0	D
32-2	Target Over	990 bit 0	Uint16	2	R	True=1 False=0	D
32-2	Target Under	990 bit 1	Uint16	2	R	True=1 False=0	D
82	Recloser Fail Alarm	990 bit 2	Uint16	2	R	True=1 False=0	D
82	Reclosing	990 bit 3	Uint16	2	R	True=1 False=0	D
82	Recloser Running	990 bit 4	Uint16	2	R	True=1 False=0	D
82	Recloser Reset	990 bit 5	Uint16	2	R	True=1 False=0	D
82	Recloser Lockout	990 bit 6	Uint16	2	R	True=1 False=0	D
82	Recloser LM Contact	990 bit 7	Uint16	2	R	True=1 False=0	D
82	Shot 1	990 bit 8	Uint16	2	R	True=1 False=0	D
82	Shot 2	990 bit 9	Uint16	2	R	True=1 False=0	D
82	Shot 3	990 bit 10	Uint16	2	R	True=1 False=0	D
82	Shot 4	990 bit 11	Uint16	2	R	True=1 False=0	D
82	Reclose Initiate	990 bit 12	Uint16	2	R	True=1 False=0	D
82	Wait	990 bit 13	Uint16	2	R	True=1 False=0	D
82	DTL	990 bit 14	Uint16	2	R	True=1 False=0	D
82	Enable 82	990 bit 15	Uint16	2	R	True=1 False=0	D
82	Bypass	991 bit 0	Uint16	2	R	True=1 False=0	D
Reserved		991 bit 1	Uint16	2	R	True=1 False=0	D
72	72 Trip Coil Monitor	991 bit 2	Uint16	2	R	True=1 False=0	D
Local Contacts	Contact 72 TCM	991 bit 3	Uint16	2	R	True=1 False=0	D
49	Block	991 bit 4	Uint16	2	R	True=1 False=0	D
49	Reset	991 bit 5	Uint16	2	R	True=1 False=0	D
49	Overload	991 bit 6	Uint16	2	R	True=1 False=0	D
49	Alarm	991 bit 7	Uint16	2	R	True=1 False=0	D
49	Trip	991 bit 8	Uint16	2	R	True=1 False=0	D
49	Target	991 bit 9	Uint16	2	R	True=1 False=0	D
Voltage Monitor	Block	991 bit 10	Uint16	2	R	True=1 False=0	D
Voltage Monitor	LB LL	991 bit 11	Uint16	2	R	True=1 False=0	D
Voltage Monitor	LB DL	991 bit 12	Uint16	2	R	True=1 False=0	D
Voltage Monitor	DB LL	991 bit 13	Uint16	2	R	True=1 False=0	D

Name	Description	Register	Type	Bytes	Writable	Range	Style
Voltage Monitor	DB DL	991 bit 14	Uint16	2	R	True=1 False=0	D
DC Demand Meter 1	I1 DC Positive Demand Alarm	991 bit 15	Uint16	2	R	True=1 False=0	D
DC Demand Meter 1	I1 DC Negative Demand Alarm	992 bit 0	Uint16	2	R	True=1 False=0	D
DC Demand Meter 1	P1 DC Positive Demand Alarm	992 bit 1	Uint16	2	R	True=1 False=0	D
DC Demand Meter 1	P1 DC Negative Demand Alarm	992 bit 2	Uint16	2	R	True=1 False=0	D



7 • Reporting Register Table

Name	Description	Register	Type	Bytes	Writable
Network Settings	Active IP Address Ethernet	1200	IP Address	4	R
Network Settings	Active Gateway Address Ethernet	1202	IP Address	4	R
Network Settings	Active Subnet Address Ethernet	1204	IP Address	4	R
Major Alarm	Read Alarms*	1206	Uint32	4	R
Minor Alarm	Read Alarms*	1208	Uint32	4	R
Targets	Read Targets*	1210	Uint32	4	R
Relay Alarms	Read Alarms*	1212	Uint32	4	R
Logic Alarms	Read Alarms*	1214	Uint32	4	R

* Refer to the *Alarms and Targets Reporting* chapter for information on reading alarms and targets.



8 • Power Quality Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Power Quality	Frequency 10 seconds	1300	Float	4	R	Hertz	150–10	FGIMT
Power Quality	VA 200 milliseconds	1302	Float	4	R	Volt	0–480	FGIMT
Power Quality	VB 200 milliseconds	1304	Float	4	R	Volt	0–480	FGIMT
Power Quality	VC 200 milliseconds	1306	Float	4	R	Volt	0–480	FGIMT
Power Quality	VA 3 seconds	1308	Float	4	R	Volt	0–480	FGIMT
Power Quality	VB 3 seconds	1310	Float	4	R	Volt	0–480	FGIMT
Power Quality	VC 3 seconds	1312	Float	4	R	Volt	0–480	FGIMT
Power Quality	VA 10 minutes	1314	Float	4	R	Volt	0–480	FGIMT
Power Quality	VB 10 minutes	1316	Float	4	R	Volt	0–480	FGIMT
Power Quality	VC 10 minutes	1318	Float	4	R	Volt	0–480	FGIMT
Power Quality	VA 2 hours	1320	Float	4	R	Volt	0–480	FGIMT
Power Quality	VB 2 hours	1322	Float	4	R	Volt	0–480	FGIMT
Power Quality	VC 2 hours	1324	Float	4	R	Volt	0–480	FGIMT
Power Quality	VAB 200 milliseconds	1326	Float	4	R	Volt	0–480	FGIMT
Power Quality	VBC 200 milliseconds	1328	Float	4	R	Volt	0–480	FGIMT
Power Quality	VCA 200 milliseconds	1330	Float	4	R	Volt	0–480	FGIMT
Power Quality	VAB 3 seconds	1332	Float	4	R	Volt	0–480	FGIMT
Power Quality	VBC 3 seconds	1334	Float	4	R	Volt	0–480	FGIMT
Power Quality	VCA 3 seconds	1336	Float	4	R	Volt	0–480	FGIMT
Power Quality	VAB 10 minutes	1338	Float	4	R	Volt	0–480	FGIMT
Power Quality	VBC 10 minutes	1340	Float	4	R	Volt	0–480	FGIMT
Power Quality	VCA 10 minutes	1342	Float	4	R	Volt	0–480	FGIMT
Power Quality	VAB 2 hours	1344	Float	4	R	Volt	0–480	FGIMT
Power Quality	VBC 2 hours	1346	Float	4	R	Volt	0–480	FGIMT
Power Quality	VCA 2 hours	1348	Float	4	R	Volt	0–480	FGIMT
Power Quality	VA 200 milliseconds primary	1350	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VB 200 milliseconds primary	1352	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VC 200 milliseconds primary	1354	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VA 3 seconds primary	1356	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VB 3 seconds primary	1358	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VC 3 seconds primary	1360	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VA 10 minutes primary	1362	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VB 10 minutes primary	1364	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VC 10 minutes primary	1366	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VA 2 hours primary	1368	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VB 2 hours primary	1370	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VC 2 hours primary	1372	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VAB 200 milliseconds primary	1374	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VBC 200 milliseconds primary	1376	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VCA 200 milliseconds primary	1378	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VAB 3 seconds primary	1380	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VBC 3 seconds primary	1382	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VCA 3 seconds primary	1384	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VAB 10 minutes primary	1386	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VBC 10 minutes primary	1388	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VCA 10 minutes primary	1390	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VAB 2 hours primary	1392	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VBC 2 hours primary	1394	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	VCA 2 hours primary	1396	Float	4	R	Volt	0–2000000000	FGIMT
Power Quality	Dip Status	1398	Float	4	R		Off=0 On=1	DFGIMT
Power Quality	Residual Voltage	1400	Float	4	R	Volt	0–480	FGIMT
Power Quality	Residual Voltage	1400	Float	4	R	Volt	0–2000	D
Power Quality	Residual Voltage Primary	1402	Float	4	R	Volt	0–2000000000	DFGIMT
Power Quality	Dip Duration	1404	Float	4	R	Millisecond	0–2000000000	DFGIMT
Power Quality	Swell Status	1406	Float	4	R	n/a	Off=0 On=1	DFGIMT
Power Quality	Swell Voltage	1408	Float	4	R	Volt	0–480	FGIMT
Power Quality	Swell Voltage	1408	Float	4	R	Volt	0–2000	D
Power Quality	Swell Voltage Primary	1410	Float	4	R	Volt	0–2000000000	DFGIMT
Power Quality	Swell Duration	1412	Float	4	R	Millisecond	0–2000000000	DFGIMT
Power Quality	U0 200 milliseconds	1414	Float	4	R	Percent	n/a	FGIMT
Power Quality	U2 200 milliseconds	1416	Float	4	R	Percent	n/a	FGIMT
Power Quality	U0 3 seconds	1418	Float	4	R	Percent	n/a	FGIMT
Power Quality	U2 3 seconds	1420	Float	4	R	Percent	n/a	FGIMT
Power Quality	U0 10 minutes	1422	Float	4	R	Percent	n/a	FGIMT
Power Quality	U2 10 minutes	1424	Float	4	R	Percent	n/a	FGIMT
Power Quality	U0 2 hours	1426	Float	4	R	Percent	n/a	FGIMT
Power Quality	U2 2 hours	1428	Float	4	R	Percent	n/a	FGIMT
Power Quality	VA Harmonic	1430	Float	4	R	Percent	n/a	FGIMT
Power Quality	VB Harmonic	1432	Float	4	R	Percent	n/a	FGIMT
Power Quality	VC Harmonic	1434	Float	4	R	Percent	n/a	FGIMT
Power Quality	IA Harmonic	1436	Float	4	R	Percent	n/a	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Power Quality	IB Harmonic	1438	Float	4	R	Percent	n/a	FGIMT
Power Quality	IC Harmonic	1440	Float	4	R	Percent	n/a	FGIMT
Power Quality	IG Harmonic	1442	Float	4	R	Percent	n/a	FGIMT
Power Quality	V1 200 milliseconds	1444	Float	4	R	Percent	n/a	D
Power Quality	V2 200 milliseconds	1446	Float	4	R	Percent	n/a	D
Power Quality	V3 200 milliseconds	1448	Float	4	R	Percent	n/a	D
Power Quality	V1 3 seconds	1450	Float	4	R	Percent	n/a	D
Power Quality	V2 3 seconds	1452	Float	4	R	Percent	n/a	D
Power Quality	V3 3 seconds	1454	Float	4	R	Percent	n/a	D
Power Quality	V1 10 minutes	1456	Float	4	R	Percent	n/a	D
Power Quality	V2 10 minutes	1458	Float	4	R	Percent	n/a	D
Power Quality	V3 10 minutes	1460	Float	4	R	Percent	n/a	D
Power Quality	V1 2 hours	1462	Float	4	R	Percent	n/a	D
Power Quality	V2 2 hours	1464	Float	4	R	Percent	n/a	D
Power Quality	V3 2 hours	1466	Float	4	R	Percent	n/a	D

9 • Fault Record Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Fault Report	Read Latest Fault ID	2000	Int32	4	R	n/a	1-255	DFGIMT
Fault Report	Record Number	2002	Int32	4	R	n/a	1-255	DFGIMT
Fault Report	Product Name	2004	String	10	R	n/a	0-10	DFGIMT
Fault Report	Station ID	2009	String	64	R	n/a	0-64	DFGIMT
Fault Report	Relay ID	2041	String	64	R	n/a	0-64	DFGIMT
Fault Report	User ID	2073	String	64	R	n/a	0-64	DFGIMT
Fault Report	Setting File Name	2105	String	64	R	n/a	0-64	DFGIMT
Fault Report	Relay IP Address	2137	IP Address	4	R	n/a	n/a	DFGIMT
Fault Report	Relay Modbus Serial Address	2139	UInt16	2	R	n/a	1-247	DFGIMT
Fault Report	Relay Modbus Ethernet Address	2140	UInt32	4	R	n/a	1-247	DFGIMT
Fault Report	Relay DNP Address	2142	UInt16	2	R	n/a	0-65519	DFGIMT
Fault Report	Timestamp Year	2143	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Month	2144	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Day of Month	2145	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Hour	2146	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Minute	2147	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Second	2148	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Timestamp Millisecond	2149	UInt16	2	R	n/a	n/a	DFGIMT
Fault Report	Clear Time	2150	UInt32	4	R	Millisecond	0-3600000	DFGIMT
Fault Report	Operate Time	2152	UInt32	4	R	Millisecond	0-3600000	DFGIMT
Fault Report	Event Type	2154	UInt16	2	R	n/a	None=0 Breaker Fail=1 Trip=2 Pickup=3 Logic=4 Forced Trigger=5	DFGIMT
Fault Report	Active Setting Group	2155	UInt16	2	R	n/a	SG0=1 SG1=2 SG2=4 SG3=8	DFGIMT
Reserved		2156						
Fault Report	IA	2157	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IB	2159	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IC	2161	Float	4	R	Amp	0-2000000000	FGIMT
Reserved		2163-64						
Fault Report	IG	2165	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	3I0	2167	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	I1	2169	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	I2	2171	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IA-ANG	2173	Float	4	R	Degree	0-360	FGIMT
Fault Report	IB-ANG	2175	Float	4	R	Degree	0-360	FGIMT
Fault Report	IC-ANG	2177	Float	4	R	Degree	0-360	FGIMT
Reserved		2179-80						
Fault Report	IG-ANG	2181	Float	4	R	Degree	0-360	FGIMT
Fault Report	3I0-ANG	2183	Float	4	R	Degree	0-360	FGIMT
Fault Report	I1-ANG	2185	Float	4	R	Degree	0-360	FGIMT
Fault Report	I2-ANG	2187	Float	4	R	Degree	0-360	FGIMT
Fault Report	VA	2189	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	VB	2191	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	VC	2193	Float	4	R	Volt	0-2000000000	FGIMT
Reserved		2195-96						
Fault Report	VX	2197	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	V0	2199	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	V1	2201	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	V2	2203	Float	4	R	Volt	0-2000000000	FGIMT
Fault Report	VA-ANG	2205	Float	4	R	Degree	0-360	FGIMT
Fault Report	VB-ANG	2207	Float	4	R	Degree	0-360	FGIMT
Fault Report	VC-ANG	2209	Float	4	R	Degree	0-360	FGIMT
Reserved		2211-12						
Fault Report	VX-ANG	2213	Float	4	R	Degree	0-360	FGIMT
Fault Report	V0-ANG	2215	Float	4	R	Degree	0-360	FGIMT
Fault Report	V1-ANG	2217	Float	4	R	Degree	0-360	FGIMT
Fault Report	V2-ANG	2219	Float	4	R	Degree	0-360	FGIMT
Fault Report	Phase Frequency	2221	Float	4	R	Hertz	150-10	FGIMT
Fault Report	Auxiliary Frequency	2223	Float	4	R	Hertz	150-10	FGIMT
Reserved		2225-26						

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Fault Report	Fault Type	2227	Uint16	2	R		NA=0 ABC=1 AG=2 BG=3 CG=4 AB=5 BC=6 CA=7 ABG=8 BCG=9 CAG=10	FGIMT
Fault Report	Number of Records	2228	Int32	4	R	n/a	n/a	DFGIMT
Fault Report	IA CT Circuit 2	2230	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IB CT Circuit 2	2232	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IC CT Circuit 2	2234	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IG CT Circuit 2	2236	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	3I0 CT Circuit 2	2238	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	I1 CT Circuit 2	2240	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	I2 CT Circuit 2	2242	Float	4	R	Amp	0-2000000000	FGIMT
Fault Report	IA-ANG CT Circuit 2	2244	Float	4	R	Degree	0-360	FGIMT
Fault Report	IB-ANG CT Circuit 2	2246	Float	4	R	Degree	0-360	FGIMT
Fault Report	IC-ANG CT Circuit 2	2248	Float	4	R	Degree	0-360	FGIMT
Fault Report	IG-ANG CT Circuit 2	2250	Float	4	R	Degree	0-360	FGIMT
Fault Report	3I0-ANG CT Circuit 2	2252	Float	4	R	Degree	0-360	FGIMT
Fault Report	I1-ANG CT Circuit 2	2254	Float	4	R	Degree	0-360	FGIMT
Fault Report	I2-ANG CT Circuit 2	2256	Float	4	R	Degree	0-360	FGIMT
Fault Report	I1 DC	2258	Float	4	R	Amp	-3.00E+14-3.00E+14	D
Fault Report	V1 DC	2260	Float	4	R	Volt	0-3.00E+14	D
Fault Report	V2 DC	2262	Float	4	R	Volt	0-3.00E+14	D
Fault Report	V3 DC	2264	Float	4	R	Volt	0-3.00E+14	D

10 • Relay Settings Register Table

Name	Description	Register	Type	Bytes	Writable	Range
UART Settings	Baud Rate	2600	UInt32	4	R W	1200 Baud=1200 2400 Baud=2400 4800 Baud=4800 9600 Baud=9600 19200 Baud=19200 38400 Baud=38400 57600 Baud=57600 115200 Baud=115200
UART Settings	Parity	2602	UInt32	4	R W	Even Parity=0 Odd Parity=1 No Parity=2
UART Settings	Bits Per Character	2604	UInt32	4	R W	8-Bits=8 7-Bits=7
UART Settings	Stop Bits	2606	UInt32	4	R W	1 Stop Bit=1 2 Stop Bits=2
Network Settings	IP Address Ethernet	2608	IP Address	4	R W	n/a
Network Settings	Gateway Address Ethernet	2610	IP Address	4	R W	n/a
Network Settings	Subnet Mask Ethernet	2612	IP Address	4	R W	n/a
Network Settings	Use DHCP Ethernet	2614	UInt32	4	R W	Off=0 On=1
Network Settings	Use Telnet Ethernet	2616	UInt32	4	R W	Off=0 On=1
Network Settings	NTP Address	2618	IP Address	4	R W	n/a
Network Settings	Use Web Server Ethernet	2620	UInt32	4	R W	Off=0 On=1
Time	Year	2622	UInt16	2	R W	2000-2099
Time	Month	2623	UInt8	1	R W	1-12
Time	Day	2624	UInt8	1	R W	1-31
Time	Hour	2625	UInt8	1	R W	0-23
Time	Minute	2626	UInt8	1	R W	0-59
Time	Second	2627	UInt8	1	R W	0-59
Time	Millisecond	2628	UInt16	2	R W	0-999
Time	Time Zone Hour Offset	2629	Int8	1	R W	-24
Time	Time Zone Minute Offset	2630	Int8	1	R W	-118
Time	Time Zone Index	2631	UInt8	1	R W	0-50
Time	RTC Update Priority	2632	UInt8	1	R W	1-5
Time	NTP Update Priority	2633	UInt8	1	R W	0-4
Time	IRIGB Update Priority	2634	UInt8	1	R W	0-4
Time	DNP Update Priority	2635	UInt8	1	R W	0-4
Time	DST Configuration	2636	UInt8	1	R W	Disabled=0 Floating=1 Fixed=2
Time	DST UTC Respective	2637	UInt8	1	R W	No=0 Yes=1
Time	DST Start Month	2638	UInt8	1	R W	January=1 February=2 March=3 April=4 May=5 June=6 July=7 August=8 September=9 October=10 November=11 December=12
Time	DST Start Day	2639	UInt8	1	R W	1-31
Time	DST Start Week Of Month	2640	UInt8	1	R W	First=0 Second=1 Third=2 Fourth=3 Last=4
Time	DST Start Day Of Week	2641	UInt8	1	R W	Sunday=1 Monday=2 Tuesday=3 Wednesday=4 Thursday=5 Friday=6 Saturday=7
Time	DST Start Hour	2642	UInt8	1	R W	0-23
Time	DST Start Minute	2643	UInt8	1	R W	0-59

Name	Description	Register	Type	Bytes	Writable	Range
Time	DST End Month	2644	UInt8	1	R W	January=1 February=2 March=3 April=4 May=5 June=6 July=7 August=8 September=9 October=10 November=11 December=12
Time	DST End Day	2645	UInt8	1	R W	1-31
Time	DST End Week Of Month	2646	UInt8	1	R W	First=0 Second=1 Third=2 Fourth=3 Last=4
Time	DST End Day Of Week	2647	UInt8	1	R W	Sunday=1 Monday=2 Tuesday=3 Wednesday=4 Thursday=5 Friday=6 Saturday=7
Time	DST End Hour	2648	UInt8	1	R W	0-23
Time	DST End Minute	2649	UInt8	1	R W	0-59
Time	DST Bias Hours	2650	Int8	1	R W	-24
Time	DST Bias Minutes	2651	Int8	1	R W	-118
Security Center	Unsecure Login	2652	String	5	R W	0-5
Security Center	Logout	2655	String	5	R W	0-5
Security Center	Secure Login	2658	String	34	R W	0-34

11 • Reporting Setup Register Table

Name	Description	Register	Type	Bytes	Writable
Major Alarm	Binary Point Number	2700	UInt32	4	R W
Major Alarm	Binary Point State	2702	UInt32	4	R W
Major Alarm	Reset	2704	UInt32	4	R W
Minor Alarm	Binary Point Number	2706	UInt32	4	R W
Minor Alarm	Binary Point State	2708	UInt32	4	R W
Minor Alarm	Reset	2710	UInt32	4	R W
Targets	Binary Point Number	2712	UInt32	4	R W
Targets	Binary Point State	2714	UInt32	4	R W
Targets	Reset	2716	UInt32	4	R W
Relay Alarms	Binary Point Number	2718	UInt32	4	R W
Relay Alarms	Binary Point State	2720	UInt32	4	R W
Relay Alarms	Reset	2722	UInt32	4	R W
Logic Alarms	Binary Point Number	2724	UInt32	4	R W
Logic Alarms	Binary Point State	2726	UInt32	4	R W
Logic Alarms	Reset	2728	UInt32	4	R W



12 • Power Quality Setup Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Power Quality	Reference Mode	2800	Uint32	4	R W	n/a	Fixed=0 Sliding=1	FGIMT
Power Quality	Dip Ratio	2802	Float	4	R W	Ratio	0.7–1	DFGIMT
Power Quality	Swell Ratio	2804	Float	4	R W	Ratio	1–1.3	DFGIMT
Power Quality	Dip Hysteresis	2806	Float	4	R W	Ratio	1–1.3	DFGIMT
Power Quality	Swell Hysteresis	2808	Float	4	R W	Ratio	0.7–1	DFGIMT
Power Quality	Harmonic to Read	2810	Float	4	R W	n/a	1–15	FGIMT



13 • Demand Setup Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Demand Meter	Period-Phase	2900	Float	4	R W	Minute	1-60	FGIMT
Demand Meter	Alarm-Threshold Phase	2902	Float	4	R W	Amp	0.5-16	FGIMT
Demand Meter	Period-Neutral	2904	Float	4	R W	Minute	1-60	FGIMT
Demand Meter	Alarm-Threshold Neutral	2906	Float	4	R W	Amp	0.5-16	FGIMT
Demand Meter	Period-I2	2908	Float	4	R W	Minute	1-60	FGIMT
Demand Meter	Alarm-Threshold I2	2910	Float	4	R W	Amp	0.5-16	FGIMT
Demand Meter	Alarm-Threshold Watt Positive	2912	Float	4	R W	Watt	0-8500	FGIMT
Demand Meter	Alarm-Threshold Watt Negative	2914	Float	4	R W	Watt	0-8500	FGIMT
Demand Meter	Alarm-Threshold var Positive	2916	Float	4	R W	var	0-8500	FGIMT
Demand Meter	Alarm-Threshold var Negative	2918	Float	4	R W	var	0-8500	FGIMT
Demand Meter	Alarm-Threshold S	2920	Float	4	R W	VA	0-8500	FGIMT
Reserved		2922						
Current Demand Circuit 2	Alarm-Threshold Phase	2924	Float	4	R W	Amp	0.5-16	FGIMT
Reserved		2926						
Current Demand Circuit 2	Alarm-Threshold Neutral	2928	Float	4	R W	Amp	0.5-16	FGIMT
Reserved		2930						
Current Demand Circuit 2	Alarm-Threshold I2	2932	Float	4	R W	Amp	0.5-16	FGIMT
Reserved		2934						
G Current Demand Circuit 2	Alarm-Threshold Neutral	2936	Float	4	R W	Amp	0.5-16	FGIMT
DC Demand Meter	Period DC	2938	Float	4	R W	Minute	1-60	D
DC Demand Meter	Alarm Threshold I1 DC Positive	2940	Float	4	R W	Millivolt	1-300	D
DC Demand Meter	Alarm Threshold I1 DC Negative	2942	Float	4	R W	Millivolt	(-300)-0	D
DC Demand Meter	Alarm Threshold P1 DC Positive	2944	Float	4	R W	Volt x Millivolt	1-562500	D
DC Demand Meter	Alarm Threshold P1 DC Negative	2946	Float	4	R W	Volt x Millivolt	(-562500)-0	D



14 • Fault Record Setup Register Table

Name	Description	Register	Type	Bytes	Writable	Range
Fault Report Selection	Record-Selection	3000	Int32	4	R W	1–255
Fault Report	Reset	3002	UInt32	4	R W	n/a



Name	Description	Register	Type	Bytes	Writable	Range
Modbus	DB key For MB Polling Block Register 104	9978	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 105	9979	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 106	9980	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 107	9981	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 108	9982	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 109	9983	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 110	9984	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 111	9985	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 112	9986	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 113	9987	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 114	9988	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 115	9989	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 116	9990	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 117	9991	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 118	9992	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 119	9993	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 120	9994	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 121	9995	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 122	9996	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 123	9997	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 124	9998	Uint16	2	R	1-9874
Modbus	DB key For MB Polling Block Register 125	9999	Uint16	2	R	1-9874

16 • Metering Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Phase Meter	VA Primary	10100 1000*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VA Angle	10102 1002*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	VB Primary	10104 1004*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VB Angle	10106 1006*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	VC Primary	10108 1008*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VC Angle	10110 1010*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	VAB Primary	10112 1012*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VAB Angle	10114 1014*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	VBC Primary	10116 1016*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VBC Angle	10118 1018*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	VCA Primary	10120 1020*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	VCA Angle	10122 1022*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	IA Primary	10124 1028*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	IA Angle	10126 1030*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	IB Primary	10128 1032*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	IB Angle	10130 1034*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	IC Primary	10132 1036*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	IC Angle	10134 1038*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	V1 Primary	10136 1040*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	V1 Angle	10138 1042*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	V2 Primary	10140 1044*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	V2 Angle	10142 1046*	Float	4	R	Degree	0-360	FGIMT
Phase Meter	3V0 Primary	10144 1048*	Float	4	R	Volt	0-2000000000	FGIMT
Phase Meter	3V0 Angle	10146 1050*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	I1 Primary	10148 1052*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	I1 Angle	10150 1054*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	I2 Primary	10152 1056*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	I2 Angle	10154 1058*	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 1	3I0 Primary	10156 1060*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	3I0 Angle	10158 1062*	Float	4	R	Degree	0-360	FGIMT
Power Meter	Watt A Average	10160 1064*	Float	4	R	Watt	0-3.00E+14	FGIMT
Power Meter	Watt B Average	10162 1066*	Float	4	R	Watt	0-3.00E+14	FGIMT
Power Meter	Watt C Average	10164 1068*	Float	4	R	Watt	0-3.00E+14	FGIMT
Power Meter	var A Average	10166 1070*	Float	4	R	var	0-3.00E+14	FGIMT
Power Meter	var B Average	10168 1072*	Float	4	R	var	0-3.00E+14	FGIMT
Power Meter	var C Average	10170 1074*	Float	4	R	var	0-3.00E+14	FGIMT
Reserved		10172 1076*						
Power Meter	VA-A	10174 1078*	Float	4	R	VA	0-3.00E+14	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Power Meter	VA-B	10176 1080*	Float	4	R	VA	0-3.00E+14	FGIMT
Power Meter	VA-C	10178 1082*	Float	4	R	VA	0-3.00E+14	FGIMT
Power Meter	PF A	10180 1084*	Float	4	R	Power Factor	(-1)-1	FGIMT
Power Meter	PF B	10182 1086*	Float	4	R	Power Factor	(-1)-1	FGIMT
Power Meter	PF C	10184 1088*	Float	4	R	Power Factor	(-1)-1	FGIMT
Power Meter	Total Watts Meter	10186 1090*	Float	4	R	Watt	0-3.00E+14	FGIMT
Power Meter	Total vars Meter	10188 1092*	Float	4	R	var	0-3.00E+14	FGIMT
Power Meter	Total VA	10190 1094*	Float	4	R	VA	0-3.00E+14	FGIMT
Power Meter	Total PF	10192 1096*	Float	4	R	Power Factor	(-1)-1	FGIMT
Reserved		10194-225						
Aux Bus Meter	Vx Primary	10226	Float	4	R	Volt	0-2000000000	FGIMT
Aux Bus Meter	Vx Angle	10228	Float	4	R	Degree	0-360	FGIMT
Reserved		10230-31						
Phase Meter - Circuit 1	Fundamental Primary	10232 1136*	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 1	Fundamental Angle	10234 1138*	Float	4	R	Degree	0-360	FGIMT
Reserved		10236-41						
Energy Meter	Positive Watthour A	10242 1146*	Float	4	R W	Watthour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive Watthour B	10244 1148*	Float	4	R W	Watthour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive Watthour C	10246 1150*	Float	4	R W	Watthour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive Watthour Total	10248 1152*	Float	4	R W	Watthour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive varhour A	10250 1154*	Float	4	R W	varhour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive varhour B	10252 1156*	Float	4	R W	varhour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive varhour C	10254 1158*	Float	4	R W	varhour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Positive varhour Total	10256 1160*	Float	4	R W	varhour	0.00E+00 - 1.00E+09	FGIMT
Energy Meter	Negative Watthour A	10258 1162*	Float	4	R W	Watthour	-1000000000	FGIMT
Energy Meter	Negative Watthour B	10260 1164*	Float	4	R W	Watthour	-1000000000	FGIMT
Energy Meter	Negative Watthour C	10262 1166*	Float	4	R W	Watthour	-1000000000	FGIMT
Energy Meter	Negative Watthour Total	10264 1168*	Float	4	R W	Watthour	-1000000000	FGIMT
Energy Meter	Negative varhour A	10266 1170*	Float	4	R W	varhour	-1000000000	FGIMT
Energy Meter	Negative varhour B	10268 1172*	Float	4	R W	varhour	-1000000000	FGIMT
Energy Meter	Negative varhour C	10270 1174*	Float	4	R W	varhour	-1000000000	FGIMT
Energy Meter	Negative varhour Total	10272 1176*	Float	4	R W	varhour	-1000000000	FGIMT
Aux Bus Meter	Vx 3rd Primary	10274 1178*	Float	4	R	Volt	0-2000000000	FGIMT
Aux Bus Meter	Vx 3rd Angle	10276 1180*	Float	4	R	Degree	0-360	FGIMT
Differential Meter 87N-1	Iop	10278	Float	4	R	Amp	0-100000	GMT
Differential Meter 87N-1	Ir	10280	Float	4	R	Amp	0-100000	GMT
Phase Meter - Circuit 2	IA Primary	10282	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	IB Primary	10284	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	IC Primary	10286	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	I1 Primary	10288	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	I2 Primary	10290	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	3I0 Primary	10292	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	IA Angle	10294	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	IB Angle	10296	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	IC Angle	10298	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	I1 Angle	10300	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	I2 Angle	10302	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	3I0 Angle	10304	Float	4	R	Degree	0-360	FGIMT
Phase Meter - Circuit 2	IG	10306	Float	4	R	Amp	0-2000000000	FGIMT
Phase Meter - Circuit 2	IG Angle	10308	Float	4	R	Degree	0-360	FGIMT
Frequency Meter	Phase Frequency	10310	Float	4	R	Hertz	10-150	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved		10312-41						
Frequency Meter	Auxiliary Frequency	10318	Float	4	R	Hertz	10-150	FGIMT
Reserved		10320-41						
Analog Input Meter 1	Scaled	10342	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 2	Scaled	10344	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 3	Scaled	10346	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 4	Scaled	10348	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 5	Scaled	10350	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 6	Scaled	10352	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 7	Scaled	10354	Float	4	R	n/a	n/a	DFGIMT
Analog Input Meter 8	Scaled	10356	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 1	Scaled	10358	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 2	Scaled	10360	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 3	Scaled	10362	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 4	Scaled	10364	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 5	Scaled	10366	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 6	Scaled	10368	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 7	Scaled	10370	Float	4	R	n/a	n/a	DFGIMT
Analog Output Meter 8	Scaled	10372	Float	4	R	n/a	n/a	DFGIMT
RTD Meter 1-1	Scaled	10374	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-2	Scaled	10376	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-3	Scaled	10378	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-4	Scaled	10380	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-5	Scaled	10382	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-6	Scaled	10384	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-7	Scaled	10386	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-8	Scaled	10388	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-9	Scaled	10390	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-10	Scaled	10392	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-11	Scaled	10394	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 1-12	Scaled	10396	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-1	Scaled	10398	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-2	Scaled	10400	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-3	Scaled	10402	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-4	Scaled	10404	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-5	Scaled	10406	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-6	Scaled	10408	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-7	Scaled	10410	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-8	Scaled	10412	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-9	Scaled	10414	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-10	Scaled	10416	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-11	Scaled	10418	Float	4	R	n/a	0-250	DFGIMT
RTD Meter 2-12	Scaled	10420	Float	4	R	n/a	0-250	DFGIMT
Phase Differential Meter	lop A	10422	Float	4	R	X Tap	0-100000	GMT
Phase Differential Meter	lr A	10424	Float	4	R	X Tap	0-100000	GMT
Phase Differential Meter	lop B	10426	Float	4	R	X Tap	0-100000	GMT
Phase Differential Meter	lr B	10428	Float	4	R	X Tap	0-100000	GMT
Phase Differential Meter	lop C	10430	Float	4	R	X Tap	0-100000	GMT
Phase Differential Meter	lr C	10432	Float	4	R	X Tap	0-100000	GMT
Learned Motor Data	Average Maximum Thermal	10434	Float	4	R	Percent	0-1000	M
Learned Motor Data	Average Max Current	10436	Float	4	R	Amp	0-2000000000	M
Learned Motor Data	Average Min Voltage	10438	Float	4	R	Volt	0-2000000000	M
Learned Motor Data	Average Max RTD Group 1	10440	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Max RTD Group 2	10442	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Max RTD Group 3	10444	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Max RTD Group 4	10446	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Start Duration Min	10448	Int32	4	R	n/a	0-500000	M
Learned Motor Data	Average Start Duration Sec	10450	Int32	4	R	n/a	0-500000	M
Motor Meter	Thermal Capacity	10452	Float	4	R	Percent	0-2001	M
Motor Meter	Current Unbalance	10454	Float	4	R	n/a	n/a	M
Motor Meter	Voltage Unbalance	10456	Float	4	R	n/a	n/a	M
Motor Meter	Effective Motor Load	10458	Float	4	R	n/a	n/a	M
Phase Differential Meter	lop 2 nd A	10460	Float	4	R	Amp	0 - 100000	GT
Phase Differential Meter	lop 2 nd B	10462	Float	4	R	Amp	0 - 100000	GT
Phase Differential Meter	lop 2 nd C	10464	Float	4	R	Amp	0 - 100000	GT
Phase Differential Meter	lop 5 th A	10466	Float	4	R	Amp	0 - 100000	GT
Phase Differential Meter	lop 5 th B	10468	Float	4	R	Amp	0 - 100000	GT
Phase Differential Meter	lop 5 th C	10470	Float	4	R	Amp	0 - 100000	GT
Differential Meter 87N-2	lop	10472	Float	4	R	Amp	0 - 100000	T
Differential Meter 87N-2	lr	10474	Float	4	R	Amp	0 - 100000	T
Learned Motor Data	Average Maximum RTD Group 5	10476	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Maximum RTD Group 6	10478	Float	4	R	n/a	0-250	M
Learned Motor Data	Average Maximum RTD Group 7	10480	Float	4	R	n/a	0-250	M
Energy Meter	Positive Watthour DC	10482	Float	4	R W	Watthour	0.00E+00-1.00E+09	D
Energy Meter	Negative Watthour DC	10484	Float	4	R W	Watthour	0.00E+00-1.00E+09	D

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
DC Bus Meter	Input I1 System	10486	Float	4	R	Amp	-3.00E+14–3.00E+14	D
DC Bus Meter	Input V1 System	10488	Float	4	R	Volt	0–3.00E+14	D
DC Bus Meter	Input V2 System	10490	Float	4	R	Volt	0–3.00E+14	D
DC Bus Meter	Input V3 System	10492	Float	4	R	Volt	0–3.00E+14	D
DC Bus Meter	Power P1 System	10494	Float	4	R	Watt	0–3.00E+14	D
DC Bus Meter	Input I1 Shunt	10496	Float	4	R	Millivolt	(-3.00E+14)–3.00E+14	D
DC Bus Meter	Power P1 Shunt	10498	Float	4	R	Volt x Millivolt	(-3.00E+14)–3.00E+14	D
DC Meter	Thermal Energy	10500	Float	4	R W	Percent	0–2000	D

* Legacy register

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Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Demand Meter	IA Present	12100 1600*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	IB Present	12102 1602*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	IC Present	12104 1604*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	IG Present	12106 1606*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	3I0 Present	12108 1608*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	I2 Present	12110 1610*	Float	4	R	Amp	0–2000000000	FGIMT
Demand Meter	Watts A Present	12112 1612*	Float	4	R	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts B Present	12114 1614*	Float	4	R	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts C Present	12116 1616*	Float	4	R	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts Total Present	12118 1618*	Float	4	R	Watt	0 - 3.00E+14	FGIMT
Demand Meter	vars A Present	12120 1620*	Float	4	R	var	0 - 3.00E+14	FGIMT
Demand Meter	vars B Present	12122 1622*	Float	4	R	var	0 - 3.00E+14	FGIMT
Demand Meter	vars C Present	12124 1624*	Float	4	R	var	0 - 3.00E+14	FGIMT
Demand Meter	vars Total Present	12126 1626*	Float	4	R	var	0 - 3.00E+14	FGIMT
Demand Meter	S A Present	12128 1628*	Float	4	R	VA	0 - 3.00E+14	FGIMT
Demand Meter	S B Present	12130 1630*	Float	4	R	VA	0 - 3.00E+14	FGIMT
Demand Meter	S C Present	12132 1632*	Float	4	R	VA	0 - 3.00E+14	FGIMT
Demand Meter	S Total Present	12134 1634*	Float	4	R	VA	0 - 3.00E+14	FGIMT
Demand Meter	IA Peak	12136 1636*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	IB Peak	12138 1638*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	IC Peak	12140 1640*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	IG Peak	12142 1642*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	3I0 Peak	12144 1644*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	I2 Peak	12146 1646*	Float	4	R W	Amp	0–2000000000	FGIMT
Demand Meter	Watts A Peak	12148 1648*	Float	4	R W	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts B Peak	12150 1650*	Float	4	R W	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts C Peak	12152 1652*	Float	4	R W	Watt	0 - 3.00E+14	FGIMT
Demand Meter	Watts Total Peak	12154 1654*	Float	4	R W	Watt	0 - 3.00E+14	FGIMT
Demand Meter	vars A Peak	12156 1656*	Float	4	R W	var	0 - 3.00E+14	FGIMT
Demand Meter	vars B Peak	12158 1658*	Float	4	R W	var	0 - 3.00E+14	FGIMT
Demand Meter	vars C Peak	12160 1660*	Float	4	R W	var	0 - 3.00E+14	FGIMT
Demand Meter	vars Total Peak	12162 1662*	Float	4	R W	var	0 - 3.00E+14	FGIMT
Demand Meter	S A Peak	12164 1664*	Float	4	R W	VA	0 - 3.00E+14	FGIMT
Demand Meter	S B Peak	12166 1666*	Float	4	R W	VA	0 - 3.00E+14	FGIMT
Demand Meter	S C Peak	12168 1668*	Float	4	R W	VA	0 - 3.00E+14	FGIMT
Demand Meter	S Total Peak	12170 1670*	Float	4	R W	VA	0 - 3.00E+14	FGIMT
Demand Meter	IA Peak Time	12172 1672*	String	24	R	n/a	n/a	FGIMT
Demand Meter	IB Peak Time	12184 1684*	String	24	R	n/a	n/a	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Demand Meter	IC Peak Time	12196 1696*	String	24	R	n/a	n/a	FGIMT
Demand Meter	IG Peak Time	12208 1708*	String	24	R	n/a	n/a	FGIMT
Demand Meter	3I0 Peak Time	12220 1720*	String	24	R	n/a	n/a	FGIMT
Demand Meter	I2 Peak Time	12232 1732*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Watts A Peak Time	12244 1744*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Watts B Peak Time	12256 1756*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Watts C Peak Time	12268 1768*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Watts TOTAL Peak Time	12280 1780*	String	24	R	n/a	n/a	FGIMT
Demand Meter	vars A Peak Time	12292 1792*	String	24	R	n/a	n/a	FGIMT
Demand Meter	vars B Peak Time	12304 1804*	String	24	R	n/a	n/a	FGIMT
Demand Meter	vars C Peak Time	12316 1816*	String	24	R	n/a	n/a	FGIMT
Demand Meter	vars Total Peak Time	12328 1828*	String	24	R	n/a	n/a	FGIMT
Demand Meter	S A Peak Time	12340 1840*	String	24	R	n/a	n/a	FGIMT
Demand Meter	S B Peak Time	12352 1852*	String	24	R	n/a	n/a	FGIMT
Demand Meter	S C Peak Time	12364 1864*	String	24	R	n/a	n/a	FGIMT
Demand Meter	S Total Peak Time	12376 1876*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative Watts A Peak	12388 1888*	Float	4	R W	Watt	-3.00E+14 - 0	FGIMT
Demand Meter	Negative Watts B Peak	12390 1890*	Float	4	R W	Watt	-3.00E+14 - 0	FGIMT
Demand Meter	Negative Watts C Peak	12392 1892*	Float	4	R W	Watt	-3.00E+14 - 0	FGIMT
Demand Meter	Negative Watts Total Peak	12394 1894*	Float	4	R W	Watt	-3.00E+14 - 0	FGIMT
Demand Meter	Negative vars A Peak	12396 1896*	Float	4	R W	var	-3.00E+14 - 0	FGIMT
Demand Meter	Negative vars B Peak	12398 1898*	Float	4	R W	var	-3.00E+14 - 0	FGIMT
Demand Meter	Negative vars C Peak	12400 1900*	Float	4	R W	var	-3.00E+14 - 0	FGIMT
Demand Meter	Negative vars Total Peak	12402 1902*	Float	4	R W	var	-3.00E+14 - 0	FGIMT
Demand Meter	Negative Watts A Peak Time	12404 1904*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative Watts B Peak Time	12416 1916*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative Watts C Peak Time	12428 1928*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative Watts Total Peak Time	12440 1940*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative vars A Peak Time	12452 1952*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative vars B Peak Time	12464 1964*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative vars C Peak Time	12476 1976*	String	24	R	n/a	n/a	FGIMT
Demand Meter	Negative vars Total Peak Time	12488 1988*	String	24	R	n/a	n/a	FGIMT
Current Demand Circuit 2	IA Present	12500	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IB Present	12502	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IC Present	12504	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	3I0 Present	12506	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	I2 Present	12508	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IA Peak	12510	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IB Peak	12512	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IC Peak	12514	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	3I0 Peak	12516	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	I2 Peak	12518	Float	4	R	Amp	0-2000000000	FGIMT
Current Demand Circuit 2	IA Peak Time	12520	String	24	R	n/a	n/a	FGIMT
Current Demand Circuit 2	IB Peak Time	12532	String	24	R	n/a	n/a	FGIMT
Current Demand Circuit 2	IC Peak Time	12544	String	24	R	n/a	n/a	FGIMT
Current Demand Circuit 2	3I0 Peak Time	12556	String	24	R	n/a	n/a	FGIMT
Current Demand Circuit 2	I2 Peak Time	12568	String	24	R	n/a	n/a	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
G Current Demand Circuit 2	IG Present	12580	Float	4	R	Amp	0–2000000000	FGIMT
G Current Demand Circuit 2	IG Peak	12582	Float	4	R	Amp	0–2000000000	FGIMT
G Current Demand Circuit 2	IG Peak Time	12584	String	24	R	n/a	n/a	FGIMT
Reserved		12586–94						
DC Demand Meter	I1 DC Present	12596	Float	4	R	Amp	(-2000000000)–2000000000	D
DC Demand Meter	P1 DC Present	12598	Float	4	R	Watt	(-3.00E+14)–3.00E+14	D
DC Demand Meter	I1 DC Positive Peak	12600	Float	4	R W	Amp	0–2000000000	D
DC Demand Meter	P1 DC Positive Peak	12602	Float	4	R W	Watt	0–3.00E+14	D
DC Demand Meter	I1 DC Positive Peak Time	12604	String	24	R	n/a	n/a	D
Reserved		12606–14						
DC Demand Meter	P1 DC Positive Peak Time	12616	String	24	R	n/a	n/a	D
Reserved		12618–26						
DC Demand Meter	I1 DC Negative Peak	12628	Float	4	R W	Amp	(-2000000000)–0	D
DC Demand Meter	P1 DC Negative Peak	12630	Float	4	R W	Watt	0–3.00E+14	D
DC Demand Meter	I1 DC Negative Peak Time	12632	String	24	R	n/a	n/a	D
Reserved		12634–42						
DC Demand Meter	P1 DC Negative Peak Time	12644	String	24	R	n/a	n/a	D

* Legacy register



18 • Global Settings Register Table

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Phase Bus	Rotation	18100 2400*	Int32	4	R W	n/a	ABC=0 ACB=1	FGIMT
Phase Bus	Phase VT Connection	18102 2402*	Int32	4	R W	n/a	AB=0 BC=1 CA=2 AN=3 BN=4 CN=5 3W D=7 4W-Y=8	FGIMT
Reserved		18104 2404*						
Phase Bus	Phase VT Ratio	18106 2406*	Float	4	R W	n/a	1–10000	FGIMT
Phase Bus 1	Phase CT Ratio	18108 2408*	Float	4	R W	n/a	1–50000	FGIMT
Phase Bus	V rated	18110 2410*	Float	4	R W	Volt	1–1000	FGIMT
Phase Bus	I rated	18112 2412*	Float	4	R W	Amp	0–15	FGIMT
Reserved		18114-19 2414-2419*						
Aux Bus	Aux VT Connection	18120 2420*	Int32	4	R W		AB=0 BC=1 CA=2 AN=3 BN=4 CN=5 Ground=10	FGIMT
Aux Bus	Aux VT Ratio	18122 2422*	Float	4	R W	n/a	1–10000	FGIMT
Aux Bus	V rated	18124 2424*	Float	4	R W	Volt	1–1000	FGIMT
System Configure	Nominal Frequency	18126 2426*	UInt32	4	R W	n/a	25 Hz=25 50 Hz=50 60 Hz=60 100 Hz=100	FGIMT
Reserved		18128-37 2428-2437*						
Aux Bus 1	Ground CT Ratio	18138 2438*	Float	4	R W	n/a	1–50000	FGIMT
System Configure	Phase-To Phase 2759	18140 2440*	UInt32	4	R W	n/a	PN=0 PP=1	FGIMT
System Configure	Phase-To Phase 27R	18142 2442*	UInt32	4	R W	n/a	PN=0 PP=1	FGIMT
60FL	Fuse Loss I Block Mode	18144 2444*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
60FL	Fuse Loss P Block Mode	18146 2446*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
60FL	Fuse Loss N Block Mode	18148 2448*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
60FL	Fuse Loss Q Block Mode	18150 2450*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
Local Contacts	Contact Input 1 Recognition Time	18152 2452*	UInt8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 2 Recognition Time	18153 2453*	UInt8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 3 Recognition Time	18154 2454*	UInt8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 4 Recognition Time	18155 2455*	UInt8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 1 Debounce Time	18156 2456*	UInt8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 2 Debounce Time	18157 2457*	UInt8	1	R W	Millisecond	4–255	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Local Contacts	Contact Input 3 Debounce Time	18158 2458*	Uint8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Input 4 Debounce Time	18159 2459*	Uint8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Output 1 Override Control	18160 2460*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 2 Override Control	18161 2461*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 3 Override Control	18162 2462*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 4 Override Control	18163 2463*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 5 Override Control	18164 2464*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output A Override Control	18165 2465*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 1 Hold Control	18166 2466*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 2 Hold Control	18167 2467*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 3 Hold Control	18168 2468*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 4 Hold Control	18169 2469*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output 5 Hold Control	18170 2470*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Local Contacts	Contact Output A Hold Control	18171 2471*	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Major Alarm	Mode	18172 2472*	Uint32	4	R W	n/a	Disabled=0 Latching=1 Non Latching=2	DFGIMT
Minor Alarm	Mode	18174 2474*	Uint32	4	R W	n/a	Disabled=0 Latching=1 Non Latching=2	DFGIMT
Targets	Mode	18176 2476*	Uint32	4	R W	n/a	Disabled=0 Latching=1 Non Latching=2	DFGIMT
Relay Alarms	Mode	18178 2478*	Uint32	4	R W	n/a	Disabled=0 Latching=1 Non Latching=2	DFGIMT
Settings Group	SG Mode	18180 2480*	Uint32	4	R W	n/a	Disabled=0 Discrete Inputs=1 Binary Inputs=2	DFGIMT
Settings Group	SGC Alarm Timer	18182 2482*	Uint32	4	R W	Second	0–10	DFGIMT
Reserved		18184- 93 2484-93*						
Settings Group	SG1 Switch Time	18194 2494*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG1 Switch Threshold	18196 2496*	Float	4	R W	Amp	0–25	DFGIMT
Settings Group	SG1 Return Time	18198 2498*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG1 Return Threshold	18200 2500*	Float	4	R W	Amp	0–25	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Settings Group	SG1 Monitor Setting	18202 2502*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 IG=5 IP=6 IN=7 I2=8 60FL=9	FIT
Settings Group	SG1 Monitor Setting	18202 2502*	Uint32	4	R W	n/a	Disabled=0 IG=5 IP=6 IN=7 I2=8 60FL=9	GM
Settings Group	SG1 Monitor Setting	18202 2502*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 I1=5	D
Settings Group	SG2 Switch Time	18204 2504*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG2 Switch Threshold	18206 2506*	Float	4	R W	Amp	0–25	DFGIMT
Settings Group	SG2 Return Time	18208 2508*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG2 Return Threshold	18210 2510*	Float	4	R W	Amp	0–25	DFGIMT
Settings Group	SG2 Monitor Setting	18212 2512*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 IG=5 IP=6 IN=7 I2=8 60FL=9	FIT
Settings Group	SG2 Monitor Setting	18212 2512*	Uint32	4	R W	n/a	Disabled=0 IG=5 IP=6 IN=7 I2=8 60FL=9	GM
Settings Group	SG2 Monitor Setting	18212 2512*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 I1=5	D
Settings Group	SG3 Switch Time	18214 2514*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG3 Switch Threshold	18216 2516*	Float	4	R W	Amp	0–25	DFGIMT
Settings Group	SG3 Return Time	18218 2518*	Uint32	4	R W	Minute	0–60	DFGIMT
Settings Group	SG3 Return Threshold	18220 2520*	Float	4	R W	Amp	0–25	DFGIMT
Settings Group	SG3 Monitor Setting	18222 2522*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 IG=5 IP=6 IN=7 I2=8 60FL=9	FIT
Settings Group	SG3 Monitor Setting	18222 2522*	Uint32	4	R W	n/a	Disabled=0 IG=5 IP=6 IN=7 I2=8 60FL=9	GM
Settings Group	SG3 Monitor Setting	18222 2522*	Uint32	4	R W	n/a	Disabled=0 1 st Recloser Shot=1 2 nd Recloser Shot=2 3 rd Recloser Shot=3 4 th Recloser Shot=4 I1=5	D

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
PLC Timed Element Settings	Logic Timer 1 Output Timeout	18224 2524*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 2 Output Timeout	18226 2526*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 3 Output Timeout	18228 2528*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 4 Output Timeout	18230 2530*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 5 Output Timeout	18232 2532*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 6 Output Timeout	18234 2534*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 7 Output Timeout	18236 2536*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 8 Output Timeout	18238 2538*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 9 Output Timeout	18240 2540*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 10 Output Timeout	18242 2542*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 11 Output Timeout	18244 2544*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 12 Output Timeout	18246 2546*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 13 Output Timeout	18248 2548*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 14 Output Timeout	18250 2550*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 15 Output Timeout	18252 2552*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Logic Timer 16 Output Timeout	18254 2554*	Float	4	R W	Second	0–1800	DFGIMT
PLC Timed Element Settings	Counter 1 Output Timeout	18256 2556*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 2 Output Timeout	18258 2558*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 3 Output Timeout	18260 2560*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 4 Output Timeout	18262 2562*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 5 Output Timeout	18264 2564*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 6 Output Timeout	18266 2566*	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 7 Output Timeout	18268 2568	Float	4	R W	n/a	0–1800	DFGIMT
PLC Timed Element Settings	Counter 8 Output Timeout	18270 2570*	Float	4	R W	n/a	0–1800	DFGIMT
System Configure Settings Group	Metric English	18272 2572*	Uint32	4	R W	n/a	English=0 Metric=1	DFGIMT
	Source	18274 2574*	Uint32	4	R W	n/a	Bus1=0 Bus2=1 Vx Fundamental=2	DFGIMT
Reserved		18276- 85						
Phase Bus 2	Phase CT Ratio	18286	Float	4	R W	n/a	1–50000	FGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved		18288-98						
Aux Bus 2	Ground CT Ratio	18300	Float	4	R W	n/a	1-50000	FGIMT
Reserved		18302-03						
Contact Outputs	Contact Output 6 Override Control	18304	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Contact Outputs	Contact Output 7 Override Control	18305	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Contact Outputs	Contact Output 8 Override Control	18306	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Reserved		18307-09						
Contact Outputs	Contact Output 6 Hold Control	18310	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Contact Outputs	Contact Output 7 Hold Control	18311	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Contact Outputs	Contact Output 8 Hold Control	18312	Uint8	1	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Reserved		18313-15						
Contact Inputs	Contact Input 5 Recognition Time	18316	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 6 Recognition Time	18317	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 7 Recognition Time	18318	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 8 Recognition Time	18319	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 5 Debounce Time	18320	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 6 Debounce Time	18321	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 7 Debounce Time	18322	Uint8	1	R W	Millisecond	4-255	DFGIMT
Contact Inputs	Contact Input 8 Debounce Time	18323	Uint8	1	R W	Millisecond	4-255	DFGIMT
RTD Group Configuration 1	Block Bitmask	18324	Uint32	4	R W	n/a	1-16777215	DFGIMT
RTD Group Configuration 2	Block Bitmask	18326	Uint32	4	R W	n/a	1-16777215	DFGIMT
RTD Group Configuration 3	Block Bitmask	18328	Uint32	4	R W	n/a	1-16777215	DFGIMT
RTD Group Configuration 4	Block Bitmask	18330	Uint32	4	R W	n/a	1-16777215	DFGIMT
Analog Input Config 1	Type	18332	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 1	Parameter Minimum	18334	Float	4	R W	n/a	(-99999.9)-99999.9	DFGIMT
Analog Input Config 1	Parameter Maximum	18336	Float	4	R W	n/a	(-99999.9)-99999.9	DFGIMT
Analog Input Config 1	Input Current Minimum	18338	Float	4	R W	Milliamp	4-20	DFGIMT
Analog Input Config 1	Input Current Maximum	18340	Float	4	R W	Milliamp	4-20	DFGIMT
Analog Input Config 1	Input Voltage Minimum	18342	Float	4	R W	Volt	0-10	DFGIMT
Analog Input Config 1	Input Voltage Maximum	18344	Float	4	R W	Volt	0-10	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Config 2	Type	18346	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 2	Parameter Minimum	18348	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 2	Parameter Maximum	18350	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 2	Input Current Minimum	18352	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 2	Input Current Maximum	18354	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 2	Input Voltage Minimum	18356	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 2	Input Voltage Maximum	18358	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 3	Type	18360	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 3	Parameter Minimum	18362	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 3	Parameter Maximum	18364	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 3	Input Current Minimum	18366	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 3	Input Current Maximum	18368	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 3	Input Voltage Minimum	18370	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 3	Input Voltage Maximum	18372	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 4	Type	18374	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 4	Parameter Minimum	18376	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 4	Parameter Maximum	18378	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 4	Input Current Minimum	18380	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 4	Input Current Maximum	18382	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 4	Input Voltage Minimum	18384	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 4	Input Voltage Maximum	18386	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 1	Type	18388	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 1	Parameter Minimum	18390	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 1	Parameter Maximum	18392	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 1	Output Current Minimum	18394	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 1	Output Current Maximum	18396	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 1	Output Voltage Minimum	18398	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 1	Output Voltage Maximum	18400	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 1	Parameter Selection	18402	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37, RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60, Effective Motor Current=61, Thermal Capacity=62, Vx=63, Vx 3 rd Harmonic=64, Scaled PF Analog=65, Scaled PF Digital=66, V1=67, V2=68, V3=69, I1=70, P=71	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Output Config 2	Type	18404	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 2	Param Minimum	18406	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 2	Parameter Maximum	18408	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 2	Output Current Minimum	18410	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 2	Output Current Maximum	18412	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 2	Output Voltage Minimum	18414	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 2	Output Voltage Maximum	18416	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 2	Parameter Selection	18418	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37 RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60, Effective Motor Current=61, Thermal Capacity=62, Vx=63, Vx 3 rd Harmonic=64, Scaled PF Analog=65, Scaled PF Digital=66, V1=67, V2=68, V3=69, I1=70, P=71	DFGIMT
Analog Output Config 3	Type	18420	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 3	Parameter Minimum	18422	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 3	Parameter Maximum	18424	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 3	Output Current Minimum	18426	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 3	Output Current Maximum	18428	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 3	Output Voltage Minimum	18430	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 3	Output Voltage Maximum	18432	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 3	Parameter Selection	18434	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37 RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60, Effective Motor Current=61, Thermal Capacity=62, Vx=63, Vx 3 rd Harmonic=64, Scaled PF Analog=65, Scaled PF Digital=66, V1=67, V2=68, V3=69, I1=70, P=71	DFGIMT
Analog Output Config 4	Type	18436	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 4	Parameter Minimum	18438	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Output Config 4	Parameter Maximum	18440	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 4	Output Current Minimum	18442	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 4	Output Current Maximum	18444	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 4	Output Voltage Minimum	18446	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 4	Output Voltage Maximum	18448	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 4	Parameter Selection	18450	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37 RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60, Effective Motor Current=61, Thermal Capacity=62, Vx=63, Vx 3 rd Harmonic=64, Scaled PF Analog=65, Scaled PF Digital=66, V1=67, V2=68, V3=69, I1=70, P=71	DFGIMT
Motor Configuration	Full Load Amps	18452	Float	4	R W	Amp	1–25	M
Motor Configuration	Service Factor	18454	Float	4	R W	n/a	1–1.4	M
Reserved		18456-57						M
Motor Configuration	Motor Start Detection	18458	Uint32	4	R W	n/a	Current Only=0 Current and Contact=1 Contact Only=2	M
Motor Configuration	Motor Stop Detection	18460	Uint32	4	R W	n/a	Current Only=0 Current and Contact=1 Contact Only=2	M
Reserved		18462-63						M
Motor Maintenance Data	Hours Running	18464	Uint32	4	R W	Hour	0–2000000	M
Motor Maintenance Data	Minutes Running	18466	Uint32	4	R W	Minute	0–59	M
Motor Maintenance Data	Max Duration Start Min	18468	Uint32	4	R W	n/a	0–59	M
Motor Maintenance Data	Max Duration Start Seconds	18470	Uint32	4	R W	n/a	0–59	M
Motor Maintenance Data	Total Successful Norm Starts	18472	Int32	4	R W	n/a	0–500000	M
Motor Maintenance Data	Total Successful Emergency Starts	18474	Int32	4	R W	n/a	0–500000	M
Motor Maintenance Data	Total Fail Norm Starts	18476	Int32	4	R W	n/a	0–500000	M
Motor Maintenance Data	Total Fail Emergency Starts	18478	Int32	4	R W	n/a	0–500000	M
Motor Maintenance Data	Total Number Trips	18480	Int32	4	R W	n/a	0–500000	M
Motor Maintenance Data	Total Number Thermal Trips	18482	Int32	4	R W	n/a	0–500000	M

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Motor Maintenance Data	Maximum Thermal Start	18484	Float	4	R W	Percent	0–1000	M
Motor Maintenance Data	Maximum Cur Start	18486	Float	4	R W	Amp	0–2000000000	M
Motor Maintenance Data	Minimum Volt Start	18488	Float	4	R W	Volt	0–2000000000	M
Motor Maintenance Data	Hot RTD Group 1	18490	Float	4	R W	n/a	0–250	M
Motor Maintenance Data	Hot RTD Group 2	18492	Float	4	R W	n/a	0–250	M
Motor Maintenance Data	Hot RTD Group 3	18494	Float	4	R W	n/a	0–250	M
Motor Maintenance Data	Hot RTD Group 4	18496	Float	4	R W	n/a	0–250	M
Analog Input Config 5	Type	18498	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 5	Parameter Minimum	18500	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 5	Parameter Maximum	18502	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 5	Input Current Minimum	18504	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 5	Input Current Maximum	18506	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 5	Input Voltage Minimum	18508	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 5	Input Voltage Maximum	18510	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 6	Type	18512	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 6	Parameter Minimum	18514	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 6	Parameter Maximum	18516	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 6	Input Current Minimum	18518	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 6	Input Current Maximum	18520	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 6	Input Voltage Minimum	18522	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 6	Input Voltage Maximum	18524	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 7	Type	18526	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 7	Parameter Minimum	18528	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 7	Parameter Maximum	18530	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 7	Input Current Minimum	18532	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 7	Input Current Maximum	18534	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 7	Input Voltage Minimum	18536	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 7	Input Voltage Maximum	18538	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 8	Type	18540	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Input Config 8	Parameter Minimum	18542	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 8	Parameter Maximum	18544	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Config 8	Input Current Minimum	18546	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 8	Input Current Maximum	18548	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Input Config 8	Input Voltage Minimum	18550	Float	4	R W	Volt	0–10	DFGIMT
Analog Input Config 8	Input Voltage Maximum	18552	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 5	Type	18554	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 5	Parameter Minimum	18556	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Output Config 5	Parameter Maximum	18558	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 5	Output Current Minimum	18560	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 5	Output Current Maximum	18562	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 5	Output Voltage Minimum	18564	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 5	Output Voltage Maximum	18566	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 5	Parameter Selection	18568	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37, RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60	DFGIMT
Analog Output Config 6	Type	18570	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 6	Parameter Minimum	18572	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 6	Parameter Maximum	18574	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 6	Output Current Minimum	18576	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 6	Output Current Maximum	18578	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 6	Output Voltage Minimum	18580	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 6	Output Voltage Maximum	18582	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 6	Parameter Selection	18584	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37, RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60	DFGIMT
Analog Output Config 7	Type	18586	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 7	Parameter Minimum	18588	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 7	Parameter Maximum	18590	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 7	Output Current Minimum	18592	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 7	Output Current Maximum	18594	Float	4	R W	Milliamp	4–20	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Output Config 7	Output Voltage Minimum	18596	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 7	Output Voltage Maximum	18598	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 7	Parameter Selection	18600	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37 RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60	DFGIMT
Analog Output Config 8	Type	18602	Uint32	4	R W	n/a	Voltage=0 Current=1	DFGIMT
Analog Output Config 8	Parameter Minimum	18604	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 8	Parameter Maximum	18606	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Output Config 8	Output Current Minimum	18608	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 8	Output Current Maximum	18610	Float	4	R W	Milliamp	4–20	DFGIMT
Analog Output Config 8	Output Voltage Minimum	18612	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 8	Output Voltage Maximum	18614	Float	4	R W	Volt	0–10	DFGIMT
Analog Output Config 8	Parameter Selection	18616	Uint32	4	R W	n/a	VA=0, VB=1, VC=2, VAB=3, VBC=4, VCA=5, 3V0=6, V1=7, V2=8, IA-1=9, IB-1=10, IC-1=11, 3I0-1=12, I1-1=13, I2-1=14, IG-1=15, IA-2=16, IB-2=17, IC-2=18, 3I0-2=19, I1-2=20, I2-2=21, IG-2=22, Analog Input 1=21, Phase Frequency=23, Aux Frequency=24, Power Factor=25, Real Power=26, Imaginary Power=27, Apparent Power=28, Analog Input 1-1=29, Analog Input 1-2=30, Analog Input 1-3=31, Analog Input 1-4=32, Analog Input 2-1=33, Analog Input 2-2=34, Analog Input 2-3=35, Analog Input 2-4=36, RTD 1-1=37 RTD 1-2=38, RTD 1-3=39, RTD 1-4=40, RTD 1-5=41, RTD 1-6=42, RTD 1-7=43, RTD 1-8=44, RTD 1-9=45, RTD 1-10=46, RTD 1-11=47, RTD 1-12=48, RTD 2-1=49, RTD 2-2=50, RTD 2-3=51, RTD 2-4=52, RTD 2-5=53, RTD 2-6=54, RTD 2-7=55, RTD 2-8=56, RTD 2-9=57, RTD 2-10=58, RTD 2-11=59, RTD 2-12=60	DFGIMT
RTD Config Module 1 Input 1	Type	18618	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 2	Type	18620	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 3	Type	18622	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 4	Type	18624	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
RTD Config Module 1 Input 5	Type	18626	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 6	Type	18628	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 7	Type	18630	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 8	Type	18632	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 9	Type	18634	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 10	Type	18636	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 11	Type	18638	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 1 Input 12	Type	18640	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 1	Type	18642	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 2	Type	18644	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 3	Type	18646	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 4	Type	18648	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 5	Type	18650	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 6	Type	18652	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 7	Type	18654	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 8	Type	18656	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
RTD Config Module 2 Input 9	Type	18658	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 10	Type	18660	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 11	Type	18662	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
RTD Config Module 2 Input 12	Type	18664	Uint32	4	R W	n/a	10 Ohm Copper=0 100 Ohm Platinum=1 100 Ohm Nickel=2 120 Ohm Nickel=3 Disabled=4	DFGIMT
Remote Module 1	Enable Module	18666	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Remote Module 1	Comm Type	18668	Uint32	4	R W	n/a	Ethernet=0 RS485=1	DFGIMT
Remote Module 1	Address	18670	Uint32	4	R W	n/a	1-254	DFGIMT
Remote Module 2	Enable Module	18672	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Remote Module 2	Comm Type	18674	Uint32	4	R W	n/a	Ethernet=0 RS485=1	DFGIMT
Remote Module 2	Address	18676	Uint32	4	R W	n/a	1-254	DFGIMT
Phase Bus	Power Polarity	18678	Uint32	4	R W	n/a	Normal=0 Reverse=1	FGIMT
60FL	Fuse Loss Z Block Mode	18680	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
60FL	Fuse Loss S Block Mode	18682	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
60FL	Ignore Breaker Status	18684	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
Modbus	Auto Save	18686	Uint16	2	R W	n/a	Off=0 On=1	DFGIMT
Logic Alarms	Mode	18687	Uint32	4	R W	n/a	Disabled=0 Latching=1 Non-Latching=2	DFGIMT
RTD Group Configuration 5	Block Bitmask	18689	Uint32	4	R W	n/a	0-16777215	DFGIMT
RTD Group Configuration 6	Block Bitmask	18691	Uint32	4	R W	n/a	0-16777215	DFGIMT
RTD Group Configuration 7	Block Bitmask	18693	Uint32	4	R W	n/a	0-16777215	DFGIMT
Motor Maintenance Data	Hot RTD Group 5	18695	Float	4	R W	n/a	0-250	M
Motor Maintenance Data	Hot RTD Group 6	18697	Float	4	R W	n/a	0-250	M
Motor Maintenance Data	Hot RTD Group 7	18699	Float	4	R W	n/a	0-250	M
Directional Configure 1	Torque Angle Z0	18701	Float	4	R W	Degree	0-359.9	FIGT
Directional Configure 1	Torque Angle Z1	18703	Float	4	R W	Degree	0-359.9	FIGT
Directional Configure 1	Torque Angle Z2	18705	Float	4	R W	Degree	0-359.9	FIGT
Directional Configure 2	Torque Angle Z0	18707	Float	4	R W	Degree	0-359.9	FIGT
Directional Configure 2	Torque Angle Z1	18709	Float	4	R W	Degree	0-359.9	FIGT
Directional Configure 2	Torque Angle Z2	18711	Float	4	R W	Degree	0- 359.9	FIGT
Contact Inputs	Contact Input 9 Recognition Time	18713	Uint8	1	R W	Millisecond	4-255	DFGIMT

Name	Description	Register	Type	Bytes	Writable	Unit	Range	Style
Contact Inputs	Contact Input 10 Recognition Time	18714	Uint8	1	R W	Millisecond	4–255	DFGIMT
Contact Inputs	Contact Input 9 Debounce Time	18715	Uint8	1	R W	Millisecond	4–255	DFGIMT
Contact Inputs	Contact Input 10 Debounce Time	18716	Uint8	1	R W	Millisecond	4–255	DFGIMT
Local Contacts	Contact Output 1 Hold Control Time	18717	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 2 Hold Control Time	18718	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 3 Hold Control Time	18719	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 4 Hold Control Time	18720	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 5 Hold Control Time	18721	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output A Hold Control Time	18722	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 6 Hold Control Time	18723	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 7 Hold Control Time	18724	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Local Contacts	Contact Output 8 Hold Control Time	18725	Uint16	2	R W	Millisecond	200–2000	DFGIMT
Reserved		18726-36						
DC Bus	Shunt Rated Millivolts (mV)	18737	Uint32	4	R W	Millivolt	25–100	D
DC Bus	Shunt Rated Current (A)	18739	Uint32	4	R W	Amp	100–250000	D
Reserved		18741-46						
DC Bus	Power Voltage Source	18747	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
DC Bus	Power Quality Voltage Reference	18749	Uint32	4	R W	Volt	50–1875	D

* Legacy register

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Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			20100-01						
24	Mode	SG0	20102 3100*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20104-05						
24	Definite Time Pickup 1	SG0	20106 3102*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Pickup 2	SG0	20108 3104*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Delay 1	SG0	20110 3106*	Float	4	R W	Millisecond	50–600000	FGIT
24	Definite Time Delay 2	SG0	20112 3108*	Float	4	R W	Millisecond	50–600000	FGIT
24	Inverse Time Pickup	SG0	20114 3110*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	TD Trip	SG0	20116 3112*	Float	4	R W	n/a	0–9.9	FGIT
24	TD Reset	SG0	20118 3114*	Float	4	R W	n/a	0–9.9	FGIT
24	Curve Exponent	SG0	20120 3116*	UInt32	4	R W	n/a	0.5=0 1=1 2=2	FGIT
24	Alarm Pickup	SG0	20122 3118*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Alarm Time Delay	SG0	20124 3120*	Float	4	R W	Millisecond	50–600000	FGIT
24	Mode	SG1	20126 3122*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20128-29						
24	Definite Time Pickup 1	SG1	20130 3124*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Pickup 2	SG1	20132 3126*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Delay 1	SG1	20134 3128*	Float	4	R W	Millisecond	50–600000	FGIT
24	Definite Time Delay 2	SG1	20136 3130*	Float	4	R W	Millisecond	50–600000	FGIT
24	Inverse Time Pickup	SG1	20138 3132*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	TD Trip	SG1	20140 3134*	Float	4	R W	n/a	0–9.9	FGIT
24	TD Reset	SG1	20142 3136*	Float	4	R W	n/a	0–9.9	FGIT
24	Curve Exponent	SG1	20144 3138*	UInt32	4	R W	n/a	0.5=0 1=1 2=2	FGIT
24	Alarm Pickup	SG1	20146 3140*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Alarm Time Delay	SG1	20148 3142*	Float	4	R W	Millisecond	50–00000	FGIT
24	Mode	SG2	20150 3144*	UInt32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20152-53						
24	Definite Time Pickup 1	SG2	20154 3146*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Pickup 2	SG2	20156 3148*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Delay 1	SG2	20158 3150*	Float	4	R W	Millisecond	50–600000	FGIT
24	Definite Time Delay 2	SG2	20160 3152*	Float	4	R W	Millisecond	50–600000	FGIT
24	Inverse Time Pickup	SG2	20162 3154*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	TD Trip	SG2	20164 3156*	Float	4	R W	n/a	0–9.9	FGIT
24	TD Reset	SG2	20166 3158*	Float	4	R W	n/a	0–9.9	FGIT
24	Curve Exponent	SG2	20168 3160*	UInt32	4	R W	n/a	0.5=0 1=1 2=2	FGIT
24	Alarm Pickup	SG2	20170 3162*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
24	Alarm Time Delay	SG2	20172 3164*	Float	4	R W	Millisecond	50–600000	FGIT
24	Mode	SG3	20174 3166*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20176- 77						
24	Definite Time Pickup 1	SG3	20178 3168*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Pickup 2	SG3	20180 3170*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Definite Time Delay 1	SG3	20182 3172*	Float	4	R W	Millisecond	50–600000	FGIT
24	Definite Time Delay 2	SG3	20184 3174*	Float	4	R W	Millisecond	50–600000	FGIT
24	Inverse Time Pickup	SG3	20186 3176*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	TD Trip	SG3	20188 3178*	Float	4	R W	n/a	0–9.9	FGIT
24	TD Reset	SG3	20190 3180*	Float	4	R W	n/a	0–9.9	FGIT
24	Curve Exponent	SG3	20192 3182*	Uint32	4	R W	n/a	0.5=0 1=1 2=2	FGIT
24	Alarm Pickup	SG3	20194 3184*	Float	4	R W	Volts Per Hertz	0.5–6	FGIT
24	Alarm Time Delay	SG3	20196 3186*	Float	4	R W	Millisecond	50–600000	FGIT
25	Mode	SG0	20198 3188*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20200- 01						
25	Voltage Monitor Mode	SG0	20202 3190*	Uint32	4	R W	n/a	Disabled=0 DLDA=1 DLDA=2 DLDA_DLLA=3 LLDA=4 LLDA_DLDA=5 DLDA_LLDA=6 DLDA_DLLA_LLDA=7	FGIT
25	Phase Angle	SG0	20204 3192*	Float	4	R W	Degree	1–99	FGIT
25	Slip Freq	SG0	20206 3194*	Float	4	R W	Hertz	0.01–0.5	FGIT
25	Volt Mag Error Percent	SG0	20208 3196*	Float	4	R W	Percent	1–50	FGIT
25	Vs Fast Vd	SG0	20210 3198*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25	Dead Voltage	SG0	20212 3200*	Float	4	R W	Percent	10–90	FGIT
25	Live Voltage	SG0	20214 3202*	Float	4	R W	Percent	10–90	FGIT
25	Drop Out Delay	SG0	20216 3204*	Float	4	R W	Millisecond	50–60000	FGIT
25	Angle Compensation	SG0	20218 3206*	Float	4	R W	Degree	0–359.9	FGIT
25	Mode	SG1	20220 3208*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20222- 23						
25	Voltage Monitor Mode	SG1	20224 3210*	Uint32	4	R W	n/a	Disabled=0 DLDA=1 DLDA=2 DLDA_DLLA=3 LLDA=4 LLDA_DLDA=5 DLDA_LLDA=6 DLDA_DLLA_LLDA=7	FGIT
25	Phase Angle	SG1	20226 3212*	Float	4	R W	Degree	1–99	FGIT
25	Slip Freq	SG1	20228 3214*	Float	4	R W	Hertz	0.01–0.5	FGIT
25	Volt Mag Error Percent	SG1	20230 3216*	Float	4	R W	Percent	1–50	FGIT
25	Vs Fast Vd	SG1	20232 3218*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25	Dead Voltage	SG1	20234 3220*	Float	4	R W	Percent	10–90	FGIT
25	Live Voltage	SG1	20236 3222*	Float	4	R W	Percent	10–90	FGIT
25	Drop Out Delay	SG1	20238 3224*	Float	4	R W	Millisecond	50– 60000	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
25	Angle Compensation	SG1	20240 3226*	Float	4	R W	Degree	0–359.9	FGIT
25	Mode	SG2	20242 3228*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20244- 45						
25	Voltage Monitor Mode	SG2	20246 3230*	Uint32	4	R W	n/a	Disabled=0 DLDA=1 DLLA=2 DLDA_DLLA=3 LLDA=4 LLDA_DLDA=5 DLLA_LLDA=6 DLDA_DLLA_LLDA=7	FGIT
25	Phase Angle	SG2	20248 3232*	Float	4	R W	Degree	1–99	FGIT
25	Slip Freq	SG2	20250 3234*	Float	4	R W	Hertz	0.01– 0.5	FGIT
25	Volt Mag Error Percent	SG2	20252 3236*	Float	4	R W	Percent	1–50	FGIT
25	Vs Fast Vd	SG2	20254 3238*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25	Dead Voltage	SG2	20256 3240*	Float	4	R W	Percent	10–90	FGIT
25	Live Voltage	SG2	20258 3242*	Float	4	R W	Percent	10–90	FGIT
25	Drop Out Delay	SG2	20260 3244*	Float	4	R W	Millisecond	50–60000	FGIT
25	Angle Compensation	SG2	20262 3246*	Float	4	R W	Degree	0–359.9	FGIT
25	Mode	SG3	20264 3248*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			20266- 67						
25	Voltage Monitor Mode	SG3	20268 3250*	Uint32	4	R W	n/a	Disabled=0 DLDA=1 DLLA=2 DLDA_DLLA=3 LLDA=4 LLDA_DLDA=5 DLLA_LLDA=6 DLDA DLLA LLDA=7	FGIT
25	Phase Angle	SG3	20270 3252*	Float	4	R W	Degree	1–99	FGIT
25	Slip Freq	SG3	20272 3254*	Float	4	R W	Hertz	0.01–0.5	FGIT
25	Volt Mag Error Percent	SG3	20274 3256*	Float	4	R W	Percent	1–50	FGIT
25	Vs Fast Vd	SG3	20276 3258*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25	Dead Voltage	SG3	20278 3260*	Float	4	R W	Percent	10–90	FGIT
25	Live Voltage	SG3	20280 3262*	Float	4	R W	Percent	10–90	FGIT
25	Drop Out Delay	SG3	20282 3264*	Float	4	R W	Millisecond	50–60000	FGIT
25	Angle Compensation	SG3	20284 3266*	Float	4	R W	Degree	0–359.9	FGIT
27P-1	Mode	SG0	20286 3268*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20288- 89						
27P-1	Timing Mode	SG0	20290 3270*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-1	Pickup	SG0	20292 3272*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Delay	SG0	20294 3274*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-1	Inhibit Level	SG0	20296 3276*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Multiplier	SG0	20298 3278*	Float	4	R W	n/a	0–9.9	FGIMT
27P-1	Mode	SG1	20300 3280*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20302- 03						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-1	Timing Mode	SG1	20304 3282*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-1	Pickup	SG1	20306 3284*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Delay	SG1	20308 3286*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-1	Inhibit Level	SG1	20310 3288*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Multiplier	SG1	20312 3290*	Float	4	R W	n/a	0–9.9	FGIMT
27P-1	Mode	SG2	20314 3292*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20316- 17						
27P-1	Timing Mode	SG2	20318 3294*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-1	Pickup	SG2	20320 3296*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Delay	SG2	20322 3298*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-1	Inhibit Level	SG2	20324 3300*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Multiplier	SG2	20326 3302*	Float	4	R W	n/a	0–9.9	FGIMT
27P-1	Mode	SG3	20328 3304*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20330- 31						
27P-1	Timing Mode	SG3	20332 3306*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-1	Pickup	SG3	20334 3308*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Delay	SG3	20336 3310*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-1	Inhibit Level	SG3	20338 3312*	Float	4	R W	Volt	1–300	FGIMT
27P-1	Time Multiplier	SG3	20340 3314*	Float	4	R W	n/a	0–9.9	FGIMT
27P-2	Mode	SG0	20342 3316*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20344- 45						
27P-2	Timing Mode	SG0	20346 3318*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-2	Pickup	SG0	20348 3320*	Float	4	R W	Volt	1–300	FGIMT
27P-2	Time Delay	SG0	20350 3322*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-2	Inhibit Level	SG0	20352 3324*	Float	4	R W	Volt	1–300	FGIMT
27P-2	Time Multiplier	SG0	20354 3326*	Float	4	R W	n/a	0–9.9	FGIMT
27P-2	Mode	SG1	20356 3328*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20358- 59						
27P-2	Timing Mode	SG1	20360 3330*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-2	Pickup	SG1	20362 3332*	Float	4	R W	Volt	1–300	FGIMT
27P-2	Time Delay	SG1	20364 3334*	Float	4	R W	Millisecond	50–600000	FGIMT
27P-2	Inhibit Level	SG1	20366 3336*	Float	4	R W	Volt	1–300	FGIMT
27P-2	Time Multiplier	SG1	20368 3338*	Float	4	R W	n/a	0–9.9	FGIMT
27P-2	Mode	SG2	20370 3340*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20372- 73						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-2	Timing Mode	SG2	20374 3342*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-2	Pickup	SG2	20376 3344*	Float	4	R W	Volt	1-300	FGIMT
27P-2	Time Delay	SG2	20378 3346*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-2	Inhibit Level	SG2	20380 3348*	Float	4	R W	Volt	1-300	FGIMT
27P-2	Time Multiplier	SG2	20382 3350*	Float	4	R W	n/a	0-9.9	FGIMT
27P-2	Mode	SG3	20384 3352*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20386- 87						
27P-2	Timing Mode	SG3	20388 3354*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-2	Pickup	SG3	20390 3356*	Float	4	R W	Volt	1-300	FGIMT
27P-2	Time Delay	SG3	20392 3358*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-2	Inhibit Level	SG3	20394 3360*	Float	4	R W	Volt	1-300	FGIMT
27P-2	Time Multiplier	SG3	20396 3362*	Float	4	R W	n/a	0-9.9	FGIMT
27P-3	Mode	SG0	20398 3364*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20400- 01						
27P-3	Timing Mode	SG0	20402 3366*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-3	Pickup	SG0	20404 3368*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Delay	SG0	20406 3370*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-3	Inhibit Level	SG0	20408 3372*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Multiplier	SG0	20410 3374*	Float	4	R W	n/a	0-9.9	FGIMT
27P-3	Mode	SG1	20412 3376*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20414- 15						
27P-3	Timing Mode	SG1	20416 3378*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-3	Pickup	SG1	20418 3380*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Delay	SG1	20420 3382*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-3	Inhibit Level	SG1	20422 3384*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Multiplier	SG1	20424 3386*	Float	4	R W	n/a	0- 9.9	FGIMT
27P-3	Mode	SG2	20426 3388*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20428- 29						
27P-3	Timing Mode	SG2	20430 3390*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-3	Pickup	SG2	20432 3392*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Delay	SG2	20434 3394*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-3	Inhibit Level	SG2	20436 3396*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Multiplier	SG2	20438 3398*	Float	4	R W	n/a	0-9.9	FGIMT
27P-3	Mode	SG3	20440 3400*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20442- 43						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-3	Timing Mode	SG3	20444 3402*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-3	Pickup	SG3	20446 3404*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Delay	SG3	20448 3406*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-3	Inhibit Level	SG3	20450 3408*	Float	4	R W	Volt	1-300	FGIMT
27P-3	Time Multiplier	SG3	20452 3410*	Float	4	R W	n/a	0-9.9	FGIMT
27P-4	Mode	SG0	20454 3412*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20456- 57						
27P-4	Timing Mode	SG0	20458 3414*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-4	Pickup	SG0	20460 3416*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Delay	SG0	20462 3418*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-4	Inhibit Level	SG0	20464 3420*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Multiplier	SG0	20466 3422*	Float	4	R W	n/a	0-9.9	FGIMT
27P-4	Mode	SG1	20468 3424*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20470- 71						
27P-4	Timing Mode	SG1	20472 3426*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-4	Pickup	SG1	20474 3428*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Delay	SG1	20476 3430*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-4	Inhibit Level	SG1	20478 3432*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Multiplier	SG1	20480 3434*	Float	4	R W	n/a	0-9.9	FGIMT
27P-4	Mode	SG2	20482 3436*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20484- 85						
27P-4	Timing Mode	SG2	20486 3438*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-4	Pickup	SG2	20488 3440*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Delay	SG2	20490 3442*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-4	Inhibit Level	SG2	20492 3444*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Multiplier	SG2	20494 3446*	Float	4	R W	n/a	0-9.9	FGIMT
27P-4	Mode	SG3	20496 3448*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIMT
Reserved			20498- 99						
27P-4	Timing Mode	SG3	20500 3450*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
27P-4	Pickup	SG3	20502 3452*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Delay	SG3	20504 3454*	Float	4	R W	Millisecond	50-600000	FGIMT
27P-4	Inhibit Level	SG3	20506 3456*	Float	4	R W	Volt	1-300	FGIMT
27P-4	Time Multiplier	SG3	20508 3458*	Float	4	R W	n/a	0-9.9	FGIMT
27P-5	Mode	SG0	20510 3460*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20512- 13						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-5	Timing Mode	SG0	20514 3462*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27P-5	Pickup	SG0	20516 3464*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Delay	SG0	20518 3466*	Float	4	R W	Millisecond	50–600000	FGIT
27P-5	Inhibit Level	SG0	20520 3468*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Multiplier	SG0	20522 3470*	Float	4	R W	n/a	0–9.9	FGIT
27P-5	Mode	SG1	20524 3472*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20526- 27						
27P-5	Timing Mode	SG1	20528 3474*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27P-5	Pickup	SG1	20530 3476*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Delay	SG1	20532 3478*	Float	4	R W	Millisecond	50–600000	FGIT
27P-5	Inhibit Level	SG1	20534 3480*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Multiplier	SG1	20536 3482*	Float	4	R W	n/a	0–9.9	FGIT
27P-5	Mode	SG2	20538 3484*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20540- 41						
27P-5	Timing Mode	SG2	20542 3486*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27P-5	Pickup	SG2	20544 3488*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Delay	SG2	20546 3490*	Float	4	R W	Millisecond	50–600000	FGIT
27P-5	Inhibit Level	SG2	20548 3492*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Multiplier	SG2	20550 3494*	Float	4	R W	n/a	0–9.9	FGIT
27P-5	Mode	SG3	20552 3496*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20554- 55						
27P-5	Timing Mode	SG3	20556 3498*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27P-5	Pickup	SG3	20558 3500*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Delay	SG3	20560 3502*	Float	4	R W	Millisecond	50–600000	FGIT
27P-5	Inhibit Level	SG3	20562 3504*	Float	4	R W	Volt	1–300	FGIT
27P-5	Time Multiplier	SG3	20564 3506*	Float	4	R W	n/a	0–9.9	FGIT
27X-1	Mode	SG0	20566 3508*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20568- 71						
27X-1	Timing Mode	SG0	20572 3510*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-1	Pickup	SG0	20574 3512*	Float	4	R W	Volt	1–150	FGIT
27-1	Pickup	SG0	3512*	Float	4	R W	Volt	50–1875	D
27X-1	Time Delay	SG0	20576 3514*	Float	4	R W	Millisecond	50–600000	FGIT
27-1	Time Delay	SG0	3514*	Float	4	R W	Millisecond	0–60000	D
27X-1	Inhibit Level	SG0	20578 3516*	Float	4	R W	Volt	1–150	FGIT
27-1	Inhibit Level	SG0	3516*	Float	4	R W	Volt	50–1875	D
27X-1	Time Multiplier	SG0	20580 3518*	Float	4	R W	n/a	0–9.9	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-1	Mode	SG1	20582 3520*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20584- 87						
27X-1	Timing Mode	SG1	20588 3522*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-1	Pickup	SG1	20590	Float	4	R W	Volt	1–150	FGIT
27-1	Pickup	SG1	3524*	Float	4	R W	Volt	50–1875	D
27X-1	Time Delay	SG1	20592	Float	4	R W	Millisecond	50–600000	FGIT
27-1	Time Delay	SG1	3526*	Float	4	R W	Millisecond	0–60000	D
27X-1	Inhibit Level	SG1	20594	Float	4	R W	Volt	1–150	FGIT
27-1	Inhibit Level	SG1	3528*	Float	4	R W	Volt	50–1875	D
27X-1	Time Multiplier	SG1	20596 3530*	Float	4	R W	n/a	0–9.9	FGIT
27X-1	Mode	SG2	20598 3532*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20600- 03						
27X-1	Timing Mode	SG2	20604 3534*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-1	Pickup	SG2	20606	Float	4	R W	Volt	1–150	FGIT
27-1	Pickup	SG2	3536*	Float	4	R W	Volt	50–1875	D
27X-1	Time Delay	SG2	20608	Float	4	R W	Millisecond	50–600000	FGIT
27-1	Time Delay	SG2	3538*	Float	4	R W	Millisecond	0–60000	D
27X-1	Inhibit Level	SG2	20610	Float	4	R W	Volt	1–150	FGIT
27-1	Inhibit Level	SG2	3540*	Float	4	R W	Volt	50–1875	D
27X-1	Time Multiplier	SG2	20612 3542*	Float	4	R W	n/a	0–9.9	FGIT
27X-1	Mode	SG3	20614 3544*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20616- 19						
27X-1	Timing Mode	SG3	20620 3546*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-1	Pickup	SG3	20622	Float	4	R W	Volt	1–150	FGIT
27-1	Pickup	SG3	3548*	Float	4	R W	Volt	50–1875	D
27X-1	Time Delay	SG3	20624	Float	4	R W	Millisecond	50–600000	FGIT
27-1	Time Delay	SG3	3550*	Float	4	R W	Millisecond	0–60000	D
27X-1	Inhibit Level	SG3	20626	Float	4	R W	Volt	1–150	FGIT
27-1	Inhibit Level	SG3	3552*	Float	4	R W	Volt	50–1875	D
27X-1	Time Multiplier	SG3	20628 3554*	Float	4	R W	n/a	0–9.9	FGIT
27X-2	Mode	SG0	20630 3556*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20632- 35						
27X-2	Timing Mode	SG0	20636 3558*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-2	Pickup	SG0	20638	Float	4	R W	Volt	1–150	FGIT
27-2	Pickup	SG0	3560*	Float	4	R W	Volt	50–1875	D
27X-2	Time Delay	SG0	20640	Float	4	R W	Millisecond	50–600000	FGIT
27-2	Time Delay	SG0	3562*	Float	4	R W	Millisecond	0–60000	D
27X-2	Inhibit Level	SG0	20642	Float	4	R W	Volt	1–150	FGIT
27-2	Inhibit Level	SG0	3564*	Float	4	R W	Volt	50–1875	D
27X-2	Time Multiplier	SG0	20644 3566*	Float	4	R W	n/a	0–9.9	FGIT
27X-2	Mode	SG1	20646 3568*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20648- 51						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-2	Timing Mode	SG1	20652 3570*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-2	Pickup	SG1	20654	Float	4	R W	Volt	1-150	FGIT
27-2	Pickup	SG1	3572*	Float	4	R W	Volt	50-1875	D
27X-2	Time Delay	SG1	20656	Float	4	R W	Millisecond	50-600000	FGIT
27-2	Time Delay	SG1	3574*	Float	4	R W	Millisecond	0-60000	D
27X-2	Inhibit Level	SG1	20658	Float	4	R W	Volt	1-150	FGIT
27-2	Inhibit Level	SG1	3576*	Float	4	R W	Volt	50-1875	D
27X-2	Time Multiplier	SG1	20660 3578*	Float	4	R W	n/a	0-9.9	FGIT
27X-2	Mode	SG2	20662 3580*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20664- 67						
27X-2	Timing Mode	SG2	20668 3582*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-2	Pickup	SG2	20670	Float	4	R W	Volt	1-150	FGIT
27-2	Pickup	SG2	3584*	Float	4	R W	Volt	50-1875	D
27X-2	Time Delay	SG2	20672	Float	4	R W	Millisecond	50-600000	FGIT
27-2	Time Delay	SG2	3586*	Float	4	R W	Millisecond	0-60000	D
27X-2	Inhibit Level	SG2	20674	Float	4	R W	Volt	1-150	FGIT
27-2	Inhibit Level	SG2	3588*	Float	4	R W	Volt	50-1875	D
27X-2	Time Multiplier	SG2	20676 3590*	Float	4	R W	n/a	0-9.9	FGIT
27X-2	Mode	SG3	20678 3592*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20680- 83						
27X-2	Timing Mode	SG3	20684 3594*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-2	Pickup	SG3	20686	Float	4	R W	Volt	1-150	FGIT
27-2	Pickup	SG3	3596*	Float	4	R W	Volt	50-1875	D
27X-2	Time Delay	SG2	20688	Float	4	R W	Millisecond	50-600000	FGIT
27-2	Time Delay	SG3	3598*	Float	4	R W	Millisecond	0-60000	D
27X-2	Inhibit Level	SG3	20690	Float	4	R W	Volt	1-150	FGIT
27-2	Inhibit Level	SG3	3600*	Float	4	R W	Volt	50-1875	D
27X-2	Time Multiplier	SG3	20692 3602*	Float	4	R W	n/a	0-9.9	FGIT
27X-3	Mode	SG0	20694 3604*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20696- 99						
27X-3	Timing Mode	SG0	20700 3606*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-3	Pickup	SG0	20702	Float	4	R W	Volt	1-150	FGIT
27-3	Pickup	SG0	3608*	Float	4	R W	Volt	50-1875	D
27X-3	Time Delay	SG0	20704	Float	4	R W	Millisecond	50-600000	FGIT
27-3	Time Delay	SG0	3610*	Float	4	R W	Millisecond	0-60000	D
27X-3	Inhibit Level	SG0	20706	Float	4	R W	Volt	1-150	FGIT
27-3	Inhibit Level	SG0	3612*	Float	4	R W	Volt	50-1875	D
27X-3	Time Multiplier	SG0	20708 3614*	Float	4	R W	n/a	0-9.9	FGIT
27X-3	Mode	SG1	20710 3616*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20712- 15						
27X-3	Timing Mode	SG1	20716 3618*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-3	Pickup	SG1	20718	Float	4	R W	Volt	1-150	FGIT
27-3	Pickup	SG1	3620*	Float	4	R W	Volt	50-1875	D
27X-3	Time Delay	SG1	20720	Float	4	R W	Millisecond	50-600000	FGIT
27-3	Time Delay	SG1	3622*	Float	4	R W	Millisecond	0-60000	D
27X-3	Inhibit Level	SG1	20722	Float	4	R W	Volt	1-150	FGIT
27-3	Inhibit Level	SG1	3624*	Float	4	R W	Volt	50-1875	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-3	Time Multiplier	SG1	20724 3626*	Float	4	R W	n/a	0–9.9	FGIT
27X-3	Mode	SG2	20726 3628*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20728- 31						
27X-3	Timing Mode	SG2	20732 3630*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-3	Pickup	SG2	20734	Float	4	R W	Volt	1–150	FGIT
27-3	Pickup	SG2	3632*	Float	4	R W	Volt	50–1875	D
27X-3	Time Delay	SG2	20736	Float	4	R W	Millisecond	50–600000	FGIT
27-3	Time Delay	SG2	3634*	Float	4	R W	Millisecond	0–60000	D
27X-3	Inhibit Level	SG2	20738	Float	4	R W	Volt	1–150	FGIT
27-3	Inhibit Level	SG2	3636*	Float	4	R W	Volt	50–1875	D
27X-3	Time Multiplier	SG2	20740 3638*	Float	4	R W	n/a	0–9.9	FGIT
27X-3	Mode	SG3	20742 3640*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20744- 47						
27X-3	Timing Mode	SG3	20748 3642*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-3	Pickup	SG3	20750	Float	4	R W	Volt	1–150	FGIT
27-3	Pickup	SG3	3644*	Float	4	R W	Volt	50–1875	D
27X-3	Time Delay	SG3	20752	Float	4	R W	Millisecond	50–600000	FGIT
27-3	Time Delay	SG3	3646*	Float	4	R W	Millisecond	0–60000	D
27X-3	Inhibit Level	SG3	20754	Float	4	R W	Volt	1–150	FGIT
27-3	Inhibit Level	SG3	3648*	Float	4	R W	Volt	50–1875	D
27X-3	Time Multiplier	SG3	20756 3650*	Float	4	R W	n/a	0–9.9	FGIT
27X-4	Mode	SG0	20758 3652*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20760- 63						
27X-4	Timing Mode	SG0	20764 3654*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-4	Pickup	SG0	20766	Float	4	R W	Volt	1–150	FGIT
27-4	Pickup	SG0	3656*	Float	4	R W	Volt	50–1875	D
27X-4	Time Delay	SG0	20768	Float	4	R W	Millisecond	50–600000	FGIT
27-4	Time Delay	SG0	3658*	Float	4	R W	Millisecond	0–60000	D
27X-4	Inhibit Level	SG0	20770	Float	4	R W	Volt	1–150	FGIT
27-4	Inhibit Level	SG0	3660*	Float	4	R W	Volt	50–1875	D
27X-4	Time Multiplier	SG0	20772 3662*	Float	4	R W	n/a	0–9.9	FGIT
27X-4	Mode	SG1	20774 3664*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20776- 79						
27X-4	Timing Mode	SG1	20780 3666*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-4	Pickup	SG1	20782	Float	4	R W	Volt	1–150	FGIT
27-4	Pickup	SG1	3668*	Float	4	R W	Volt	50–1875	D
27X-4	Time Delay	SG1	20784	Float	4	R W	Millisecond	50–600000	FGIT
27-4	Time Delay	SG1	3670*	Float	4	R W	Millisecond	0–60000	D
27X-4	Inhibit Level	SG1	20786	Float	4	R W	Volt	1–150	FGIT
27-4	Inhibit Level	SG1	3672*	Float	4	R W	Volt	50–1875	D
27X-4	Time Multiplier	SG1	20788 3674*	Float	4	R W	n/a	0–9.9	FGIT
27X-4	Mode	SG2	20790 3676*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			20792-95						
27X-4	Timing Mode	SG2	20796 3678*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-4	Pickup	SG2	20798	Float	4	R W	Volt	1-150	FGIT
27-4	Pickup	SG2	3680*	Float	4	R W	Volt	50-1875	D
27X-4	Time Delay	SG2	20800	Float	4	R W	Millisecond	50-600000	FGIT
27-4	Time Delay	SG2	3682*	Float	4	R W	Millisecond	0-60000	D
27X-4	Inhibit Level	SG2	20802	Float	4	R W	Volt	1 - 150	FGIT
27-4	Inhibit Level	SG2	3684*	Float	4	R W	Volt	50-1875	D
27X-4	Time Multiplier	SG2	20804 3686*	Float	4	R W	n/a	0-9.9	FGIT
27X-4	Mode	SG3	20806 3688*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3 rd Harmonic=5	FGIT
Reserved			20808-11						
27X-4	Timing Mode	SG3	20812 3690*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
27X-4	Pickup	SG3	20814	Float	4	R W	Volt	1-150	FGIT
27-4	Pickup	SG3	3692*	Float	4	R W	Volt	50-1875	D
27X-4	Time Delay	SG3	20816	Float	4	R W	Millisecond	50-600000	FGIT
27-4	Time Delay	SG3	3694*	Float	4	R W	Millisecond	0-60000	D
27X-4	Inhibit Level	SG3	20818	Float	4	R W	Volt	1-150	FGIT
27-4	Inhibit Level	SG3	3696*	Float	4	R W	Volt	50-1875	D
27X-4	Time Multiplier	SG3	20820 3698*	Float	4	R W	n/a	0-9.9	FGIT
59P-1	Mode	SG0	20822 3700*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20824-25						
59P-1	Timing Mode	SG0	20826 3702*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-1	Pickup	SG0	20828 3704*	Float	4	R W	Volt	1-300	FIGMT
59P-1	Time Delay	SG0	20830 3706*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-1	Time Multiplier	SG0	20832 3708*	Float	4	R W	n/a	0-9.9	FIGMT
59P-1	Mode	SG1	20834 3710*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20836-37						
59P-1	Timing Mode	SG1	20838 3712*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-1	Pickup	SG1	20840 3714*	Float	4	R W	Volt	1-300	FIGMT
59P-1	Time Delay	SG1	20842 3716*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-1	Time Multiplier	SG1	20844 3718*	Float	4	R W	n/a	0-9.9	FIGMT
59P-1	Mode	SG2	20846 3720*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20848-49						
59P-1	Timing Mode	SG2	20850 3722*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-1	Pickup	SG2	20852 3724*	Float	4	R W	Volt	1-300	FIGMT
59P-1	Time Delay	SG2	20854 3726*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-1	Time Multiplier	SG2	20856 3728*	Float	4	R W	n/a	0-9.9	FIGMT
59P-1	Mode	SG3	20858 3730*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20860-61						
59P-1	Timing Mode	SG3	20862 3732*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59P-1	Pickup	SG3	20864 3734*	Float	4	R W	Volt	1-300	FIGMT
59P-1	Time Delay	SG3	20866 3736*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-1	Time Multiplier	SG3	20868 3738*	Float	4	R W	n/a	0-9.9	FIGMT
59P-2	Mode	SG0	20870 3740*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20872- 73						
59P-2	Timing Mode	SG0	20874 3742*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-2	Pickup	SG0	20876 3744*	Float	4	R W	Volt	1-300	FIGMT
59P-2	Time Delay	SG0	20878 3746*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-2	Time Multiplier	SG0	20880 3748*	Float	4	R W	n/a	0-9.9	FIGMT
59P-2	Mode	SG1	20882 3750*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20884- 85						
59P-2	Timing Mode	SG1	20886 3752*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-2	Pickup	SG1	20888 3754*	Float	4	R W	Volt	1-300	FIGMT
59P-2	Time Delay	SG1	20890 3756*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-2	Time Multiplier	SG1	20892 3758*	Float	4	R W	n/a	0-9.9	FIGMT
59P-2	Mode	SG2	20894 3760*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20896- 97						
59P-2	Timing Mode	SG2	20898 3762*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-2	Pickup	SG2	20900 3764*	Float	4	R W	Volt	1-300	FIGMT
59P-2	Time Delay	SG2	20902 3766*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-2	Time Multiplier	SG2	20904 3768*	Float	4	R W	n/a	0-9.9	FIGMT
59P-2	Mode	SG3	20906 3770*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20908- 09						
59P-2	Timing Mode	SG3	20910 3772*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FIGMT
59P-2	Pickup	SG3	20912 3744*	Float	4	R W	Volt	1-300	FIGMT
59P-2	Time Delay	SG3	20914 3776*	Float	4	R W	Millisecond	50-600000	FIGMT
59P-2	Time Multiplier	SG3	20916 3778*	Float	4	R W	n/a	0-9.9	FIGMT
59P-3	Mode	SG0	20918 3780*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FIGMT
Reserved			20920- 21						
59P-3	Timing Mode	SG0	20922 3782*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-3	Pickup	SG0	20924 3784*	Float	4	R W	Volt	1-300	FGIT
59P-3	Time Delay	SG0	20926 3786*	Float	4	R W	Millisecond	50-600000	FGIT
59P-3	Time Multiplier	SG0	20928 3788*	Float	4	R W	n/a	0-9.9	FGIT
59P-3	Mode	SG1	20930 3790*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			20932-33						
59P-3	Timing Mode	SG1	20934 3792*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-3	Pickup	SG1	20936 3794*	Float	4	R W	Volt	1-300	FGIT
59P-3	Time Delay	SG1	20938 3796*	Float	4	R W	Millisecond	50-600000	FGIT
59P-3	Time Multiplier	SG1	20940 3798*	Float	4	R W	n/a	0-9.9	FGIT
59P-3	Mode	SG2	20942 3800*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20944-45						
59P-3	Timing Mode	SG2	20946 3802*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-3	Pickup	SG2	20948 3804*	Float	4	R W	Volt	1-300	FGIT
59P-3	Time Delay	SG2	20950 3806*	Float	4	R W	Millisecond	50-600000	FGIT
59P-3	Time Multiplier	SG2	20952 3808*	Float	4	R W	n/a	0-9.9	FGIT
59P-3	Mode	SG3	20954 3810*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20956-57						
59P-3	Timing Mode	SG3	20958 3812*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-3	Pickup	SG3	20960 3814*	Float	4	R W	Volt	1-300	FGIT
59P-3	Time Delay	SG3	20962 3816*	Float	4	R W	Millisecond	50-600000	FGIT
59P-3	Time Multiplier	SG3	20964 3818*	Float	4	R W	n/a	0-9.9	FGIT
59P-4	Mode	SG0	20966 3820*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20968-69						
59P-4	Timing Mode	SG0	20970 3822*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-4	Pickup	SG0	20972 3824*	Float	4	R W	Volt	1-300	FGIT
59P-4	Time Delay	SG0	20974 3826*	Float	4	R W	Millisecond	50-600000	FGIT
59P-4	Time Multiplier	SG0	20976 3828*	Float	4	R W	n/a	0-9.9	FGIT
59P-4	Mode	SG1	20978 3830*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20980-81						
59P-4	Timing Mode	SG1	20982 3832*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-4	Pickup	SG1	20984 3834*	Float	4	R W	Volt	1-300	FGIT
59P-4	Time Delay	SG1	20986 3836*	Float	4	R W	Millisecond	50-600000	FGIT
59P-4	Time Multiplier	SG1	20988 3838*	Float	4	R W	n/a	0-9.9	FGIT
59P-4	Mode	SG2	20990 3840*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			20992-93						
59P-4	Timing Mode	SG2	20994 3842*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-4	Pickup	SG2	20996 3844*	Float	4	R W	Volt	1-300	FGIT
59P-4	Time Delay	SG2	20998 3846*	Float	4	R W	Millisecond	50-600000	FGIT
59P-4	Time Multiplier	SG2	21000 3848*	Float	4	R W	n/a	0-9.9	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59P-4	Mode	SG3	21002 3850*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3	FGIT
Reserved			21004- 05						
59P-4	Timing Mode	SG3	21006 3852*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59P-4	Pickup	SG3	21008 3854*	Float	4	R W	Volt	1-300	FGIT
59P-4	Time Delay	SG3	21010 3856*	Float	4	R W	Millisecond	50-600000	FGIT
59P-4	Time Multiplier	SG3	21012 3858*	Float	4	R W	n/a	0-9.9	FGIT
59X-1	Mode	SG0	21014 3860*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21016- 19						
59X-1	Timing Mode	SG0	21020 3862*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-1	Pickup	SG0	21022	Float	4	R W	Volt	1-150	FGIMT
59-1	Pickup	SG0	3864*	Float	4	R W	Volt	50-1875	D
59X-1	Time Delay	SG0	21024	Float	4	R W	Millisecond	50-600000	FGIMT
59-1	Time Delay	SG0	3866*	Float	4	R W	Millisecond	0-60000	D
59X-1	Time Multiplier	SG0	21026 3868*	Float	4	R W	n/a	0-9.9	FGIMT
59X-1	Mode	SG1	21028 3870*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21030- 33						
59X-1	Timing Mode	SG1	21034 3872*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-1	Pickup	SG1	21036	Float	4	R W	Volt	1-150	FGIMT
59-1	Pickup	SG1	3874*	Float	4	R W	Volt	50-1875	D
59X-1	Time Delay	SG1	21038	Float	4	R W	Millisecond	50-600000	FGIMT
59-1	Time Delay	SG1	3876*	Float	4	R W	Millisecond	0-60000	D
59X-1	Time Multiplier	SG1	21040 3878*	Float	4	R W	n/a	0-9.9	FGIMT
59X-1	Mode	SG2	21042 3880*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21044- 47						
59X-1	Timing Mode	SG2	21048 3882*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-1	Pickup	SG2	21050	Float	4	R W	Volt	1-150	FGIMT
59-1	Pickup	SG2	3884*	Float	4	R W	Volt	50-1875	D
59X-1	Time Delay	SG2	21052	Float	4	R W	Millisecond	50-600000	FGIMT
59-1	Time Delay	SG2	3886*	Float	4	R W	Millisecond	0-60000	D
59X-1	Time Multiplier	SG2	21054 3888*	Float	4	R W	n/a	0-9.9	FGIMT
59X-1	Mode	SG3	21056 3890*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21058- 61						
59X-1	Timing Mode	SG3	21062 3892*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-1	Pickup	SG3	21064	Float	4	R W	Volt	1-150	FGIMT
59-1	Pickup	SG3	3894*	Float	4	R W	Volt	50-1875	D
59X-1	Time Delay	SG3	21066	Float	4	R W	Millisecond	50 - 600000	FGIMT
59-1	Time Delay	SG3	3896*	Float	4	R W	Millisecond	0-60000	D
59X-1	Time Multiplier	SG3	21068 3898*	Float	4	R W	n/a	0-9.9	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59X-2	Mode	SG0	21070 3900*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21072- 75						
59X-2	Timing Mode	SG0	21076 3902*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-2	Pickup	SG0	21078	Float	4	R W	Volt	1-150	FGIMT
59-2	Pickup	SG0	3904*	Float	4	R W	Volt	50-1875	D
59X-2	Time Delay	SG0	21080	Float	4	R W	Millisecond	50-600000	FGIMT
59-2	Time Delay	SG0	3906*	Float	4	R W	Millisecond	0-60000	D
59X-2	Time Multiplier	SG0	21082 3908*	Float	4	R W	n/a	0-9.9	FGIMT
59X-2	Mode	SG1	21084 3910*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21086- 89						
59X-2	Timing Mode	SG1	21090 3912*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-2	Pickup	SG1	21092	Float	4	R W	Volt	1-150	FGIMT
59-2	Pickup	SG1	3914*	Float	4	R W	Volt	50-1875	D
59X-2	Time Delay	SG1	21094	Float	4	R W	Millisecond	50-600000	FGIMT
59-2	Time Delay	SG1	3916*	Float	4	R W	Millisecond	0-60000	D
59X-2	Time Multiplier	SG1	21096 3918*	Float	4	R W	n/a	0-9.9	FGIMT
59X-2	Mode	SG2	21098 3920*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21100- 03						
59X-2	Timing Mode	SG2	21104 3922*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-2	Pickup	SG2	21106	Float	4	R W	Volt	1-150	FGIMT
59-2	Pickup	SG2	3924*	Float	4	R W	Volt	50-1875	D
59X-2	Time Delay	SG2	21108	Float	4	R W	Millisecond	50-600000	FGIMT
59-2	Time Delay	SG2	3926*	Float	4	R W	Millisecond	0-60000	D
59X-2	Time Multiplier	SG2	21110 3928*	Float	4	R W	n/a	0-9.9	FGIMT
59X-2	Mode	SG3	21112 3930*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIMT
Reserved			21114- 16						
59X-2	Timing Mode	SG3	21118 3932*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIMT
59X-2	Pickup	SG3	21120	Float	4	R W	Volt	1-150	FGIMT
59-2	Pickup	SG3	3934*	Float	4	R W	Volt	50-1875	D
59X-2	Time Delay	SG3	21122	Float	4	R W	Millisecond	50-600000	FGIMT
59-2	Time Delay	SG3	3936*	Float	4	R W	Millisecond	0-60000	D
59X-2	Time Multiplier	SG3	21124 3938*	Float	4	R W	n/a	0-9.9	FGIMT
59X-3	Mode	SG0	21126 3940*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21128- 31						
59X-3	Timing Mode	SG0	21132 3942*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-3	Pickup	SG0	21134	Float	4	R W	Volt	1-150	FGIT
59-3	Pickup	SG0	3944*	Float	4	R W	Volt	50-1875	D
59X-3	Time Delay	SG0	21136	Float	4	R W	Millisecond	50-600000	FGIT
59-3	Time Delay	SG0	3946*	Float	4	R W	Millisecond	0-60000	D
59X-3	Time Multiplier	SG0	21138 3948*	Float	4	R W	n/a	0-9.9	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59X-3	Mode	SG1	21140 3950*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21142-45						
59X-3	Timing Mode	SG1	21146 3952*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-3	Pickup	SG1	21148	Float	4	R W	Volt	1-150	FGIT
59-3	Pickup	SG1	3954*	Float	4	R W	Volt	50-1875	D
59X-3	Time Delay	SG1	21150	Float	4	R W	Millisecond	50-600000	FGIT
59-3	Time Delay	SG1	3956*	Float	4	R W	Millisecond	0-60000	D
59X-3	Time Multiplier	SG1	21152 3958*	Float	4	R W	n/a	0-9.9	FGIT
59X-3	Mode	SG2	21154 3960*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21156-59						
59X-3	Timing Mode	SG2	21160 3962*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-3	Pickup	SG2	21162	Float	4	R W	Volt	1-150	FGIT
59-3	Pickup	SG2	3964*	Float	4	R W	Volt	50-1875	D
59X-3	Time Delay	SG2	21164	Float	4	R W	Millisecond	50-600000	FGIT
59-3	Time Delay	SG2	3966*	Float	4	R W	Millisecond	0-60000	D
59X-3	Time Multiplier	SG2	21166 3968*	Float	4	R W	n/a	0-9.9	FGIT
59X-3	Mode	SG3	21168 3970*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21170-73						
59X-3	Timing Mode	SG3	21174 3972*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-3	Pickup	SG3	21176	Float	4	R W	Volt	1-150	FGIT
59-3	Pickup	SG3	3974*	Float	4	R W	Volt	50-1875	D
59X-3	Time Delay	SG3	21178	Float	4	R W	Millisecond	50-600000	FGIT
59-3	Time Delay	SG3	3976*	Float	4	R W	Millisecond	0-60000	D
59X-3	Time Multiplier	SG3	21180 3978*	Float	4	R W	n/a	0-9.9	FGIT
59X-4	Mode	SG0	21182 3980*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21184-87						
59X-4	Timing Mode	SG0	21188 3982*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-4	Pickup	SG0	21190	Float	4	R W	Volt	1-150	FGIT
59-4	Pickup	SG0	3984*	Float	4	R W	Volt	50-1875	D
59X-4	Time Delay	SG0	21192	Float	4	R W	Millisecond	50-600000	FGIT
59-4	Time Delay	SG0	3986*	Float	4	R W	Millisecond	0-60000	D
59X-4	Time Multiplier	SG0	21194 3988*	Float	4	R W	n/a	0-9.9	FGIT
59X-4	Mode	SG1	21196 3990*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21198-201						
59X-4	Timing Mode	SG1	21202 3992*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-4	Pickup	SG1	21204	Float	4	R W	Volt	1-150	FGIT
59-4	Pickup	SG1	3994*	Float	4	R W	Volt	50-1875	D
59X-4	Time Delay	SG1	21206	Float	4	R W	Millisecond	50-600000	FGIT
59-4	Time Delay	SG1	3996*	Float	4	R W	Millisecond	0-60000	D
59X-4	Time Multiplier	SG1	21208 3998*	Float	4	R W	n/a	0-9.9	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59X-4	Mode	SG2	21210 4000*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21212- 15						
59X-4	Timing Mode	SG2	21216 4002*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-4	Pickup	SG2	21218	Float	4	R W	Volt	1–150	FGIT
59-4	Pickup	SG2	4004*	Float	4	R W	Volt	50–1875	D
59X-4	Time Delay	SG2	21220	Float	4	R W	Millisecond	50–600000	FGIT
59-4	Time Delay	SG2	4006*	Float	4	R W	Millisecond	0–60000	D
59X-4	Time Multiplier	SG2	21222 4008*	Float	4	R W	n/a	0–9.9	FGIT
59X-4	Mode	SG3	21224 4010*	Uint32	4	R W	n/a	Disabled=0 3V0=1 V1=2 V2=3 Vx Fundamental=4 Vx 3rd Harmonic=5	FGIT
Reserved			21226- 29						
59X-4	Timing Mode	SG3	21230 4012*	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	FGIT
59X-4	Pickup	SG3	21232	Float	4	R W	Volt	1–150	FGIT
59-4	Pickup	SG3	4014*	Float	4	R W	Volt	50–1875	D
59X-4	Time Delay	SG3	21234	Float	4	R W	Millisecond	50–600000	FGIT
59-4	Time Delay	SG3	4016*	Float	4	R W	Millisecond	0–60000	D
59X-4	Time Multiplier	SG3	21236 4018*	Float	4	R W	n/a	0–9.9	FGIT
67-1	Mode	SG0	21238 4020*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			21240- 41						
67-1	Zero Sequence Mode	SG0	21242 4022*	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-1	QVI Mode	SG0	21244 4024*	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-1	Mode	SG1	21246 4026*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			21248- 49						
67-1	Zero Sequence Mode	SG1	21250 4028*	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-1	QVI Mode	SG1	21252 4030*	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-1	Mode	SG2	21254 4032*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			21256- 57						
67-1	Zero Sequence Mode	SG2	21258 4034*	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-1	QVI Mode	SG2	21260 4036*	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-1	Mode	SG3	21262 4038*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			21264- 65						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
67-1	Zero Sequence Mode	SG3	21266 4040*	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-1	QVI Mode	SG3	21268 4042*	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
50-1	Mode	SG0	21270 4044*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-1	Source	SG0	21272	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-1	Pickup	SG0	21274 4046*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-1	Time Delay	SG0	21276 4048*	Float	4	R W	Millisecond	0 - 60000	FGIMT
50-1	Direction	SG0	21278 4050*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-1	Mode	SG1	21280 4052*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-1	Source	SG1	21282	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-1	Pickup	SG1	21284 4054*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-1	Time Delay	SG1	21286 4056*	Float	4	R W	Millisecond	0–60000	FGIMT
50-1	Direction	SG1	21288 4058*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-1	Mode	SG2	21290 4060*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-1	Source	SG2	21292	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-1	Pickup	SG2	21294 4062*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-1	Time Delay	SG2	21296 4064*	Float	4	R W	Millisecond	0–60000	FGIMT
50-1	Direction	SG2	21298 4066*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-1	Mode	SG3	21300 4068*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-1	Source	SG3	21302	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-1	Pickup	SG3	21304 4070*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-1	Time Delay	SG3	21306 4072*	Float	4	R W	Millisecond	0–60000	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-1	Direction	SG3	21308 4074*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-2	Mode	SG0	21310 4076*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-2	Source	SG0	21312	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-2	Pickup	SG0	21314 4078*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-2	Time Delay	SG0	21316 4080*	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Direction	SG0	21318 4082*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-2	Mode	SG1	21320 4084*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-2	Source	SG1	21322	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-2	Pickup	SG1	21324 4086*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-2	Time Delay	SG1	21326 4088*	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Direction	SG1	21328 4090*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-2	Mode	SG2	21330 4092*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-2	Source	SG2	21332	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-2	Pickup	SG2	21334 4094*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-2	Time Delay	SG2	21336 4096*	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Direction	SG2	21338 4098*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-2	Mode	SG3	21340 4100*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-2	Source	SG3	21342	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-2	Pickup	SG3	21344 4102*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-2	Time Delay	SG3	21346 4104*	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Direction	SG3	21348 4106*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-3	Mode	SG0	21350 4108*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-3	Source	SG0	21352	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-3	Pickup	SG0	21354 4110*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-3	Time Delay	SG0	21356 4112*	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Direction	SG0	21358 4114*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-3	Mode	SG1	21360 4116*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-3	Source	SG1	21362	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-3	Pickup	SG1	21364 4118*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-3	Time Delay	SG1	21366 4120*	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Direction	SG1	21368 4122*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-3	Mode	SG2	21370 4124*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-3	Source	SG2	21372	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-3	Pickup	SG2	21374 4126*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-3	Time Delay	SG2	21376 4128*	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Direction	SG2	21378 4130*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-3	Mode	SG3	21380 4132*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-3	Source	SG3	21382	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-3	Pickup	SG3	21384 4134*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-3	Time Delay	SG3	21386 4136*	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Direction	SG3	21388 4138*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-4	Mode	SG0	21390 4140*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-4	Source	SG0	21392	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-4	Pickup	SG0	21394 4142*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-4	Time Delay	SG0	21396 4144*	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Direction	SG0	21398 4146*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-4	Mode	SG1	21400 4148*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-4	Source	SG1	21402	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-4	Pickup	SG1	21404 4150*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-4	Time Delay	SG1	21406 4152*	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Direction	SG1	21408 4154*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-4	Mode	SG2	21410 4156*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-4	Source	SG2	21412	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-4	Pickup	SG2	21414 4158*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-4	Time Delay	SG2	21416 4160*	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Direction	SG2	21418 4162*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-4	Mode	SG3	21420 4164*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-4	Source	SG3	21422	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-4	Pickup	SG3	21424 4166*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-4	Time Delay	SG3	21426 4168*	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Direction	SG3	21428 4170*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-5	Mode	SG0	21430 4172*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-5	Source	SG0	21432	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-5	Pickup	SG0	21434 4174*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-5	Time Delay	SG0	21436 4176*	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Direction	SG0	21438 4178*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-5	Mode	SG1	21440 4180*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-5	Source	SG1	21442	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-5	Pickup	SG1	21444 4182*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-5	Time Delay	SG1	21446 4184*	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Direction	SG1	21448 4186*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-5	Mode	SG2	21450 4188*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-5	Source	SG2	21452	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-5	Pickup	SG2	21454 4190*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-5	Time Delay	SG2	21456 4192*	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Direction	SG2	21458 4194*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-5	Mode	SG3	21460 4196*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
50-5	Source	SG3	21462	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-5	Pickup	SG3	21464 4198*	Float	4	R W	Amp	0.5–150 (5A CT) 0.1–30 (1A CT)	FGIMT
50-5	Time Delay	SG3	21466 4200*	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Direction	SG3	21468 4202*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-6	Mode	SG0	21470 4204*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-6	Source	SG0	21472	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-6	Pickup	SG0	21474 4206*	Float	4	R W	Amp	0.5-150 (5A CT) 0.1-30 (1A CT)	FGIMT
50-6	Time Delay	SG0	21476 4208*	Float	4	R W	Millisecond	0-60000	FGIMT
50-6	Direction	SG0	21478 4210*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-6	Mode	SG1	21480 4212*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-6	Source	SG1	21482	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-6	Pickup	SG1	21484 4214*	Float	4	R W	Amp	0.5-150 (5A CT) 0.1-30 (1A CT)	FGIMT
50-6	Time Delay	SG1	21486 4216*	Float	4	R W	Millisecond	0-60000	FGIMT
50-6	Direction	SG1	21488 4218*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-6	Mode	SG2	21490 4220*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-6	Source	SG2	21492	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-6	Pickup	SG2	21494 4222*	Float	4	R W	Amp	0.5-150 (5A CT) 0.1-30 (1A CT)	FGIMT
50-6	Time Delay	SG2	21496 4224*	Float	4	R W	Millisecond	0-60000	FGIMT
50-6	Direction	SG2	21498 4226*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
50-6	Mode	SG3	21500 4228*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8	FGIMT
50-6	Source	SG3	21502	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50-6	Pickup	SG3	21504 4230*	Float	4	R W	Amp	0.5-150 (5A CT) 0.1-30 (1A CT)	FGIMT
50-6	Time Delay	SG3	21506 4232*	Float	4	R W	Millisecond	0-60000	FGIMT
50-6	Direction	SG3	21508 4234*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-1	Mode	SG0	21510 4236*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-1	Source	SG0	21512	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-1	Pickup	SG0	21514 4238*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-1	Curve Index	SG0	21516 4240*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-1	Direction	SG0	21518 4242*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-1	Use Instantaneous Reset	SG0	21520 4244*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-1	Constant TD	SG0	21522 4246*	Float	4	R W	n/a	0–9.9	FGIMT
51-1	Constant A	SG0	21524 4248*	Float	4	R W	n/a	0–600	FGIMT
51-1	Constant B	SG0	21526 4250*	Float	4	R W	n/a	0–25	FGIMT
51-1	Constant C	SG0	21528 4252*	Float	4	R W	n/a	0–1	FGIMT
51-1	Constant N	SG0	21530 4254*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-1	Constant R	SG0	21532 4256*	Float	4	R W	n/a	0–30	FGIMT
51-1	Voltage Restraint Setpoint	SG0	21534 4258*	Float	4	R W	Volt	30–250	FGIMT
51-1	Voltage Restraint Mode	SG0	21536 4260*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-1	Mode	SG1	21538 4262*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-1	Source	SG1	21540	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-1	Pickup	SG1	21542 4264*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-1	Curve Index	SG1	21544 4266*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-1	Direction	SG1	21546 4268*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-1	Use Instantaneous Reset	SG1	21548 4270*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-1	Constant TD	SG1	21550 4272*	Float	4	R W	n/a	0–9.9	FGIMT
51-1	Constant A	SG1	21552 4274*	Float	4	R W	n/a	0–600	FGIMT
51-1	Constant B	SG1	21554 4276*	Float	4	R W	n/a	0–25	FGIMT
51-1	Constant C	SG1	21556 4278*	Float	4	R W	n/a	0–1	FGIMT
51-1	Constant N	SG1	21558 4280*	Float	4	R W	n/a	0.5–2.5	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-1	Constant R	SG1	21560 4282*	Float	4	R W	n/a	0–30	FGIMT
51-1	Voltage Restraint Setpoint	SG1	21562 4284*	Float	4	R W	Volt	30–250	FGIMT
51-1	Voltage Restraint Mode	SG1	21564 4286*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-1	Mode	SG2	21566 4288*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-1	Source	SG2	21568	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-1	Pickup	SG2	21570 4290*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-1	Curve Index	SG2	21572 4292*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-1	Direction	SG2	21574 4294*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-1	Use Instantaneous Reset	SG2	21576 4296*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-1	Constant TD	SG2	21578 4298*	Float	4	R W	n/a	0–9.9	FGIMT
51-1	Constant A	SG2	21580 4300*	Float	4	R W	n/a	0–600	FGIMT
51-1	Constant B	SG2	21582 4302*	Float	4	R W	n/a	0–25	FGIMT
51-1	Constant C	SG2	21584 4304*	Float	4	R W	n/a	0–1	FGIMT
51-1	Constant N	SG2	21586 4306*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-1	Constant R	SG2	21588 4308*	Float	4	R W	n/a	0–30	FGIMT
51-1	Voltage Restraint Setpoint	SG2	21590 4310*	Float	4	R W	Volt	30–250	FGIMT
51-1	Voltage Restraint Mode	SG2	21592 4312*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-1	Mode	SG3	21594 4314*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-1	Source	SG3	21596	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-1	Pickup	SG3	21598 4316*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-1	Curve Index	SG3	21600 4318*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-1	Direction	SG3	21602 4320*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-1	Use Instantaneous Reset	SG3	21604 4322*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-1	Constant TD	SG3	21606 4324*	Float	4	R W	n/a	0–9.9	FGIMT
51-1	Constant A	SG3	21608 4326*	Float	4	R W	n/a	0–600	FGIMT
51-1	Constant B	SG3	21610 4328*	Float	4	R W	n/a	0–25	FGIMT
51-1	Constant C	SG3	21612 4330*	Float	4	R W	n/a	0–1	FGIMT
51-1	Constant N	SG3	21614 4332*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-1	Constant R	SG3	21616 4334*	Float	4	R W	n/a	0–30	FGIMT
51-1	Voltage Restraint Setpoint	SG3	21618 4336*	Float	4	R W	Volt	30–250	FGIMT
51-1	Voltage Restraint Mode	SG3	21620 4338*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-2	Mode	SG0	21622 4340*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-2	Source	SG0	21624	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-2	Pickup	SG0	21626 4342*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-2	Curve Index	SG0	21628 4344*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-2	Direction	SG0	21630 4346*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-2	Use Instantaneous Reset	SG0	21632 4348*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-2	Constant TD	SG0	21634 4350*	Float	4	R W	n/a	0–9.9	FGIMT
51-2	Constant A	SG0	21636 4352*	Float	4	R W	n/a	0–600	FGIMT
51-2	Constant B	SG0	21638 4354*	Float	4	R W	n/a	0–25	FGIMT
51-2	Constant C	SG0	21640 4356*	Float	4	R W	n/a	0–1	FGIMT
51-2	Constant N	SG0	21642 4358*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-2	Constant R	SG0	21644 4360*	Float	4	R W	n/a	0–30	FGIMT
51-2	Voltage Restraint Setpoint	SG0	21646 4362*	Float	4	R W	Volt	30–250	FGIMT
51-2	Voltage Restraint Mode	SG0	21648 4364*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-2	Mode	SG1	21650 4366*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-2	Source	SG1	21652	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-2	Pickup	SG1	21654 4368*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-2	Curve Index	SG1	21656 4370*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-2	Direction	SG1	21658 4372*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-2	Use Instantaneous Reset	SG1	21660 4374*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-2	Constant TD	SG1	21662 4376*	Float	4	R W	n/a	0–9.9	FGIMT
51-2	Constant A	SG1	21664 4378*	Float	4	R W	n/a	0–600	FGIMT
51-2	Constant B	SG1	21666 4380*	Float	4	R W	n/a	0–25	FGIMT
51-2	Constant C	SG1	21668 4382*	Float	4	R W	n/a	0–1	FGIMT
51-2	Constant N	SG1	21670 4384*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-2	Constant R	SG1	21672 4386*	Float	4	R W	n/a	0–30	FGIMT
51-2	Voltage Restraint Setpoint	SG1	21674 4388*	Float	4	R W	Volt	30–250	FGIMT
51-2	Voltage Restraint Mode	SG1	21676 4390*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-2	Mode	SG2	21678 4392*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-2	Source	SG2	21680	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-2	Pickup	SG2	21682 4394*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-2	Curve Index	SG2	21684 4396*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-2	Direction	SG2	21686 4398*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-2	Use Instantaneous Reset	SG2	21688 4400*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-2	Constant TD	SG2	21690 4402*	Float	4	R W	n/a	0–9.9	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-2	Constant A	SG2	21692 4404*	Float	4	R W	n/a	0–600	FGIMT
51-2	Constant B	SG2	21694 4406*	Float	4	R W	n/a	0–25	FGIMT
51-2	Constant C	SG2	21696 4408*	Float	4	R W	n/a	0–1	FGIMT
51-2	Constant N	SG2	21698 4410*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-2	Constant R	SG2	21700 4412*	Float	4	R W	n/a	0–30	FGIMT
51-2	Voltage Restraint Setpoint	SG2	21702 4414*	Float	4	R W	Volt	30–250	FGIMT
51-2	Voltage Restraint Mode	SG2	21704 4416*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-2	Mode	SG3	21706 4418*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-2	Source	SG3	21708	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-2	Pickup	SG3	21710 4420*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-2	Curve Index	SG3	21712 4422*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-2	Direction	SG3	21714 4424*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-2	Use Instantaneous Reset	SG3	21716 4426*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-2	Constant TD	SG3	21718 4428*	Float	4	R W	n/a	0–9.9	FGIMT
51-2	Constant A	SG3	21720 4430*	Float	4	R W	n/a	0–600	FGIMT
51-2	Constant B	SG3	21722 4432*	Float	4	R W	n/a	0–25	FGIMT
51-2	Constant C	SG3	21724 4434*	Float	4	R W	n/a	0–1	FGIMT
51-2	Constant N	SG3	21726 4436*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-2	Constant R	SG3	21728 4438*	Float	4	R W	n/a	0–30	FGIMT
51-2	Voltage Restraint Setpoint	SG3	21730 4440*	Float	4	R W	Volt	30–250	FGIMT
51-2	Voltage Restraint Mode	SG3	21732 4442*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-3	Mode	SG0	21734 4444*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-3	Source	SG0	21736	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-3	Pickup	SG0	21738 4446*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-3	Curve Index	SG0	21740 4448*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-3	Direction	SG0	21742 4450*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-3	Use Instantaneous Reset	SG0	21744 4452*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-3	Constant TD	SG0	21746 4454*	Float	4	R W	n/a	0–9.9	FGIMT
51-3	Constant A	SG0	21748 4456*	Float	4	R W	n/a	0–600	FGIMT
51-3	Constant B	SG0	21750 4458*	Float	4	R W	n/a	0–25	FGIMT
51-3	Constant C	SG0	21752 4460*	Float	4	R W	n/a	0–1	FGIMT
51-3	Constant N	SG0	21754 4462*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-3	Constant R	SG0	21756 4464*	Float	4	R W	n/a	0–30	FGIMT
51-3	Voltage Restraint Setpoint	SG0	21758 4466*	Float	4	R W	Volt	30–250	FGIMT
51-3	Voltage Restraint Mode	SG0	21760 4468*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-3	Mode	SG1	21762 4470*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-3	Source	SG1	21764	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-3	Pickup	SG1	21766 4472*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-3	Curve Index	SG1	21768 4474*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-3	Direction	SG1	21770 4476*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-3	Use Instantaneous Reset	SG1	21772 4478*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-3	Constant TD	SG1	21774 4480*	Float	4	R W	n/a	0–9.9	FGIMT
51-3	Constant A	SG1	21776 4482*	Float	4	R W	n/a	0–600	FGIMT
51-3	Constant B	SG1	21778 4484*	Float	4	R W	n/a	0–25	FGIMT
51-3	Constant C	SG1	21780 4486*	Float	4	R W	n/a	0–1	FGIMT
51-3	Constant N	SG1	21782 4488*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-3	Constant R	SG1	21784 4490*	Float	4	R W	n/a	0–30	FGIMT
51-3	Voltage Restraint Setpoint	SG1	21786 4492*	Float	4	R W	Volt	30–250	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-3	Voltage Restraint Mode	SG1	21788 4494*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-3	Mode	SG2	21790 4496*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-3	Source	SG2	21792	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-3	Pickup	SG2	21794 4498*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-3	Curve Index	SG2	21796 4500*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-3	Direction	SG2	21798 4502*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-3	Use Instantaneous Reset	SG2	21800 4504*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-3	Constant TD	SG2	21802 4506*	Float	4	R W	n/a	0–9.9	FGIMT
51-3	Constant A	SG2	21804 4508*	Float	4	R W	n/a	0–600	FGIMT
51-3	Constant B	SG2	21806 4510*	Float	4	R W	n/a	0–25	FGIMT
51-3	Constant C	SG2	21808 4512*	Float	4	R W	n/a	0–1	FGIMT
51-3	Constant N	SG2	21810 4514*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-3	Constant R	SG2	21812 4516*	Float	4	R W	n/a	0–30	FGIMT
51-3	Voltage Restraint Setpoint	SG2	21814 4518*	Float	4	R W	Volt	30–250	FGIMT
51-3	Voltage Restraint Mode	SG2	21816 4520*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-3	Mode	SG3	21818 4522*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-3	Source	SG3	21820	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-3	Pickup	SG3	21822 4524*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-3	Curve Index	SG3	21824 4526*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-3	Direction	SG3	21826 4528*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-3	Use Instantaneous Reset	SG3	21828 4530*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-3	Constant TD	SG3	21830 4532*	Float	4	R W	n/a	0–9.9	FGIMT
51-3	Constant A	SG3	21832 4534*	Float	4	R W	n/a	0–600	FGIMT
51-3	Constant B	SG3	21834 4536*	Float	4	R W	n/a	0–25	FGIMT
51-3	Constant C	SG3	21836 4538*	Float	4	R W	n/a	0–1	FGIMT
51-3	Constant N	SG3	21838 4540*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-3	Constant R	SG3	21840 4542*	Float	4	R W	n/a	0–30	FGIMT
51-3	Voltage Restraint Setpoint	SG3	21842 4544*	Float	4	R W	Volt	30–250	FGIMT
51-3	Voltage Restraint Mode	SG3	21844 4546*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-4	Mode	SG0	21846 4548*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-4	Source	SG0	21848	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-4	Pickup	SG0	21850 4550*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-4	Curve Index	SG0	21852 4552*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-4	Direction	SG0	21854 4554*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-4	Use Instantaneous Reset	SG0	21856 4556*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-4	Constant TD	SG0	21858 4558*	Float	4	R W	n/a	0–9.9	FGIMT
51-4	Constant A	SG0	21860 4560*	Float	4	R W	n/a	0–600	FGIMT
51-4	Constant B	SG0	21862 4562*	Float	4	R W	n/a	0–25	FGIMT
51-4	Constant C	SG0	21864 4564*	Float	4	R W	n/a	0–1	FGIMT
51-4	Constant N	SG0	21866 4566*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-4	Constant R	SG0	21868 4568*	Float	4	R W	n/a	0–30	FGIMT
51-4	Voltage Restraint Setpoint	SG0	21870 4570*	Float	4	R W	Volt	30–250	FGIMT
51-4	Voltage Restraint Mode	SG0	21872 4572*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-4	Mode	SG1	21874 4574*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-4	Source	SG1	21876	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-4	Pickup	SG1	21878 4576*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-4	Curve Index	SG1	21880 4578*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-4	Direction	SG1	21882 4580*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-4	Use Instantaneous Reset	SG1	21884 4582*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-4	Constant TD	SG1	21886 4584*	Float	4	R W	n/a	0–9.9	FGIMT
51-4	Constant A	SG1	21888 4586*	Float	4	R W	n/a	0–600	FGIMT
51-4	Constant B	SG1	21890 4588*	Float	4	R W	n/a	0–25	FGIMT
51-4	Constant C	SG1	21892 4590*	Float	4	R W	n/a	0–1	FGIMT
51-4	Constant N	SG1	21894 4592*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-4	Constant R	SG1	21896 4594*	Float	4	R W	n/a	0–30	FGIMT
51-4	Voltage Restraint Setpoint	SG1	21898 4596*	Float	4	R W	Volt	30–250	FGIMT
51-4	Voltage Restraint Mode	SG1	21900 4598*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-4	Mode	SG2	21902 4600*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-4	Source	SG2	21904	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-4	Pickup	SG2	21906 4602*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-4	Curve Index	SG2	21908 4604*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-4	Direction	SG2	21910 4606*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-4	Use Instantaneous Reset	SG2	21912 4608*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-4	Constant TD	SG2	21914 4610*	Float	4	R W	n/a	0–9.9	FGIMT
51-4	Constant A	SG2	21916 4612*	Float	4	R W	n/a	0–600	FGIMT
51-4	Constant B	SG2	21918 4614*	Float	4	R W	n/a	0–25	FGIMT
51-4	Constant C	SG2	21920 4616*	Float	4	R W	n/a	0–1	FGIMT
51-4	Constant N	SG2	21922 4618*	Float	4	R W	n/a	0.5–2.5	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-4	Constant R	SG2	21924 4620*	Float	4	R W	n/a	0-30	FGIMT
51-4	Voltage Restraint Setpoint	SG2	21926 4622*	Float	4	R W	Volt	30-250	FGIMT
51-4	Voltage Restraint Mode	SG2	21928 4624*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-4	Mode	SG3	21930 4626*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-4	Source	SG3	21932	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-4	Pickup	SG3	21934 4628*	Float	4	R W	Amp	0.5-16 (5A CT) 0.1-3.2 (1A CT)	FGIMT
51-4	Curve Index	SG3	21936 4630*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-4	Direction	SG3	21938 4632*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-4	Use Instantaneous Reset	SG3	21940 4634*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-4	Constant TD	SG3	21942 4636*	Float	4	R W	n/a	0-9.9	FGIMT
51-4	Constant A	SG3	21944 4638*	Float	4	R W	n/a	0-600	FGIMT
51-4	Constant B	SG3	21946 4640*	Float	4	R W	n/a	0-25	FGIMT
51-4	Constant C	SG3	21948 4642*	Float	4	R W	n/a	0-1	FGIMT
51-4	Constant N	SG3	21950 4644*	Float	4	R W	n/a	0.5-2.5	FGIMT
51-4	Constant R	SG3	21952 4646*	Float	4	R W	n/a	0-30	FGIMT
51-4	Voltage Restraint Setpoint	SG3	21954 4648*	Float	4	R W	Volt	30-250	FGIMT
51-4	Voltage Restraint Mode	SG3	21956 4650*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-5	Mode	SG0	21958 4652*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-5	Source	SG0	21960	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-5	Pickup	SG0	21962 4654*	Float	4	R W	Amp	0.5-16 (5A CT) 0.1-3.2 (1A CT)	FGIMT
51-5	Curve Index	SG0	21964 4656*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-5	Direction	SG0	21966 4658*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-5	Use Instantaneous Reset	SG0	21968 4660*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-5	Constant TD	SG0	21970 4662*	Float	4	R W	n/a	0–9.9	FGIMT
51-5	Constant A	SG0	21972 4664*	Float	4	R W	n/a	0–600	FGIMT
51-5	Constant B	SG0	21974 4666*	Float	4	R W	n/a	0–25	FGIMT
51-5	Constant C	SG0	21976 4668*	Float	4	R W	n/a	0–1	FGIMT
51-5	Constant N	SG0	21978 4670*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-5	Constant R	SG0	21980 4672*	Float	4	R W	n/a	0–30	FGIMT
51-5	Voltage Restraint Setpoint	SG0	21982 4674*	Float	4	R W	Volt	30–250	FGIMT
51-5	Voltage Restraint Mode	SG0	21984 4676*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-5	Mode	SG1	21986 4678*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-5	Source	SG1	21988	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-5	Pickup	SG1	21990 4680*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-5	Curve Index	SG1	21992 4682*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-5	Direction	SG1	21994 4684*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-5	Use Instantaneous Reset	SG1	21996 4686*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-5	Constant TD	SG1	21998 4688*	Float	4	R W	n/a	0–9.9	FGIMT
51-5	Constant A	SG1	22000 4690*	Float	4	R W	n/a	0–600	FGIMT
51-5	Constant B	SG1	22002 4692*	Float	4	R W	n/a	0–25	FGIMT
51-5	Constant C	SG1	22004 4694*	Float	4	R W	n/a	0–1	FGIMT
51-5	Constant N	SG1	22006 4696*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-5	Constant R	SG1	22008 4698*	Float	4	R W	n/a	0–30	FGIMT
51-5	Voltage Restraint Setpoint	SG1	22010 4700*	Float	4	R W	Volt	30–250	FGIMT
51-5	Voltage Restraint Mode	SG1	22012 4702*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-5	Mode	SG2	22014 4704*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-5	Source	SG2	22016	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-5	Pickup	SG2	22018 4706*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-5	Curve Index	SG2	22020 4708*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-5	Direction	SG2	22022 4710*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-5	Use Instantaneous Reset	SG2	22024 4712*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-5	Constant TD	SG2	22026 4714*	Float	4	R W	n/a	0–9.9	FGIMT
51-5	Constant A	SG2	22028 4716*	Float	4	R W	n/a	0–600	FGIMT
51-5	Constant B	SG2	22030 4718*	Float	4	R W	n/a	0–25	FGIMT
51-5	Constant C	SG2	22032 4720*	Float	4	R W	n/a	0–1	FGIMT
51-5	Constant N	SG2	22034 4722*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-5	Constant R	SG2	22036 4724*	Float	4	R W	n/a	0–30	FGIMT
51-5	Voltage Restraint Setpoint	SG2	22038 4726*	Float	4	R W	Volt	30–250	FGIMT
51-5	Voltage Restraint Mode	SG2	22040 4728*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-5	Mode	SG3	22042 4730*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIMT
51-5	Source	SG3	22044	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
51-5	Pickup	SG3	22046 4732*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIMT
51-5	Curve Index	SG3	22048 4734*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIMT
51-5	Direction	SG3	22050 4736*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-5	Use Instantaneous Reset	SG3	22052 4738*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIMT
51-5	Constant TD	SG3	22054 4740*	Float	4	R W	n/a	0–9.9	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-5	Constant A	SG3	22056 4742*	Float	4	R W	n/a	0–600	FGIMT
51-5	Constant B	SG3	22058 4744*	Float	4	R W	n/a	0–25	FGIMT
51-5	Constant C	SG3	22060 4746*	Float	4	R W	n/a	0–1	FGIMT
51-5	Constant N	SG3	22062 4748*	Float	4	R W	n/a	0.5–2.5	FGIMT
51-5	Constant R	SG3	22064 4750*	Float	4	R W	n/a	0–30	FGIMT
51-5	Voltage Restraint Setpoint	SG3	22066 4752*	Float	4	R W	Volt	30–250	FGIMT
51-5	Voltage Restraint Mode	SG3	22068 4754*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIMT
51-6	Mode	SG0	22070 4756*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-6	Source	SG0	22072	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-6	Pickup	SG0	22074 4758*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-6	Curve Index	SG0	22076 4760*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-6	Direction	SG0	22078 4762*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-6	Use Instantaneous Reset	SG0	22080 4764*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-6	Constant TD	SG0	22082 4766*	Float	4	R W	n/a	0–9.9	FGIT
51-6	Constant A	SG0	22084 4768*	Float	4	R W	n/a	0–600	FGIT
51-6	Constant B	SG0	22086 4770*	Float	4	R W	n/a	0–25	FGIT
51-6	Constant C	SG0	22088 4772*	Float	4	R W	n/a	0–1	FGIT
51-6	Constant N	SG0	22090 4774*	Float	4	R W	n/a	0.5–2.5	FGIT
51-6	Constant R	SG0	22092 4776*	Float	4	R W	n/a	0–30	FGIT
51-6	Voltage Restraint Setpoint	SG0	22094 4778*	Float	4	R W	Volt	30–250	FGIT
51-6	Voltage Restraint Mode	SG0	22096 4780*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-6	Mode	SG1	22098 4782*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-6	Source	SG1	22100	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-6	Pickup	SG1	22102 4784*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-6	Curve Index	SG1	22104 4786*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-6	Direction	SG1	22106 4788*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-6	Use Instantaneous Reset	SG1	22108 4790*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-6	Constant TD	SG1	22110 4792*	Float	4	R W	n/a	0–9.9	FGIT
51-6	Constant A	SG1	22112 4794*	Float	4	R W	n/a	0–600	FGIT
51-6	Constant B	SG1	22114 4796*	Float	4	R W	n/a	0–25	FGIT
51-6	Constant C	SG1	22116 4798*	Float	4	R W	n/a	0–1	FGIT
51-6	Constant N	SG1	22118 4800*	Float	4	R W	n/a	0.5–2.5	FGIT
51-6	Constant R	SG1	22120 4802*	Float	4	R W	n/a	0–30	FGIT
51-6	Voltage Restraint Setpoint	SG1	22122 4804*	Float	4	R W	Volt	30–250	FGIT
51-6	Voltage Restraint Mode	SG1	22124 4806*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-6	Mode	SG2	22126 4808*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-6	Source	SG2	22128	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-6	Pickup	SG2	22130 4810*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-6	Curve Index	SG2	22132 4812*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-6	Direction	SG2	22134 4814*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-6	Use Instantaneous Reset	SG2	22136 4816*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-6	Constant TD	SG2	22138 4818*	Float	4	R W	n/a	0–9.9	FGIT
51-6	Constant A	SG2	22140 4820*	Float	4	R W	n/a	0–600	FGIT
51-6	Constant B	SG2	22142 4822*	Float	4	R W	n/a	0–25	FGIT
51-6	Constant C	SG2	22144 4824*	Float	4	R W	n/a	0–1	FGIT
51-6	Constant N	SG2	22146 4826*	Float	4	R W	n/a	0.5–2.5	FGIT
51-6	Constant R	SG2	22148 4828*	Float	4	R W	n/a	0–30	FGIT
51-6	Voltage Restraint Setpoint	SG2	22150 4830*	Float	4	R W	Volt	30–250	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-6	Voltage Restraint Mode	SG2	22152 4832*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-6	Mode	SG3	22154 4834*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-6	Source	SG3	22156	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-6	Pickup	SG3	22158 4836*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-6	Curve Index	SG3	22160 4838*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-6	Direction	SG3	22162 4840*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-6	Use Instantaneous Reset	SG3	22164 4842*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-6	Constant TD	SG3	22166 4844*	Float	4	R W	n/a	0–9.9	FGIT
51-6	Constant A	SG3	22168 4846*	Float	4	R W	n/a	0–600	FGIT
51-6	Constant B	SG3	22170 4848*	Float	4	R W	n/a	0–25	FGIT
51-6	Constant C	SG3	22172 4850*	Float	4	R W	n/a	0–1	FGIT
51-6	Constant N	SG3	22174 4852*	Float	4	R W	n/a	0.5–2.5	FGIT
51-6	Constant R	SG3	22176 4854*	Float	4	R W	n/a	0–30	FGIT
51-6	Voltage Restraint Setpoint	SG3	22178 4856*	Float	4	R W	Volt	30–250	FGIT
51-6	Voltage Restraint Mode	SG3	22180 4858*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-7	Mode	SG0	22182 4860*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-7	Source	SG0	22184	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-7	Pickup	SG0	22186 4862*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-7	Curve Index	SG0	22188 4864*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-7	Direction	SG0	22190 4866*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-7	Use Instantaneous Reset	SG0	22092 4868*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-7	Constant TD	SG0	22194 4870*	Float	4	R W	n/a	0–9.9	FGIT
51-7	Constant A	SG0	22196 4872*	Float	4	R W	n/a	0–600	FGIT
51-7	Constant B	SG0	22198 4874*	Float	4	R W	n/a	0–25	FGIT
51-7	Constant C	SG0	22200 4876*	Float	4	R W	n/a	0–1	FGIT
51-7	Constant N	SG0	22202 4878*	Float	4	R W	n/a	0.5–2.5	FGIT
51-7	Constant R	SG0	22204 4880*	Float	4	R W	n/a	0–30	FGIT
51-7	Voltage Restraint Setpoint	SG0	22206 4882*	Float	4	R W	Volt	30–250	FGIT
51-7	Voltage Restraint Mode	SG0	22208 4884*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-7	Mode	SG1	22210 4886*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-7	Source	SG1	22212	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-7	Pickup	SG1	22214 4888*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-7	Curve Index	SG1	22216 4890*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-7	Direction	SG1	22218 4892*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-7	Use Instantaneous Reset	SG1	22220 4894*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-7	Constant TD	SG1	22222 4896*	Float	4	R W	n/a	0–9.9	FGIT
51-7	Constant A	SG1	22224 4898*	Float	4	R W	n/a	0–600	FGIT
51-7	Constant B	SG1	22226 4900*	Float	4	R W	n/a	0–25	FGIT
51-7	Constant C	SG1	22228 4902*	Float	4	R W	n/a	0–1	FGIT
51-7	Constant N	SG1	22230 4904*	Float	4	R W	n/a	0.5–2.5	FGIT
51-7	Constant R	SG1	22232 4906*	Float	4	R W	n/a	0–30	FGIT
51-7	Voltage Restraint Setpoint	SG1	22234 4908*	Float	4	R W	Volt	30–250	FGIT
51-7	Voltage Restraint Mode	SG1	22236 4910*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-7	Mode	SG2	22238 4912*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-7	Source	SG2	22240	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-7	Pickup	SG2	22242 4914*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-7	Curve Index	SG2	22244 4916*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-7	Direction	SG2	22246 4918*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-7	Use Instantaneous Reset	SG2	22248 4920*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-7	Constant TD	SG2	22250 4922*	Float	4	R W	n/a	0–9.9	FGIT
51-7	Constant A	SG2	22252 4924*	Float	4	R W	n/a	0–600	FGIT
51-7	Constant B	SG2	22254 4926*	Float	4	R W	n/a	0–25	FGIT
51-7	Constant C	SG2	22256 4928*	Float	4	R W	n/a	0–1	FGIT
51-7	Constant N	SG2	22258 4930*	Float	4	R W	n/a	0.5–2.5	FGIT
51-7	Constant R	SG2	22260 4932*	Float	4	R W	n/a	0–30	FGIT
51-7	Voltage Restraint Setpoint	SG2	22262 4934*	Float	4	R W	Volt	30–250	FGIT
51-7	Voltage Restraint Mode	SG2	22264 4936*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51-7	Mode	SG3	22266 4938*	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	FGIT
51-7	Source	SG3	22268	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
51-7	Pickup	SG3	22270 4940*	Float	4	R W	Amp	0.5–16 (5A CT) 0.1–3.2 (1A CT)	FGIT
51-7	Curve Index	SG3	22272 4942*	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	FGIT
51-7	Direction	SG3	22274 4944*	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	FGIT
51-7	Use Instantaneous Reset	SG3	22276 4946*	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	FGIT
51-7	Constant TD	SG3	22278 4948*	Float	4	R W	n/a	0–9.9	FGIT
51-7	Constant A	SG3	22280 4950*	Float	4	R W	n/a	0–600	FGIT
51-7	Constant B	SG3	22282 4952*	Float	4	R W	n/a	0–25	FGIT
51-7	Constant C	SG3	22284 4954*	Float	4	R W	n/a	0–1	FGIT
51-7	Constant N	SG3	22286 4956*	Float	4	R W	n/a	0.5–2.5	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-7	Constant R	SG3	22288 4958*	Float	4	R W	n/a	0-30	FGIT
51-7	Voltage Restraint Setpoint	SG3	22290 4960*	Float	4	R W	Volt	30-250	FGIT
51-7	Voltage Restraint Mode	SG3	22292 4962*	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	FGIT
51 Table Curves	Input Trip Mop Point	GG	22294 4964*	Float	4	R W	n/a	0-40	FGIMT
51 Table Curves	Input Trip Time Point	GG	22296 4966*	Float	4	R W	n/a	0-9999999	FGIMT
51 Table Curves	Input Reset Mop Point	GG	22298 4968*	Float	4	R W	n/a	0-1	FGIMT
51 Table Curves	Input Reset Time Point	GG	22300 4970*	Float	4	R W	n/a	0-9999999	FGIMT
51 Table Curves	Start Curve Input	GG	22302 4972*	Uint32	4	R W	n/a	1-4	FGIMT
51 Table Curves	Start Curve Output	GG	22304 4974*	Uint32	4	R W	n/a	1-4	FGIMT
32-1	Mode	SG0	22306 4976*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGIM
32-1	Mode	SG0	22306 4976*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22308- 09						
32-1	Pickup	SG0	22310 4978*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGIM
32-1	Pickup	SG0	22310 4978*	Float	4	R W	System Watt	50-1875000	D
32-1	Time Delay	SG0	22312 4980*	Float	4	R W	Millisecond	50-600000	DFGIM
32-1	Over Under	SG0	22314 4982*	Uint32	4	R W	n/a	Over=0 Under=1	DFGIM
32-1	Direction	SG0	22316 4984*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGIM
32-1	Mode	SG1	22318 4986*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGIM
32-1	Mode	SG1	22318 4986*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22320- 21						
32-1	Pickup	SG1	22322 4988*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGIM
32-1	Pickup	SG1	22322 4988*	Float	4	R W	System Watt	50-1875000	D
32-1	Time Delay	SG1	22324 4990*	Float	4	R W	Millisecond	50-600000	DFGIM
32-1	Over Under	SG1	22326 4992*	Uint32	4	R W	n/a	Over=0 Under=1	DFGIM
32-1	Direction	SG1	22328 4994*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGIM
32-1	Mode	SG2	22330 4996*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGIM
32-1	Mode	SG2	22330 4996*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22332- 33						
32-1	Pickup	SG2	22334 4998*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGIM
32-1	Pickup	SG2	22334 4998*	Float	4	R W	System Watt	50-1875000	D
32-1	Time Delay	SG2	22336 5000*	Float	4	R W	Millisecond	50-600000	DFGIM
32-1	Over Under	SG2	22338 5002*	Uint32	4	R W	n/a	Over=0 Under=1	DFGIM
32-1	Direction	SG2	22340 5004*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGIM

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
32-1	Mode	SG3	22342 5006*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGIM
32-1	Mode	SG3	22342 5006*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22344- 45						
32-1	Pickup	SG3	22346 5008*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGIM
32-1	Pickup	SG3	22346 5008*	Float	4	R W	System Watt	50-1875000	D
32-1	Time Delay	SG3	22348 5010*	Float	4	R W	Millisecond	50-600000	DFGIM
32-1	Over Under	SG3	22350 5012*	Uint32	4	R W	n/a	Over=0 Under=1	DFGIM
32-1	Direction	SG3	22352 5014*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGIM
32-2	Mode	SG0	22354 5016*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGI
32-2	Mode	SG0	22354 5016*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22356- 57						
32-2	Pickup	SG0	22358 5018*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGI
32-2	Pickup	SG0	22358 5018*	Float	4	R W	System Watt	50-1875000	D
32-2	Time Delay	SG0	22360 5020*	Float	4	R W	Millisecond	50-600000	DFGI
32-2	Over Under	SG0	22362 5022*	Uint32	4	R W	n/a	Over=0 Under=1	DFGI
32-2	Direction	SG0	22364 5024*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGI
32-2	Mode	SG1	22366 5026*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGI
32-2	Mode	SG1	22366 5026*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22368- 69						
32-2	Pickup	SG1	22370 5028*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGI
32-2	Pickup	SG1	22370 5028*	Float	4	R W	System Watt	50-1875000	D
32-2	Time Delay	SG1	22372 5030*	Float	4	R W	Millisecond	50-600000	DFGI
32-2	Over Under	SG1	22374 5032*	Uint32	4	R W	n/a	Over=0 Under=1	DFGI
32-2	Direction	SG1	22376 5034*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGI
32-2	Mode	SG2	22378 5036*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGI
32-2	Mode	SG2	22378 5036*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22380- 81						
32-2	Pickup	SG2	22382 5038*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGI
32-2	Pickup	SG2	22382 5038*	Float	4	R W	System Watt	50-1875000	D
32-2	Time Delay	SG2	22384 5040*	Float	4	R W	Millisecond	50-600000	DFGI
32-2	Over Under	SG2	22386 5042*	Uint32	4	R W	n/a	Over=0 Under=1	DFGI
32-2	Direction	SG2	22388 5044*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGI
32-2	Mode	SG3	22390 5046*	Uint32	4	R W	n/a	Disabled=0 One of Three=1 Two of Three=2 Three of Three=3 Total Power=4	FGI

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
32-2	Mode	SG3	22390 5046*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			22392- 93						
32-2	Pickup	SG3	22394 5048*	Float	4	R W	Watt	1-6000 (5A CT) 1-1200 (1A CT)	FGI
32-2	Pickup	SG3	22394 5048*	Float	4	R W	System Watt	50-1875000	D
32-2	Time Delay	SG3	22396 5050*	Float	4	R W	Millisecond	50-600000	DFGI
32-2	Over Under	SG3	22398 5052*	Uint32	4	R W	n/a	Over=0 Under=1	DFGI
32-2	Direction	SG3	22400 5054*	Uint32	4	R W	n/a	Forward=0 Reverse=1	DFGI
40Z	Mode	SG0	22402 5056*	Uint32	4	R W	n/a	Disabled=0 Non-VC=1 VC=2 Both=3	G
Reserved			22404- 05						
40Z	Offset 1	SG0	22406 5058*	Float	4	R W	Ohm	0-110	G
40Z	Diameter 1	SG0	22408 5060*	Float	4	R W	Ohm	0.1-100	G
40Z	Time Delay 1	SG0	22410 5062*	Float	4	R W	Millisecond	0-300000	G
40Z	Voltage Pickup 1	SG0	22412 5064*	Float	4	R W	Volt	5-180	G
40Z	Voltage Time Delay 1	SG0	22414 5066*	Float	4	R W	Millisecond	0-300000	G
40Z	Offset 2	SG0	22416 5068*	Float	4	R W	Ohm	0-110	G
40Z	Diameter 2	SG0	22418 5070*	Float	4	R W	Ohm	0.1-100	G
40Z	Time Delay 2	SG0	22420 5072*	Float	4	R W	Millisecond	0-300000	G
40Z	Voltage Pickup 2	SG0	22422 5074*	Float	4	R W	Volt	5-180	G
40Z	Voltage Time Delay 2	SG0	22424 5076*	Float	4	R W	Millisecond	0-300000	G
40Z	Torque Angle Z1	SG0	22426 5078*	Float	4	R W	Degree	(-90)-0	G
40Z	Mode	SG1	22428 5080*	Uint32	4	R W	n/a	Disabled=0 Non-VC=1 VC=2 Both=3	G
Reserved			22430- 31						
40Z	Offset 1	SG1	22432 5082*	Float	4	R W	Ohm	0-110	G
40Z	Diameter 1	SG1	22434 5084*	Float	4	R W	Ohm	0.1-100	G
40Z	Time Delay 1	SG1	22436 5086*	Float	4	R W	Millisecond	0-300000	G
40Z	Voltage Pickup 1	SG1	22438 5088*	Float	4	R W	Volt	5-180	G
40Z	Voltage Time Delay 1	SG1	22440 5090*	Float	4	R W	Millisecond	0-300000	G
40Z	Offset 2	SG1	22442 5092*	Float	4	R W	Ohm	0-110	G
40Z	Diameter 2	SG1	22444 5094*	Float	4	R W	Ohm	0.1-100	G
40Z	Time Delay 2	SG1	22446 5096*	Float	4	R W	Millisecond	0-300000	G
40Z	Voltage Pickup 2	SG1	22448 5098*	Float	4	R W	Volt	5-180	G
40Z	Voltage Time Delay 2	SG1	22450 5100*	Float	4	R W	Millisecond	0-300000	G
40Z	Torque Angle Z1	SG1	22452 5102*	Float	4	R W	Degree	(-90)-0	G
40Z	Mode	SG2	22454 5104*	Uint32	4	R W	n/a	Disabled=0 Non-VC=1 VC=2 Both=3	G
Reserved			22456- 57						
40Z	Offset 1	SG2	22458 5106*	Float	4	R W	Ohm	0-110	G
40Z	Diameter 1	SG2	22460 5108*	Float	4	R W	Ohm	0.1-100	G
40Z	Time Delay 1	SG2	22462 5110*	Float	4	R W	Millisecond	0-300000	G

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
40Z	Voltage Pickup 1	SG2	22464 5112*	Float	4	R W	Volt	5–180	G
40Z	Voltage Time Delay 1	SG2	22466 5114*	Float	4	R W	Millisecond	0–300000	G
40Z	Offset 2	SG2	22468 5116*	Float	4	R W	Ohm	0–110	G
40Z	Diameter 2	SG2	22470 5118*	Float	4	R W	Ohm	0.1–100	G
40Z	Time Delay 2	SG2	22472 5120*	Float	4	R W	Millisecond	0–300000	G
40Z	Voltage Pickup 2	SG2	22474 5122*	Float	4	R W	Volt	5–180	G
40Z	Voltage Time Delay 2	SG2	22476 5124*	Float	4	R W	Millisecond	0–300000	G
40Z	Torque Angle Z1	SG2	22478 5126*	Float	4	R W	Degree	(-90)–0	G
40Z	Mode	SG3	22480 5128*	Uint32	4	R W	n/a	Disabled=0 Non-VC=1 VC=2 Both=3	G
Reserved			22482- 83						
40Z	Offset 1	SG3	22484 5130*	Float	4	R W	Ohm	0–110	G
40Z	Diameter 1	SG3	22486 5132*	Float	4	R W	Ohm	0.1–100	G
40Z	Time Delay 1	SG3	22488 5134*	Float	4	R W	Millisecond	0–300000	G
40Z	Voltage Pickup 1	SG3	22490 5136*	Float	4	R W	Volt	5–180	G
40Z	Voltage Time Delay 1	SG3	22492 5138*	Float	4	R W	Millisecond	0–300000	G
40Z	Offset 2	SG3	22494 5140*	Float	4	R W	Ohm	0–110	G
40Z	Diameter 2	SG3	22496 5142*	Float	4	R W	Ohm	0.1–100	G
40Z	Time Delay 2	SG3	22498 5144*	Float	4	R W	Millisecond	0–300000	G
40Z	Voltage Pickup 2	SG3	22500 5146*	Float	4	R W	Volt	5–180	G
40Z	Voltage Time Delay 2	SG3	22502 5148*	Float	4	R W	Millisecond	0–300000	G
40Z	Torque Angle Z1	SG3	22504 5150*	Float	4	R W	Degree	(-90)–0	G
40Q	Mode	SG0	22506 5152*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GM
40Q	Pickup	SG0	22508 5154*	Float	4	R W	Var	1–1200	GM
40Q	Time Delay	SG0	22510 5156*	Float	4	R W	Millisecond	1–600000	GM
Reserved			22512- 13						
40Q	Mode	SG1	22514 5158*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GM
40Q	Pickup	SG1	22516 5160*	Float	4	R W	Var	1–1200	GM
40Q	Time Delay	SG1	22518 5162*	Float	4	R W	Millisecond	1–600000	GM
Reserved			22520- 21						
40Q	Mode	SG2	22522 5164*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GM
40Q	Pickup	SG2	22524 5166*	Float	4	R W	Var	1–1200	GM
40Q	Time Delay	SG2	22526 5168*	Float	4	R W	Millisecond	1–600000	GM
Reserved			22528- 29						
40Q	Mode	SG3	22530 5170*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GM
40Q	Pickup	SG3	22532 5172*	Float	4	R W	Var	1–1200	GM
40Q	Time Delay	SG3	22534 5174*	Float	4	R W	Millisecond	1–600000	GM
Reserved			22536- 37						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-1	Mode	SG0	22538 5176*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-1	Pickup	SG0	22540 5178*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Time Delay	SG0	22542 5180*	Float	4	R W	Millisecond	0-600000	FGIMT
81-1	Voltage Inhibit	SG0	22544 5182*	Float	4	R W	Volt	15-250	FGIMT
81-1	Negative Sequence Inhibit	SG0	22546 5184*	Float	4	R W	Percent	0-99	FGIMT
81-1	Over Frequency	SG0	22548 5186*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Under Frequency	SG0	22550 5188*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Mode	SG1	22552 5190*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-1	Pickup	SG1	22554 5192*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Time Delay	SG1	22556 5194*	Float	4	R W	Millisecond	0-600000	FGIMT
81-1	Voltage Inhibit	SG1	22558 5196*	Float	4	R W	Volt	15-250	FGIMT
81-1	Negative Sequence Inhibit	SG1	22560 5198*	Float	4	R W	Percent	0-99	FGIMT
81-1	Over Frequency	SG1	22562 5200*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Under Frequency	SG1	22564 5202*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Mode	SG2	22566 5204*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-1	Pickup	SG2	22568 5206*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Time Delay	SG2	22570 5208*	Float	4	R W	Millisecond	0-600000	FGIMT
81-1	Voltage Inhibit	SG2	22572 5210*	Float	4	R W	Volt	15-250	FGIMT
81-1	Negative Sequence Inhibit	SG2	22574 5212*	Float	4	R W	Percent	0-99	FGIMT
81-1	Over Frequency	SG2	22576 5214*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Under Frequency	SG2	22578 5216*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Mode	SG3	22580 5218*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-1	Pickup	SG3	22582 5220*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Time Delay	SG3	22584 5222*	Float	4	R W	Millisecond	0-600000	FGIMT
81-1	Voltage Inhibit	SG3	22586 5224*	Float	4	R W	Volt	15-250	FGIMT
81-1	Negative Sequence Inhibit	SG3	22588 5226*	Float	4	R W	Percent	0-99	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-1	Over Frequency	SG3	22590 5228*	Float	4	R W	Hertz	15-110	FGIMT
81-1	Under Frequency	SG3	22592 5230*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Mode	SG0	22594 5232*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-2	Pickup	SG0	22596 5234*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Time Delay	SG0	22598 5236*	Float	4	R W	Millisecond	0-600000	FGIMT
81-2	Voltage Inhibit	SG0	22600 5238*	Float	4	R W	Volt	15-250	FGIMT
81-2	Negative Sequence Inhibit	SG0	22602 5240*	Float	4	R W	Percent	0-99	FGIMT
81-2	Over Frequency	SG0	22604 5242*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Under Frequency	SG0	22606 5244*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Mode	SG1	22608 5246*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-2	Pickup	SG1	22610 5248*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Time Delay	SG1	22612 5250*	Float	4	R W	Millisecond	0-600000	FGIMT
81-2	Voltage Inhibit	SG1	22614 5252*	Float	4	R W	Volt	15-250	FGIMT
81-2	Negative Sequence Inhibit	SG1	22616 5254*	Float	4	R W	Percent	0-99	FGIMT
81-2	Over Frequency	SG1	22618 5256*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Under Frequency	SG1	22620 5258*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Mode	SG2	22622 5260*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-2	Pickup	SG2	22624 5262*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Time Delay	SG2	22626 5264*	Float	4	R W	Millisecond	0-600000	FGIMT
81-2	Voltage Inhibit	SG2	22628 5266*	Float	4	R W	Volt	15-250	FGIMT
81-2	Negative Sequence Inhibit	SG2	22630 5268*	Float	4	R W	Percent	0-99	FGIMT
81-2	Over Frequency	SG2	22632 5270*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Under Frequency	SG2	22634 5272*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Mode	SG3	22636 5274*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-2	Pickup	SG3	22638 5276*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Time Delay	SG3	22640 5278*	Float	4	R W	Millisecond	0-6000000	FGIMT
81-2	Voltage Inhibit	SG3	22642 5280*	Float	4	R W	Volt	15-250	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-2	Negative Sequence Inhibit	SG3	22644 5282*	Float	4	R W	Percent	0-99	FGIMT
81-2	Over Frequency	SG3	22646 5284*	Float	4	R W	Hertz	15-110	FGIMT
81-2	Under Frequency	SG3	22648 5286*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Mode	SG0	22650 5288*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-3	Pickup	SG0	22652 5290*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Time Delay	SG0	22654 5292*	Float	4	R W	Millisecond	0-600000	FGIMT
81-3	Voltage Inhibit	SG0	22656 5294*	Float	4	R W	Volt	15-250	FGIMT
81-3	Negative Sequence Inhibit	SG0	22658 5296*	Float	4	R W	Percent	0-99	FGIMT
81-3	Over Frequency	SG0	22660 5298*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Under Frequency	SG0	22662 5300*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Mode	SG1	22664 5302*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-3	Pickup	SG1	22666 5304*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Time Delay	SG1	22668 5306*	Float	4	R W	Millisecond	0-600000	FGIMT
81-3	Voltage Inhibit	SG1	22670 5308*	Float	4	R W	Volt	15-250	FGIMT
81-3	Negative Sequence Inhibit	SG1	22672 5310*	Float	4	R W	Percent	0-99	FGIMT
81-3	Over Frequency	SG1	22674 5312*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Under Frequency	SG1	22676 5314*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Mode	SG2	22678 5316*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-3	Pickup	SG2	22680 5318*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Time Delay	SG2	22682 5320*	Float	4	R W	Millisecond	0-600000	FGIMT
81-3	Voltage Inhibit	SG2	22684 5322*	Float	4	R W	Volt	15-250	FGIMT
81-3	Negative Sequence Inhibit	SG2	22686 5324*	Float	4	R W	Percent	0-99	FGIMT
81-3	Over Frequency	SG2	22688 5326*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Under Frequency	SG2	22690 5328*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Mode	SG3	22692 5330*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-3	Pickup	SG3	22694 5332*	Float	4	R W	Hertz	15-110	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-3	Time Delay	SG3	22696 5334*	Float	4	R W	Millisecond	0-600000	FGIMT
81-3	Voltage Inhibit	SG3	22698 5336*	Float	4	R W	Volt	15-250	FGIMT
81-3	Negative Sequence Inhibit	SG3	22700 5338*	Float	4	R W	Percent	0-99	FGIMT
81-3	Over Frequency	SG3	22702 5340*	Float	4	R W	Hertz	15-110	FGIMT
81-3	Under Frequency	SG3	22704 5342*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Mode	SG0	22706 5344*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-4	Pickup	SG0	22708 5346*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Time Delay	SG0	22710 5348*	Float	4	R W	Millisecond	0-600000	FGIMT
81-4	Voltage Inhibit	SG0	22712 5350*	Float	4	R W	Volt	15-250	FGIMT
81-4	Negative Sequence Inhibit	SG0	22714 5352*	Float	4	R W	Percent	0-99	FGIMT
81-4	Over Frequency	SG0	22716 5354*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Under Frequency	SG0	22718 5356*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Mode	SG1	22720 5358*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-4	Pickup	SG1	22722 5360*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Time Delay	SG1	22724 5362*	Float	4	R W	Millisecond	0-600000	FGIMT
81-4	Voltage Inhibit	SG1	22726 5364*	Float	4	R W	Volt	15-250	FGIMT
81-4	Negative Sequence Inhibit	SG1	22728 5366*	Float	4	R W	Percent	0-99	FGIMT
81-4	Over Frequency	SG1	22730 5368*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Under Frequency	SG1	22732 5370*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Mode	SG2	22734 5372*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-4	Pickup	SG2	22736 5374*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Time Delay	SG2	22738 5376*	Float	4	R W	Millisecond	0-600000	FGIMT
81-4	Voltage Inhibit	SG2	22740 5378*	Float	4	R W	Volt	15-250	FGIMT
81-4	Negative Sequence Inhibit	SG2	22742 5380*	Float	4	R W	Percent	0-99	FGIMT
81-4	Over Frequency	SG2	22744 5382*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Under Frequency	SG2	22746 5384*	Float	4	R W	Hertz	15-110	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-4	Mode	SG3	22748 5388*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIMT
81-4	Pickup	SG3	22750 5388*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Time Delay	SG3	22752 5390*	Float	4	R W	Millisecond	0-600000	FGIMT
81-4	Voltage Inhibit	SG3	22754 5392*	Float	4	R W	Volt	15-250	FGIMT
81-4	Negative Sequence Inhibit	SG3	22756 5394*	Float	4	R W	Percent	0-99	FGIMT
81-4	Over Frequency	SG3	22758 5396*	Float	4	R W	Hertz	15-110	FGIMT
81-4	Under Frequency	SG3	22760 5398*	Float	4	R W	Hertz	15-110	FGIMT
81-5	Mode	SG0	22762 5400*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-5	Pickup	SG0	22764 5402*	Float	4	R W	Hertz	15-110	FGIT
81-5	Time Delay	SG0	22766 5404*	Float	4	R W	Millisecond	0-600000	FGIT
81-5	Voltage Inhibit	SG0	22768 5406*	Float	4	R W	Volt	15-250	FGIT
81-5	Negative Sequence Inhibit	SG0	22770 5408*	Float	4	R W	Percent	0-99	FGIT
81-5	Over Frequency	SG0	22772 5410*	Float	4	R W	Hertz	15-110	FGIT
81-5	Under Frequency	SG0	22774 5412*	Float	4	R W	Hertz	15-110	FGIT
81-5	Mode	SG1	22776 5414*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-5	Pickup	SG1	22778 5416*	Float	4	R W	Hertz	15-110	FGIT
81-5	Time Delay	SG1	22780 5418*	Float	4	R W	Millisecond	0-600000	FGIT
81-5	Voltage Inhibit	SG1	22782 5420*	Float	4	R W	Volt	15-250	FGIT
81-5	Negative Sequence Inhibit	SG1	22784 5422*	Float	4	R W	Percent	0-99	FGIT
81-5	Over Frequency	SG1	22786 5424*	Float	4	R W	Hertz	15-110	FGIT
81-5	Under Frequency	SG1	22788 5426*	Float	4	R W	Hertz	15-110	FGIT
81-5	Mode	SG2	22790 5428*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-5	Pickup	SG2	22792 5430*	Float	4	R W	Hertz	15-110	FGIT
81-5	Time Delay	SG2	22794 5432*	Float	4	R W	Millisecond	0-600000	FGIT
81-5	Voltage Inhibit	SG2	22796 5434*	Float	4	R W	Volt	15-250	FGIT
81-5	Negative Sequence Inhibit	SG2	22798 5436*	Float	4	R W	Percent	0-99	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-5	Over Frequency	SG2	22800 5438*	Float	4	R W	Hertz	15-110	FGIT
81-5	Under Frequency	SG2	22802 5440*	Float	4	R W	Hertz	15-110	FGIT
81-5	Mode	SG3	22804 5442*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-5	Pickup	SG3	22806 5444*	Float	4	R W	Hertz	15-110	FGIT
81-5	Time Delay	SG3	22808 5446*	Float	4	R W	Millisecond	0-600000	FGIT
81-5	Voltage Inhibit	SG3	22810 5448*	Float	4	R W	Volt	15-250	FGIT
81-5	Negative Sequence Inhibit	SG3	22812 5450*	Float	4	R W	Percent	0-99	FGIT
81-5	Over Frequency	SG3	22814 5452*	Float	4	R W	Hertz	15-110	FGIT
81-5	Under Frequency	SG3	22816 5454*	Float	4	R W	Hertz	15-110	FGIT
81-6	Mode	SG0	22818 5456*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-6	Pickup	SG0	22820 5458*	Float	4	R W	Hertz	15-110	FGIT
81-6	Time Delay	SG0	22822 5460*	Float	4	R W	Millisecond	0-600000	FGIT
81-6	Voltage Inhibit	SG0	22824 5462*	Float	4	R W	Volt	15-250	FGIT
81-6	Negative Sequence Inhibit	SG0	22826 5464*	Float	4	R W	Percent	0-99	FGIT
81-6	Over Frequency	SG0	22828 5466*	Float	4	R W	Hertz	15-110	FGIT
81-6	Under Frequency	SG0	22830 5468*	Float	4	R W	Hertz	15-110	FGIT
81-6	Mode	SG1	22832 5470*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-6	Pickup	SG1	22834 5472*	Float	4	R W	Hertz	15-110	FGIT
81-6	Time Delay	SG1	22836 5474*	Float	4	R W	Millisecond	0-600000	FGIT
81-6	Voltage Inhibit	SG1	22838 5476*	Float	4	R W	Volt	15-250	FGIT
81-6	Negative Sequence Inhibit	SG1	22840 5478*	Float	4	R W	Percent	0-99	FGIT
81-6	Over Frequency	SG1	22842 5480*	Float	4	R W	Hertz	15-110	FGIT
81-6	Under Frequency	SG1	22844 5482*	Float	4	R W	Hertz	15-110	FGIT
81-6	Mode	SG2	22846 5484*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-6	Pickup	SG2	22848 5486*	Float	4	R W	Hertz	15-110	FGIT
81-6	Time Delay	SG2	22850 5488*	Float	4	R W	Millisecond	0-600000	FGIT
81-6	Voltage Inhibit	SG2	22852 5490*	Float	4	R W	Volt	15-250	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-6	Negative Sequence Inhibit	SG2	22854 5492*	Float	4	R W	Percent	0-99	FGIT
81-6	Over Frequency	SG2	22856 5494*	Float	4	R W	Hertz	15-110	FGIT
81-6	Under Frequency	SG2	22858 5496*	Float	4	R W	Hertz	15-110	FGIT
81-6	Mode	SG3	22860 5498*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-6	Pickup	SG3	22862 5500*	Float	4	R W	Hertz	15-110	FGIT
81-6	Time Delay	SG3	22864 5502*	Float	4	R W	Millisecond	0-600000	FGIT
81-6	Voltage Inhibit	SG3	22866 5504*	Float	4	R W	Volt	15-250	FGIT
81-6	Negative Sequence Inhibit	SG3	22868 5506*	Float	4	R W	Percent	0-99	FGIT
81-6	Over Frequency	SG3	22870 5508*	Float	4	R W	Hertz	15-110	FGIT
81-6	Under Frequency	SG3	22872 5510*	Float	4	R W	Hertz	15-110	FGIT
81-7	Mode	SG0	22874 5512*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-7	Pickup	SG0	22876 5514*	Float	4	R W	Hertz	15-110	FGIT
81-7	Time Delay	SG0	22878 5516*	Float	4	R W	Millisecond	0-600000	FGIT
81-7	Voltage Inhibit	SG0	22880 5518*	Float	4	R W	Volt	15-250	FGIT
81-7	Negative Sequence Inhibit	SG0	22882 5520*	Float	4	R W	Percent	0-99	FGIT
81-7	Over Frequency	SG0	22884 5522*	Float	4	R W	Hertz	15-110	FGIT
81-7	Under Frequency	SG0	22886 5524*	Float	4	R W	Hertz	15-110	FGIT
81-7	Mode	SG1	22888 5526*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-7	Pickup	SG1	22890 5528*	Float	4	R W	Hertz	15-110	FGIT
81-7	Time Delay	SG1	22892 5530*	Float	4	R W	Millisecond	0-600000	FGIT
81-7	Voltage Inhibit	SG1	22894 5532*	Float	4	R W	Volt	15-250	FGIT
81-7	Negative Sequence Inhibit	SG1	22896 5534*	Float	4	R W	Percent	0-99	FGIT
81-7	Over Frequency	SG1	22898 5536*	Float	4	R W	Hertz	15-110	FGIT
81-7	Under Frequency	SG1	22900 5538*	Float	4	R W	Hertz	15-110	FGIT
81-7	Mode	SG2	22902 5540*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-7	Pickup	SG2	22904 5542*	Float	4	R W	Hertz	15-110	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-7	Time Delay	SG2	22906 5544*	Float	4	R W	Millisecond	0-600000	FGIT
81-7	Voltage Inhibit	SG2	22908 5546*	Float	4	R W	Volt	15-250	FGIT
81-7	Negative Sequence Inhibit	SG2	22910 5548*	Float	4	R W	Percent	0-99	FGIT
81-7	Over Frequency	SG2	22912 5550*	Float	4	R W	Hertz	15-110	FGIT
81-7	Under Frequency	SG2	22914 5552*	Float	4	R W	Hertz	15-110	FGIT
81-7	Mode	SG3	22916 5554*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-7	Pickup	SG3	22918 5556*	Float	4	R W	Hertz	15-110	FGIT
81-7	Time Delay	SG3	22920 5558*	Float	4	R W	Millisecond	0-600000	FGIT
81-7	Voltage Inhibit	SG3	22922 5560*	Float	4	R W	Volt	15-250	FGIT
81-7	Negative Sequence Inhibit	SG3	22924 5562*	Float	4	R W	Percent	0-99	FGIT
81-7	Over Frequency	SG3	22926 5564*	Float	4	R W	Hertz	15-110	FGIT
81-7	Under Frequency	SG3	22928 5566*	Float	4	R W	Hertz	15-110	FGIT
81-8	Mode	SG0	22930 5568*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-8	Pickup	SG0	22932 5570*	Float	4	R W	Hertz	15-110	FGIT
81-8	Time Delay	SG0	22934 5572*	Float	4	R W	Millisecond	0-600000	FGIT
81-8	Voltage Inhibit	SG0	22936 5574*	Float	4	R W	Volt	15-250	FGIT
81-8	Negative Sequence Inhibit	SG0	22938 5576*	Float	4	R W	Percent	0-99	FGIT
81-8	Over Frequency	SG0	22940 5578*	Float	4	R W	Hertz	15-110	FGIT
81-8	Under Frequency	SG0	22942 5580*	Float	4	R W	Hertz	15-110	FGIT
81-8	Mode	SG1	22944 5582*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-8	Pickup	SG1	22946 5584*	Float	4	R W	Hertz	15-110	FGIT
81-8	Time Delay	SG1	22948 5586*	Float	4	R W	Millisecond	0-600000	FGIT
81-8	Voltage Inhibit	SG1	22950 5588*	Float	4	R W	Volt	15-250	FGIT
81-8	Negative Sequence Inhibit	SG1	22952 5590*	Float	4	R W	Percent	0-99	FGIT
81-8	Over Frequency	SG1	22954 5592*	Float	4	R W	Hertz	15-110	FGIT
81-8	Under Frequency	SG1	22956 5594*	Float	4	R W	Hertz	15-110	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-8	Mode	SG2	22958 5596*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-8	Pickup	SG2	22960 5598*	Float	4	R W	Hertz	15-110	FGIT
81-8	Time Delay	SG2	22962 5600*	Float	4	R W	Millisecond	0-600000	FGIT
81-8	Voltage Inhibit	SG2	22964 5602*	Float	4	R W	Volt	15-250	FGIT
81-8	Negative Sequence Inhibit	SG2	22966 5604*	Float	4	R W	Percent	0-99	FGIT
81-8	Over Frequency	SG2	22968 5606*	Float	4	R W	Hertz	15-110	FGIT
81-8	Under Frequency	SG2	22970 5608*	Float	4	R W	Hertz	15-110	FGIT
81-8	Mode	SG3	22972 5610*	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Rate of Change=3 Positive Rate of Change=4 Negative Rate of Change=5	FGIT
81-8	Pickup	SG3	22974 5612*	Float	4	R W	Hertz	15-110	FGIT
81-8	Time Delay	SG3	22976 5614*	Float	4	R W	Millisecond	0-600000	FGIT
81-8	Voltage Inhibit	SG3	22978 5616*	Float	4	R W	Volt	15-250	FGIT
81-8	Negative Sequence Inhibit	SG3	22980 5618*	Float	4	R W	Percent	0-99	FGIT
81-8	Over Frequency	SG3	22982 5620*	Float	4	R W	Hertz	15-110	FGIT
81-8	Under Frequency	SG3	22984 5622*	Float	4	R W	Hertz	15-110	FGIT
43-1	Mode	GG	22986 5624*	Uint32	4	R W	n/a	Disabled=0 Switch/Pulse=1 Switch=2 Pulse=3	DFGIMT
43-2	Mode	GG	22988 5626*	Uint32	4	R W	n/a	Disabled=0 Switch/Pulse=1 Switch=2 Pulse=3	DFGIMT
43-3	Mode	GG	22990 5628*	Uint32	4	R W	n/a	Disabled=0 Switch/Pulse=1 Switch=2 Pulse=3	DFGIMT
43-4	Mode	GG	22992 5630*	Uint32	4	R W	n/a	Disabled=0 Switch/Pulse=1 Switch=2 Pulse=3	DFGIMT
43-5	Mode	GG	22994 5632*	Uint32	4	R W	n/a	Disabled=0 Switch/Pulse=1 Switch=2 Pulse=3	DFGIMT
86-1	Mode	GG	22996 5634*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
86-2	Mode	GG	22998 5636*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
101	Mode	GG	23000 5638*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
62-1	Mode	SG0	23002 5640*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-1	Time Delay 1	SG0	23004 5642*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Time Delay 2	SG0	23006 5644*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-1	Mode	SG1	23008 5646*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-1	Time Delay 1	SG1	23010 5648*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Time Delay 2	SG1	23012 5650*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Mode	SG2	23014 5652*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-1	Time Delay 1	SG2	23016 5654*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Time Delay 2	SG2	23018 5656*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Mode	SG3	23020 5658*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-1	Time Delay 1	SG3	23022 5660*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-1	Time Delay 2	SG3	23024 5662*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Mode	SG0	23026 5664*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-2	Time Delay 1	SG0	23028 5666*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Time Delay 2	SG0	23030 5668*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Mode	SG1	23032 5670*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-2	Time Delay 1	SG1	23034 5672*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Time Delay 2	SG1	23036 5674*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Mode	SG2	23038 5676*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-2	Time Delay 1	SG2	23040 5678*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Time Delay 2	SG2	23042 5680*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-2	Mode	SG3	23044 5682*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-2	Time Delay 1	SG3	23046 5684*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-2	Time Delay 2	SG3	23048 5686*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Mode	SG0	23050 5690*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-3	Time Delay 1	SG0	23052 5690*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Time Delay 2	SG0	23054 5692*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Mode	SG1	23056 5694*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-3	Time Delay 1	SG1	23058 5696*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Time Delay 2	SG1	23060 5698*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Mode	SG2	23062 5700*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-3	Time Delay 1	SG2	23064 5702*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Time Delay 2	SG2	23066 5704*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Mode	SG3	23068 5706*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-3	Time Delay 1	SG3	23070 5708*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-3	Time Delay 2	SG3	23072 5710*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Mode	SG0	23074 5712*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-4	Time Delay 1	SG0	23076 5714*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Time Delay 2	SG0	23078 5716*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-4	Mode	SG1	23080 5718*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-4	Time Delay 1	SG1	23082 5720*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Time Delay 2	SG1	23084 5722*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Mode	SG2	23086 5724*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-4	Time Delay 1	SG2	23088 5726*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Time Delay 2	SG2	23090 5728*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Mode	SG3	23092 5730*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-4	Time Delay 1	SG3	23094 5732*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-4	Time Delay 2	SG3	23096 5734*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Mode	SG0	23098 5736*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-5	Time Delay 1	SG0	23100 5738*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Time Delay 2	SG0	23102 5740*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Mode	SG1	23104 5742*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-5	Time Delay 1	SG1	23106 5744*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Time Delay 2	SG1	23108 5746*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Mode	SG2	23110 5748*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-5	Time Delay 1	SG2	23112 5752*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Time Delay 2	SG2	23114 5752*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-5	Mode	SG3	23116 5754*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-5	Time Delay 1	SG3	23118 5756*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-5	Time Delay 2	SG3	23120 5758*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Mode	SG0	23122 5760*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-6	Time Delay 1	SG0	23124 5762*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Time Delay 2	SG0	23126 5764*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Mode	SG1	23128 5766*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-6	Time Delay 1	SG1	23130 5768*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Time Delay 2	SG1	23132 5770*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Mode	SG2	23134 5772*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-6	Time Delay 1	SG2	23136 5774*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Time Delay 2	SG2	23138 5776*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Mode	SG3	23140 5778*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-6	Time Delay 1	SG3	23142 5780*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-6	Time Delay 2	SG3	23144 5782*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Mode	SG0	23146 5784*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-7	Time Delay 1	SG0	23148 5786*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Time Delay 2	SG0	23150 5788*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-7	Mode	SG1	23152 5790*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-7	Time Delay 1	SG1	23154 5792*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Time Delay 2	SG1	23156 5794*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Mode	SG2	23158 5796*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-7	Time Delay 1	SG2	23160 5798*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Time Delay 2	SG2	23162 5800*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Mode	SG3	23164 5802*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-7	Time Delay 1	SG3	23166 5804*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-7	Time Delay 2	SG3	23168 5806*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Mode	SG0	23170 5808*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-8	Time Delay 1	SG0	23172 5810*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Time Delay 2	SG0	23174 5812*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Mode	SG1	23176 5814*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-8	Time Delay 1	SG1	23178 5816*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Time Delay 2	SG1	23180 5818*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Mode	SG2	23182 5820*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-8	Time Delay 1	SG2	23184 5822*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Time Delay 2	SG2	23186 5824*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
62-8	Mode	SG3	23188 5826*	Uint32	4	R W	n/a	Disabled=0 Pickup/Dropout=1 One-Shot/Non- Retriggerable=2 One- Shot/Retriggerable=3 Oscillator=4 Integrating Timer=5 Latched=6	DFGIMT
62-8	Time Delay 1	SG3	23190 5828*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
62-8	Time Delay 2	SG3	23192 5830*	Uint32	4	R W	Millisecond	0-9999000	DFGIMT
79	Mode	SG0	23194 5832*	Uint32	4	R W	n/a	Disabled=0 Power To Lockout=1 Power To Reclose=2	FIT
79	Reclose 1 Time	SG0	23196 5834*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 2 Time	SG0	23198 5836*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 3 Time	SG0	23200 5838*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 4 Time	SG0	23202 5840*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reset Time	SG0	23204 5842*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Fail Time	SG0	23206 5844*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Max Time	SG0	23208 5846*	Uint32	4	R W	Millisecond	100-600000	FIT
79	SCB Trip 1	SG0	23210 5848*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 2	SG0	23212 5850*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 3	SG0	23214 5852*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 4	SG0	23216 5854*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 5	SG0	23218 5856*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	Mode	SG1	23220 5858*	Uint32	4	R W	n/a	Disabled=0 Power To Lockout=1 Power To Reclose=2	FIT
79	Reclose 1 Time	SG1	23222 5860*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 2 Time	SG1	23224 5862*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 3 Time	SG1	23226 5864*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 4 Time	SG1	23228 5866*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reset Time	SG1	23230 5868*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Fail Time	SG1	23232 5870*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Max Time	SG1	23234 5872*	Uint32	4	R W	Millisecond	100-600000	FIT
79	SCB Trip 1	SG1	23236 5874*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 2	SG1	23238 5876*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 3	SG1	23240 5878*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 4	SG1	23242 5880*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 5	SG1	23244 5882*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	Mode	SG2	23246 5884*	Uint32	4	R W	n/a	Disabled=0 Power To Lockout=1 Power To Reclose=2	FIT
79	Reclose 1 Time	SG2	23248 5886*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 2 Time	SG2	23250 5888*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 3 Time	SG2	23252 5890*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reclose 4 Time	SG2	23254 5892*	Uint32	4	R W	Millisecond	100-600000	FIT
79	Reset Time	SG2	23256 5894*	Uint32	4	R W	Millisecond	100-600000	FIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
79	Fail Time	SG2	23258 5896*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Max Time	SG2	23260 5898*	Uint32	4	R W	Millisecond	100–600000	FIT
79	SCB Trip 1	SG2	23262 5900*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 2	SG2	23264 5902*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 3	SG2	23266 5904*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 4	SG2	23268 5906*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 5	SG2	23270 5908*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	Mode	SG3	23272 5910*	Uint32	4	R W	n/a	Disabled=0 Power To Lockout=1 Power To Reclose=2	FIT
79	Reclose 1 Time	SG3	23274 5912*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Reclose 2 Time	SG3	23276 5914*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Reclose 3 Time	SG3	23278 5916*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Reclose 4 Time	SG3	23280 5918*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Reset Time	SG3	23282 5920*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Fail Time	SG3	23284 5922*	Uint32	4	R W	Millisecond	100–600000	FIT
79	Max Time	SG3	23286 5924*	Uint32	4	R W	Millisecond	100–600000	FIT
79	SCB Trip 1	SG3	23288 5926*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 2	SG3	23290 5928*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 3	SG3	23292 5930*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 4	SG3	23294 5932*	Uint32	4	R W	n/a	Off=0 On=1	FIT
79	SCB Trip 5	SG3	23296 5934*	Uint32	4	R W	n/a	Off=0 On=1	FIT
50BF	Mode	SG0	23298 5936*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
50BF	Source	SG0	23300	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50BF	Phase Pickup	SG0	23302 5938*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Neutral Pickup	SG0	23304 5940*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Time Delay	SG0	23306 5942*	Float	4	R W	Millisecond	50–999	FGIMT
50BF	Control Time	SG0	23308 5944*	Float	4	R W	Millisecond	50–999	FGIMT
50BF	IG Source	SG0	23310	Uint32	4	R W	n/a	IG1=0 IG2=1	FGIMT
50BF	Mode	SG1	23312 5946*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
50BF	Source	SG1	23314	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50BF	Phase Pickup	SG1	23316 5948*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Neutral Pickup	SG1	23318 5950*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Time Delay	SG1	23320 5952*	Float	4	R W	Millisecond	50–999	FGIMT
50BF	Control Time	SG1	23322 5954*	Float	4	R W	Millisecond	50–999	FGIMT
50BF	IG Source	SG1	23324	Uint32	4	R W	n/a	IG1=0 IG2=1	FGIMT
50BF	Mode	SG2	23326 5956*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
50BF	Source	SG2	23328	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50BF	Phase Pickup	SG2	23330 5958*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Neutral Pickup	SG2	23332 5960*	Float	4	R W	Amp	0.25–10	FGIMT
50BF	Time Delay	SG2	23334 5962*	Float	4	R W	Millisecond	50–999	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50BF	Control Time	SG2	23336 5964*	Float	4	R W	Millisecond	50-999	FGIMT
50BF	IG Source	SG2	23338	Uint32	4	R W	n/a	IG1=0 IG2=1	FGIMT
50BF	Mode	SG3	23340 5966*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
50BF	Source	SG3	23342	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
50BF	Phase Pickup	SG3	23344 5968*	Float	4	R W	Amp	0.25-10	FGIMT
50BF	Neutral Pickup	SG3	23346 5970*	Float	4	R W	Amp	0.25-10	FGIMT
50BF	Time Delay	SG3	23348 5972*	Float	4	R W	Millisecond	50-999	FGIMT
50BF	Control Time	SG3	23350 5974*	Float	4	R W	Millisecond	50-999	FGIMT
50BF	IG Source	SG3	23352	Uint32	4	R W	n/a	IG1=0 IG2=1	FGIMT
52	Mode	GG	23354 5976*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIMT
Breaker Monitor	Mode	GG	23356 5978*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Breaker Monitor	Source	GG	23358	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIMT
Breaker Monitor	Exponent	GG	23360 5980*	Float	4	R W	n/a	1-3	DFGIMT
Breaker Monitor	Max Duty	GG	23362 5982*	Scientific	4	R W	n/a	0-42000000	DFGIMT
Breaker Monitor	Alarm 1 Type	GG	23364 5984*	Uint32	4	R W	n/a	Disabled=0 Duty=1 Operation=2 Clearing=3	DFGIMT
Breaker Monitor	Alarm 1 Threshold	GG	23366 5986*	Float	4	R W	See range	Duty: 0-200% Operation: 0-99,999 Clearing: 0-1,000 milliseconds	DFGIMT
Breaker Monitor	Alarm 2 Type	GG	23368 5988*	Uint32	4	R W	n/a	Disabled=0 Duty=1 Operation=2 Clearing=3	DFGIMT
Breaker Monitor	Alarm 2 Threshold	GG	23370 5990*	Float	4	R W	See range	Duty: 0-200% Operation: 0-99,999 Clearing: 0-1,000 milliseconds	DFGIMT
Breaker Monitor	Alarm 3 Type	GG	23372 5992*	Uint32	4	R W	n/a	Disabled=0 Duty=1 Operation=2 Clearing=3	DFGIMT
Breaker Monitor	Alarm 3 Threshold	GG	23374 5994*	Float	4	R W	See range	Duty: 0-200% Operation: 0-99,999 Clearing: 0-1,000 milliseconds	DFGIMT
Breaker Monitor	Operations	GG	23376 5996*	Uint32	4	R W	n/a	0-99999	DFGIMT
Breaker Monitor	Duty A	GG	23378 5998*	Float	4	R W	Percent	0-200	FGIMT
Breaker Monitor	Duty	GG	23378	Float	4	R W	Percent	0-200	D
Breaker Monitor	Duty B	GG	23380 6000*	Float	4	R W	Percent	0-200	FGIMT
Breaker Monitor	Duty C	GG	23382 6002*	Float	4	R W	Percent	0-200	FGIMT
81-1	Source	SG0	23384 6004*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-1	Source	SG1	23386 6006*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-1	Source	SG2	23388 6008*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-1	Source	SG3	23390 6010*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-2	Source	SG0	23392 6012*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-2	Source	SG1	23394 6014*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-2	Source	SG2	23396 6016*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-2	Source	SG3	23398 6018*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-3	Source	SG0	23400 6020*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-3	Source	SG1	23402 6022*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
81-3	Source	SG2	23404 6024*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-3	Source	SG3	23406 6026*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-4	Source	SG0	23408 6028*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-4	Source	SG1	23410 6030*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-4	Source	SG2	23412 6032*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-4	Source	SG3	23414 6034*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-5	Source	SG0	23416 6036*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-5	Source	SG1	23418 6038*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-5	Source	SG2	23420 6040*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-5	Source	SG3	23422 6042*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-6	Source	SG0	23424 6044*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-6	Source	SG1	23426 6046*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-6	Source	SG2	23428 6048*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-6	Source	SG3	23430 6050*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-7	Source	SG0	23432 6052*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-7	Source	SG1	23434 6054*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-7	Source	SG2	23436 6056*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-7	Source	SG3	23438 6058*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-8	Source	SG0	23440 6060*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-8	Source	SG1	23442 6062*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-8	Source	SG2	23444 6064*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
81-8	Source	SG3	23446 6066*	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	FGIT
25VM	Dead Line/Dead Aux	SG0	23448 6068*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Live Aux	SG0	23450 6070*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Live Line/Dead Aux	SG0	23452 6072*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Dead Aux	SG1	23454 6074*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Live Aux	SG1	23456 6076*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Live Line/Dead Aux	SG1	23458 6078*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Dead Aux	SG2	23460 6080*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Live Aux	SG2	23462 6082*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Live Line/Dead Aux	SG2	23464 6084*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Dead Aux	SG3	23466 6086*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Dead Line/Live Aux	SG3	23468 6088*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
25VM	Live Line/Dead Aux	SG3	23470 6090*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
87N-1	Mode	SG0	23472 6092*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GT
87N-1	CT Source	SG0	23474	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	GT
Reserved			23476- 77						
87N-1	Iop Minimum	SG0	23478 6094*	Float	4	R W	Amp	0.1–5	GT
87N-1	Time Delay	SG0	23480 6096*	Float	4	R W	Millisecond	0–60000	GT
87N-1	Overcorrection Coefficient	SG0	23482 6098*	Float	4	R W	n/a	1–1.3	GT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
87N-1	CT Flip	SG0	23484 6100*	Uint32	4	R W	n/a	No=0 Yes=1	GT
87N-1	Mode	SG1	23486 6102*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GT
87N-1	CT Source	SG1	23488	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	GT
Reserved			23490- 91						
87N-1	Iop Minimum	SG1	23492 6104*	Float	4	R W	Amp	0.1-5	GT
87N-1	Time Delay	SG1	23494 6106*	Float	4	R W	Millisecond	0-60000	GT
87N-1	Overcorrection Coefficient	SG1	23496 6108*	Float	4	R W	n/a	1-1.3	GT
87N-1	CT Flip	SG1	23498 6110*	Uint32	4	R W	n/a	No=0 Yes=1	GT
87N-1	Mode	SG2	23500 6112*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GT
87N-1	CT Source	SG2	23502	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	GT
Reserved			23504- 05						
87N-1	Iop Minimum	SG2	23506 6114*	Float	4	R W	Amp	0.1-5	GT
87N-1	Time Delay	SG2	23508 6116*	Float	4	R W	Millisecond	0-60000	GT
87N-1	Overcorrection Coefficient	SG2	23510 6118*	Float	4	R W	n/a	1-1.3	GT
87N-1	CT Flip	SG2	23512 6120*	Uint32	4	R W	n/a	No=0 Yes=1	GT
87N-1	Mode	SG3	23514 6122*	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GT
87N-1	CT Source	SG3	23516	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	GT
Reserved			23518- 19						
87N-1	Iop Minimum	SG3	23520 6124*	Float	4	R W	Amp	0.1-5	GT
87N-1	Time Delay	SG3	23522 6126*	Float	4	R W	Millisecond	0-60000	GT
87N-1	Overcorrection Coefficient	SG3	23524 6128*	Float	4	R W	n/a	1-1.3	GT
87N-1	CT Flip	SG3	23526 6130*	Uint32	4	R W	n/a	No=0 Yes=1	GT
37	Mode	SG0	23558	Uint32	4	R W	n/a	Disabled=0 3PH=4	M
37	Source	SG0	23560	Uint32	4	R W	n/a	CT1=0 CT2=1	M
37	Pickup	SG0	23562	Float	4	R W	Amp	0.5-100	M
37	Time Delay	SG0	23564	Float	4	R W	Millisecond	0-60000	M
37	Inhibit Level	SG0	23566	Float	4	R W	Amp	0.5-100	M
37	Mode	SG1	23568	Uint32	4	R W	n/a	Disabled=0 3PH=4	M
37	Source	SG1	23570	Uint32	4	R W	n/a	CT1=0 CT2=1	M
37	Pickup	SG1	23572	Float	4	R W	Amp	0.5-100	M
37	Time Delay	SG1	23574	Float	4	R W	Millisecond	0-60000	M
37	Inhibit Level	SG1	23576	Float	4	R W	Amp	0.5-100	M
37	Mode	SG2	23578	Uint32	4	R W	n/a	Disabled=0 3PH=4	M
37	Source	SG2	23580	Uint32	4	R W	n/a	CT1=0 CT2=1	M
37	Pickup	SG2	23582	Float	4	R W	Amp	0.5-100	M
37	Time Delay	SG2	23584	Float	4	R W	Millisecond	0-60000	M
37	Inhibit Level	SG2	23586	Float	4	R W	Amp	0.5-100	M
37	Mode	SG3	23588	Uint32	4	R W	n/a	Disabled=0 3PH=4	M
37	Source	SG3	23590	Uint32	4	R W	n/a	CT1=0 CT2=1	M
37	Pickup	SG3	23592	Float	4	R W	Amp	0.5-100	M
37	Time Delay	SG3	23594	Float	4	R W	Millisecond	0-60000	M
37	Inhibit Level	SG3	23596	Float	4	R W	Amp	0.5-100	M
Analog Input Protection 1	Mode	SG0	23598	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 1	Source	SG0	23600	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 1	Pickup	SG0	23602	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Time Delay	SG0	23604	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 1	Inhibit Level	SG0	23606	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Mode	SG1	23608	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 1	Source	SG1	23610	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 1	Pickup	SG1	23612	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Time Delay	SG1	23614	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 1	Inhibit Level	SG1	23616	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Mode	SG2	23618	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 1	Source	SG2	23620	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 1	Pickup	SG2	23622	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Time Delay	SG2	23624	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 1	Inhibit Level	SG2	23626	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Mode	SG3	23628	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 1	Source	SG3	23630	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 1	Pickup	SG3	23632	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 1	Time Delay	SG3	23634	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 1	Inhibit Level	SG3	23636	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Mode	SG0	23638	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 2	Source	SG0	23640	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 2	Pickup	SG0	23642	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Time Delay	SG0	23644	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 2	Inhibit Level	SG0	23646	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Mode	SG1	23648	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 2	Source	SG1	23650	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 2	Pickup	SG1	23652	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Time Delay	SG1	23654	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 2	Inhibit Level	SG1	23656	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 2	Mode	SG2	23658	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 2	Source	SG2	23660	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 2	Pickup	SG2	23662	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Time Delay	SG2	23664	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 2	Inhibit Level	SG2	23666	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Mode	SG3	23668	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 2	Source	SG3	23670	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 2	Pickup	SG3	23672	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 2	Time Delay	SG3	23674	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 2	Inhibit Level	SG3	23676	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Mode	SG0	23678	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 3	Source	SG0	23680	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 3	Pickup	SG0	23682	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Time Delay	SG0	23684	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 3	Inhibit Level	SG0	23686	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Mode	SG1	23688	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 3	Source	SG1	23690	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 3	Pickup	SG1	23692	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Time Delay	SG1	23694	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 3	Inhibit Level	SG1	23696	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Mode	SG2	23698	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 3	Source	SG2	23700	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 3	Pickup	SG2	23702	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Time Delay	SG2	23704	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 3	Inhibit Level	SG2	23706	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Mode	SG3	23708	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 3	Source	SG3	23710	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 3	Pickup	SG3	23712	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 3	Time Delay	SG3	23714	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 3	Inhibit Level	SG3	23716	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Mode	SG0	23718	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 4	Source	SG0	23720	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 4	Pickup	SG0	23722	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Time Delay	SG0	23724	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 4	Inhibit Level	SG0	23726	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Mode	SG1	23728	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 4	Source	SG1	23730	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 4	Pickup	SG1	23732	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Time Delay	SG1	23734	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 4	Inhibit Level	SG1	23736	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Mode	SG2	23738	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 4	Source	SG2	23740	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 4	Pickup	SG2	23742	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Time Delay	SG2	23744	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 4	Inhibit Level	SG2	23746	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Mode	SG3	23748	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 4	Source	SG3	23750	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 4	Pickup	SG3	23752	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 4	Time Delay	SG3	23754	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 4	Inhibit Level	SG3	23756	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Mode	SG0	23758	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 5	Source	SG0	23760	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 5	Pickup	SG0	23762	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Time Delay	SG0	23764	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 5	Inhibit Level	SG0	23766	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Mode	SG1	23768	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 5	Source	SG1	23770	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 5	Pickup	SG1	23772	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Time Delay	SG1	23774	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 5	Inhibit Level	SG1	23776	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Mode	SG2	23778	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 5	Source	SG2	23780	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 5	Pickup	SG2	23782	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Time Delay	SG2	23784	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 5	Inhibit Level	SG2	23786	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Mode	SG3	23788	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 5	Source	SG3	23790	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 5	Pickup	SG3	23792	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 5	Time Delay	SG3	23794	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 5	Inhibit Level	SG3	23796	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Mode	SG0	23798	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 6	Source	SG0	23800	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 6	Pickup	SG0	23802	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Time Delay	SG0	23804	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 6	Inhibit Level	SG0	23806	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Mode	SG1	23808	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 6	Source	SG1	23810	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 6	Pickup	SG1	23812	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Time Delay	SG1	23814	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 6	Inhibit Level	SG1	23816	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Mode	SG2	23818	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 6	Source	SG2	23820	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 6	Pickup	SG2	23822	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Time Delay	SG2	23824	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 6	Inhibit Level	SG2	23826	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 6	Mode	SG3	23828	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 6	Source	SG3	23830	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 6	Pickup	SG3	23832	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 6	Time Delay	SG3	23834	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 6	Inhibit Level	SG3	23836	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Mode	SG0	23838	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 7	Source	SG0	23840	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 7	Pickup	SG0	23842	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Time Delay	SG0	23844	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 7	Inhibit Level	SG0	23846	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Mode	SG1	23848	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 7	Source	SG1	23850	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 7	Pickup	SG1	23852	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Time Delay	SG1	23854	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 7	Inhibit Level	SG1	23856	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Mode	SG2	23858	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 7	Source	SG2	23860	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 7	Pickup	SG2	23862	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Time Delay	SG2	23864	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 7	Inhibit Level	SG2	23866	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Mode	SG3	23868	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 7	Source	SG3	23870	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 7	Pickup	SG3	23872	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 7	Time Delay	SG3	23874	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 7	Inhibit Level	SG3	23876	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Mode	SG0	23878	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 8	Source	SG0	23880	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input Protection 8	Pickup	SG0	23882	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Time Delay	SG0	23884	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 8	Inhibit Level	SG0	23886	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Mode	SG1	23888	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 8	Source	SG1	23890	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 8	Pickup	SG1	23892	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Time Delay	SG1	23894	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 8	Inhibit Level	SG1	23896	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Mode	SG2	23898	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 8	Source	SG2	23900	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 8	Pickup	SG2	23902	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Time Delay	SG2	23904	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 8	Inhibit Level	SG2	23906	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Mode	SG3	23908	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2	DFGIMT
Analog Input Protection 8	Source	SG3	23910	Uint32	4	R W	n/a	Module 1 Input 1=0 Module 1 Input 2=1 Module 1 Input 3=2 Module 1 Input 4=3 Module 2 Input 1=4 Module 2 Input 2=5 Module 2 Input 3=6 Module 2 Input 4=7	DFGIMT
Analog Input Protection 8	Pickup	SG3	23912	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
Analog Input Protection 8	Time Delay	SG3	23914	Float	4	R W	Millisecond	0–60000	DFGIMT
Analog Input Protection 8	Inhibit Level	SG3	23916	Float	4	R W	n/a	(-99999.9)–99999.9	DFGIMT
48	Mode	SG0	23918	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
48	Time Interval	SG0	23920	Float	4	R W	Second	0–600	M
48	Mode	SG1	23922	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
48	Time Interval	SG1	23924	Float	4	R W	Second	0–600	M
48	Mode	SG2	23926	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
48	Time Interval	SG2	23928	Float	4	R W	Second	0–600	M
48	Mode	SG3	23930	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
48	Time Interval	SG3	23932	Float	4	R W	Second	0–600	M
66-1	Mode	SG0	23934	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
66-1	Time Interval	SG0	23936	Uint32	4	R W	Minute	1–1440	M
66-1	Number Of Starts	SG0	23938	Uint32	4	R W	n/a	1–100	M
66-1	Mode	SG1	23940	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
66-1	Time Interval	SG1	23942	Uint32	4	R W	Minute	1–1440	M
66-1	Number Of Starts	SG1	23944	Uint32	4	R W	n/a	1–100	M
66-1	Mode	SG2	23946	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
66-1	Time Interval	SG2	23948	Uint32	4	R W	Minute	1–1440	M
66-1	Number Of Starts	SG2	23950	Uint32	4	R W	n/a	1–100	M
66-1	Mode	SG3	23952	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
66-1	Time Interval	SG3	23954	Uint32	4	R W	Minute	1–1440	M
66-1	Number Of Starts	SG3	23956	Uint32	4	R W	n/a	1–100	M
Reserved			23958-61						
Restart Inhibit	Restart Delay	SG0	23962	Uint32	4	R W	Minute	0–7200	M

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Restart Inhibit	Time Between Starts	SG0	23964	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Thermal Capacity Inhibit	SG0	23966	Float	4	R W	Percent	0-70	M
Reserved			23968-70						
Restart Inhibit	Restart Delay	SG1	23972	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Time Between Starts	SG1	23974	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Thermal Capacity Inhibit	SG1	23976	Float	4	R W	Percent	0-70	M
Reserved			23978-80						
Restart Inhibit	Restart Delay	SG2	23982	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Time Between Starts	SG2	23984	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Thermal Capacity Inhibit	SG2	23986	Float	4	R W	Percent	0-70	M
Reserved			23988-90						
Restart Inhibit	Restart Delay	SG3	23992	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Time Between Starts	SG3	23994	Uint32	4	R W	Minute	0-7200	M
Restart Inhibit	Thermal Capacity Inhibit	SG3	23996	Float	4	R W	Percent	0-70	M
49RTD-1	Mode	SG0	23998	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-1	Source	SG0	24000	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-1	Over Pickup	SG0	24002	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Under Pickup	SG0	24004	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Time Delay	SG0	24006	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-1	Voting	SG0	24008	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-1	Mode	SG1	24010	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-1	Source	SG1	24012	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-1	Over Pickup	SG1	24014	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Under Pickup	SG1	24016	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Time Delay	SG1	24018	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-1	Voting	SG1	24020	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-1	Mode	SG2	24022	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-1	Source	SG2	24024	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-1	Over Pickup	SG2	24026	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Under Pickup	SG2	24028	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Time Delay	SG2	24030	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-1	Voting	SG2	24032	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-1	Mode	SG3	24034	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-1	Source	SG3	24036	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-1	Over Pickup	SG3	24038	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Under Pickup	SG3	24040	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-1	Time Delay	SG3	24042	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-1	Voting	SG3	24044	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-2	Mode	SG0	24046	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-2	Source	SG0	24048	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-2	Over Pickup	SG0	24050	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Under Pickup	SG0	24052	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Time Delay	SG0	24054	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-2	Voting	SG0	24056	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-2	Mode	SG1	24058	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-2	Source	SG1	24060	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-2	Over Pickup	SG1	24062	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Under Pickup	SG1	24064	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-2	Time Delay	SG1	24066	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-2	Voting	SG1	24068	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-2	Mode	SG2	24070	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-2	Source	SG2	24072	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-2	Over Pickup	SG2	24074	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Under Pickup	SG2	24076	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Time Delay	SG2	24078	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-2	Voting	SG2	24080	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-2	Mode	SG3	24082	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-2	Source	SG3	24084	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-2	Over Pickup	SG3	24086	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Under Pickup	SG3	24088	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-2	Time Delay	SG3	24090	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-2	Voting	SG3	24092	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-3	Mode	SG0	24094	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-3	Source	SG0	24096	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-3	Over Pickup	SG0	24098	Float	4	R W	Deg C	0-250	DFGIMT
49RTD-3	Under Pickup	SG0	24100	Float	4	R W	Deg C	0-250	DFGIMT
49RTD-3	Time Delay	SG0	24102	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-3	Voting	SG0	24104	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-3	Mode	SG1	24106	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-3	Source	SG1	24108	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-3	Over Pickup	SG1	24110	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Under Pickup	SG1	24112	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Time Delay	SG1	24114	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-3	Voting	SG1	24116	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-3	Mode	SG2	24118	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-3	Source	SG2	24120	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-3	Over Pickup	SG2	24122	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Under Pickup	SG2	24124	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Time Delay	SG2	24126	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-3	Voting	SG2	24128	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-3	Mode	SG3	24130	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-3	Source	SG3	24132	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-3	Over Pickup	SG3	24134	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Under Pickup	SG3	24136	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-3	Time Delay	SG3	24138	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-3	Voting	SG3	24140	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-4	Mode	SG0	24142	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-4	Source	SG0	24144	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-4	Over Pickup	SG0	24146	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Under Pickup	SG0	24148	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-4	Time Delay	SG0	24150	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-4	Voting	SG0	24152	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-4	Mode	SG1	24154	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-4	Source	SG1	24156	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-4	Over Pickup	SG1	24158	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Under Pickup	SG1	24160	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Time Delay	SG1	24162	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-4	Voting	SG1	24164	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-4	Mode	SG2	24166	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-4	Source	SG2	24168	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-4	Over Pickup	SG2	24170	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Under Pickup	SG2	24172	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Time Delay	SG2	24174	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-4	Voting	SG2	24176	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-4	Mode	SG3	24178	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-4	Source	SG3	24180	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-4	Over Pickup	SG3	24182	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Under Pickup	SG3	24184	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-4	Time Delay	SG3	24186	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-4	Voting	SG3	24188	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-5	Mode	SG0	24190	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-5	Source	SG0	24192	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-5	Over Pickup	SG0	24194	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Under Pickup	SG0	24196	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Time Delay	SG0	24198	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-5	Voting	SG0	24200	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-5	Mode	SG1	24202	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-5	Source	SG1	24204	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-5	Over Pickup	SG1	24206	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Under Pickup	SG1	24208	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Time Delay	SG1	24210	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-5	Voting	SG1	24212	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-5	Mode	SG2	24214	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-5	Source	SG2	24216	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-5	Over Pickup	SG2	24218	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Under Pickup	SG2	24220	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Time Delay	SG2	24222	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-5	Voting	SG2	24224	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-5	Mode	SG3	24226	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-5	Source	SG3	24228	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-5	Over Pickup	SG3	24230	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-5	Under Pickup	SG3	24232	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-5	Time Delay	SG3	24234	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-5	Voting	SG3	24236	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-6	Mode	SG0	24238	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-6	Source	SG0	24240	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-6	Over Pickup	SG0	24242	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Under Pickup	SG0	24244	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Time Delay	SG0	24246	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-6	Voting	SG0	24248	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-6	Mode	SG1	24250	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-6	Source	SG1	24252	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-6	Over Pickup	SG1	24254	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Under Pickup	SG1	24256	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Time Delay	SG1	24258	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-6	Voting	SG1	24260	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-6	Mode	SG2	24262	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-6	Source	SG2	24264	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-6	Over Pickup	SG2	24266	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Under Pickup	SG2	24268	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Time Delay	SG2	24270	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-6	Voting	SG2	24272	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-6	Mode	SG3	24274	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-6	Source	SG3	24276	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-6	Over Pickup	SG3	24278	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Under Pickup	SG3	24280	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-6	Time Delay	SG3	24282	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-6	Voting	SG3	24284	Uint32	4	R W	n/a	1-24	DFGIMT
87	Mode	SG0	24286	Uint32	4	R W	n/a	Disabled=0 Percent Differential=1 Flux Balance=2	GMT
87	Time Delay	SG0	24288	Float	4	R W	Millisecond	0-60000	GMT
87	Source	SG0	24290	Uint32	4	R W	n/a	CT1=0 CT2=1	GMT
87	Min Restrained Pickup Value	SG0	24292	Float	4	R W	n/a	0.1-1	GMT
87	Second Restrained Pickup Value	SG0	24294	Float	4	R W	n/a	1-20	GMT
87	Restraint Slope Mode	SG0	24296	Uint32	4	R W	n/a	Maximum=0 Average=1	GMT
87	Restraint Slope 1	SG0	24298	Float	4	R W	Percent	5-100	GMT
87	Restraint Slope 2	SG0	24300	Float	4	R W	Percent	15-140	GMT
87	Alarm Slope	SG0	24302	Float	4	R W	Percent	50-100	GMT
Reserved			24304-05						
87	2 nd Harmonic Restraint	SG0	24306	Float	4	R W	Percent	0-75	GT
87	5 th Harmonic	SG0	24308	Float	4	R W	Percent	0-75	GT
87	Shared Harmonic Restraint	SG0	24310	Uint32	4	R W	n/a	Independent=0 Shared=1	GMT
87	Unrestrained Pickup Mode	SG0	24312	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GMT
87	Unrestrained Pickup Value	SG0	24314	Float	4	R W	n/a	0-21	GMT
87	Flux Balance Pickup	SG0	24316	Float	4	R W	Amp	0.1-5	GMT
87	Flux Balance Alarm	SG0	24318	Float	4	R W	Amp	0.1-5	GMT
87	Mode	SG1	24320	Uint32	4	R W	n/a	Disabled=0 Percent Differential=1 Flux Balance=2	GMT
87	Time Delay	SG1	24322	Float	4	R W	Millisecond	0 - 60000	GMT
87	Source	SG1	24324	Uint32	4	R W	n/a	CT1=0 CT2=1	GMT
87	Min Restrained Pickup Value	SG1	24326	Float	4	R W	n/a	0.1-1	GMT
87	Second Restrained Pickup Value	SG1	24328	Float	4	R W	n/a	1-20	GMT
87	Restraint Slope Mode	SG1	24330	Uint32	4	R W	n/a	Maximum=0 Average=1	GMT
87	Restraint Slope 1	SG1	24332	Float	4	R W	Percent	5-100	GMT
87	Restraint Slope 2	SG1	24334	Float	4	R W	Percent	15-140	GMT
87	Alarm Slope	SG1	24336	Float	4	R W	Percent	50-100	GMT
Reserved			24338-39						
87	2 nd Harmonic Restraint	SG1	24340	Float	4	R W	Percent	0-75	GT
87	5 th Harmonic	SG1	24342	Float	4	R W	Percent	0-75	GT
87	Shared Harmonic Restraint	SG1	24344	Uint32	4	R W	n/a	Independent=0 Shared=1	GMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
87	Unrestrained Pickup Mode	SG1	24346	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GMT
87	Unrestrained Pickup Value	SG1	24348	Float	4	R W	n/a	0–21	GMT
87	Flux Balance Pickup	SG1	24350	Float	4	R W	Amp	0.1–5	GMT
87	Flux Balance Alarm	SG1	24352	Float	4	R W	Amp	0.1–5	GMT
87	Mode	SG2	24354	Uint32	4	R W	n/a	Disabled=0 Percent Differential=1 Flux Balance=2	GMT
87	Time Delay	SG2	24356	Float	4	R W	Millisecond	0–60000	GMT
87	Source	SG2	24358	Uint32	4	R W	n/a	CT1=0 CT2=1	GMT
87	Min Restrained Pickup Value	SG2	24360	Float	4	R W	n/a	0.1–1	GMT
87	Second Restrained Pickup Value	SG2	24362	Float	4	R W	n/a	1–20	GMT
87	Restraint Slope Mode	SG2	24364	Uint32	4	R W	n/a	Maximum=0 Average=1	GMT
87	Restraint Slope 1	SG2	24366	Float	4	R W	Percent	5–100	GMT
87	Restraint Slope 2	SG2	24368	Float	4	R W	Percent	15–140	GMT
87	Alarm Slope	SG2	24370	Float	4	R W	Percent	50–100	GMT
Reserved			24372-73						
87	2 nd Harmonic Restraint	SG2	24374	Float	4	R W	Percent	0–75	GT
87	5 th Harmonic	SG2	24376	Float	4	R W	Percent	0–75	GT
87	Shared Harmonic Restraint	SG2	24378	Uint32	4	R W	n/a	Independent=0 Shared=1	GMT
87	Unrestrained Pickup Mode	SG2	24380	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GMT
87	Unrestrained Pickup Value	SG2	24382	Float	4	R W	n/a	0–21	GMT
87	Flux Balance Pickup	SG2	24384	Float	4	R W	Amp	0.1–5	GMT
87	Flux Balance Alarm	SG2	24386	Float	4	R W	Amp	0.1–5	GMT
87	Mode	SG3	24388	Uint32	4	R W	n/a	Disabled=0 Percent Differential=1 Flux Balance=2	GMT
87	Time Delay	SG3	24390	Float	4	R W	Millisecond	0–60000	GMT
87	Source	SG3	24392	Uint32	4	R W	n/a	CT1=0 CT2=1	GMT
87	Min Restrained Pickup Value	SG3	24394	Float	4	R W	n/a	0.1–1	GMT
87	Second Restrained Pickup Value	SG3	24396	Float	4	R W	n/a	1–20	GMT
87	Restraint Slope Mode	SG3	24398	Uint32	4	R W	n/a	Maximum=0 Average=1	GMT
87	Restraint Slope 1	SG3	24400	Float	4	R W	Percent	5–100	GMT
87	Restraint Slope 2	SG3	24402	Float	4	R W	Percent	15–140	GMT
87	Alarm Slope	SG3	24404	Float	4	R W	Percent	50–100	GMT
Reserved			24406-07						
87	2 nd Harmonic Restraint	SG3	24408	Float	4	R W	Percent	0–75	GT
87	5 th Harmonic	SG3	24410	Float	4	R W	Percent	0–75	GT
87	Shared Harmonic Restraint	SG3	24412	Uint32	4	R W	n/a	Independent=0 Shared=1	GMT
87	Unrestrained Pickup Mode	SG3	24414	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GMT
87	Unrestrained Pickup Value	SG3	24416	Float	4	R W	n/a	0–21	GMT
87	Flux Balance Pickup	SG3	24418	Float	4	R W	Amp	0.1–5	GMT
87	Flux Balance Alarm	SG3	24420	Float	4	R W	Amp	0.1–5	GMT
Thermal Table Curves 1	Input Trip Mop Point	GG	24422	Float	4	R W	n/a	0–40	M

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Thermal Table Curves 1	Input Trip Time Point	GG	24424	Float	4	R W	n/a	0-9999999	M
Thermal Table Curves 1	Start Curve Input	GG	24426	Uint32	4	R W	n/a	1-4	M
Thermal Table Curves 1	Start Curve Output	GG	24428	Uint32	4	R W	n/a	1-4	M
ThermalTableCurveConfig1	Curve A Voltage	GG	24430	Float	4	R W	Percent	20-100	M
ThermalTableCurveConfig1	Curve B Voltage	GG	24432	Float	4	R W	Percent	20-100	M
ThermalTableCurveConfig1	Curve C Voltage	GG	24434	Float	4	R W	Percent	20-100	M
49TC	Mode	SG0	24436	Uint32	4	R W	n/a	Disabled=0 Standard Curve=1 IEC Curve=2 User Curve=3	M
Reserved			24438						
49TC	K	SG0	24440	Float	4	R W	n/a	0-20	M
49TC	IEC Time Constant	SG0	24442	Float	4	R W	Second	1-10000	M
49TC	Hot Curve Current	SG0	24444	Float	4	R W	Amp	0-25	M
49TC	Standard Curve TD	SG0	24446	Float	4	R W	n/a	1-20	M
49TC	Running Cool Time Constant	SG0	24448	Float	4	R W	Minute	1-1000	M
49TC	Stopped Cool Time Constant	SG0	24450	Float	4	R W	Minute	1-1000	M
49TC	Hot Safe Stall Time	SG0	24452	Float	4	R W	Second	1-2000	M
49TC	Cool Safe Stall Time	SG0	24454	Float	4	R W	Second	1-2000	M
49TC	Do RTD Biasing	SG0	24456	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
49TC	RTD Source	SG0	24458	Uint32	4	R W	n/a	RTD Group 1=0 RTD Group 2=1 RTD Group 3=2 RTD Group 4=3	M
49TC	Hot Start TC	SG0	24460	Float	4	R W	Percent	0-20	M
49TC	Max Emergency Thermal Capacity	SG0	24462	Float	4	R W	Percent	150-1000	M
49TC	Overload	SG0	24464	Float	4	R W	n/a	0.9-1.2	M
49TC	Mode	SG1	24466	Uint32	4	R W	n/a	Disabled=0 Standard Curve=1 IEC Curve=2 User Curve=3	M
Reserved			24468-69						
49TC	K	SG1	24470	Float	4	R W	n/a	0-20	M
49TC	IEC Time Constant	SG1	24472	Float	4	R W	Second	1-10000	M
49TC	Hot Curve Current	SG1	24474	Float	4	R W	Amp	0-25	M
49TC	Standard Curve TD	SG1	24476	Float	4	R W	n/a	1-20	M
49TC	Running Cool Time Constant	SG1	24478	Float	4	R W	Minute	1-1000	M
49TC	Stopped Cool Time Constant	SG1	24480	Float	4	R W	Minute	1-1000	M
49TC	Hot Safe Stall Time	SG1	24482	Float	4	R W	Second	1-2000	M
49TC	Cool Safe Stall Time	SG1	24484	Float	4	R W	Second	1-2000	M
49TC	Do RTD Biasing	SG1	24486	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
49TC	RTD Source	SG1	24488	Uint32	4	R W	n/a	RTD Group 1=0 RTD Group 2=1 RTD Group 3=2 RTD Group 4=3	M
49TC	Hot Start TC	SG1	24490	Float	4	R W	Percent	0-20	M
49TC	Max Emergency Thermal Capacity	SG1	24492	Float	4	R W	Percent	150-1000	M
49TC	Overload	SG1	24494	Float	4	R W	n/a	0.9-1.2	M
49TC	Mode	SG2	24496	Uint32	4	R W	n/a	Disabled=0 Standard Curve=1 IEC Curve=2 User Curve=3	M

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			24498-99						
49TC	K	SG2	24500	Float	4	R W	n/a	0–20	M
49TC	IEC Time Constant	SG2	24502	Float	4	R W	Second	1–10000	M
49TC	Hot Curve Current	SG2	24504	Float	4	R W	Amp	0–25	M
49TC	Standard Curve TD	SG2	24506	Float	4	R W	n/a	1–20	M
49TC	Running Cool Time Constant	SG2	24508	Float	4	R W	Minute	1–1000	M
49TC	Stopped Cool Time Constant	SG2	24510	Float	4	R W	Minute	1–1000	M
49TC	Hot Safe Stall Time	SG2	24512	Float	4	R W	Second	1–2000	M
49TC	Cool Safe Stall Time	SG2	24514	Float	4	R W	Second	1–2000	M
49TC	Do RTD Biasing	SG2	24516	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
49TC	RTD Source	SG2	24518	Uint32	4	R W	n/a	RTD Group 1=0 RTD Group 2 1 RTD Group 3=2 RTD Group 4=3	M
49TC	Hot Start TC	SG2	24520	Float	4	R W	Percent	0–20	M
49TC	Max Emergency Thermal Capacity	SG2	24522	Float	4	R W	Percent	150–1000	M
49TC	Overload	SG2	24524	Float	4	R W	n/a	0.9–1.2	M
49TC	Mode	SG3	24526	Uint32	4	R W	n/a	Disabled=0 Standard Curve=1 IEC Curve=2 User Curve=3	M
Reserved			24528-29						
49TC	K	SG3	24530	Float	4	R W	n/a	0–20	M
49TC	IEC Time Constant	SG3	24532	Float	4	R W	Second	1–10000	M
49TC	Hot Curve Current	SG3	24534	Float	4	R W	Amp	0–25	M
49TC	Standard Curve TD	SG3	24536	Float	4	R W	n/a	1–20	M
49TC	Running Cool Time Constant	SG3	24538	Float	4	R W	Minute	1–1000	M
49TC	Stopped Cool Time Constant	SG3	24540	Float	4	R W	Minute	1–1000	M
49TC	Hot Safe Stall Time	SG3	24542	Float	4	R W	Second	1–2000	M
49TC	Cool Safe Stall Time	SG3	24544	Float	4	R W	Second	1–2000	M
49TC	Do RTD Biasing	SG3	24546	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
49TC	RTD Source	SG3	24548	Uint32	4	R W	n/a	RTD Group 1=0 RTD Group 2=1 RTD Group 3=2 RTD Group 4=3	M
49TC	Hot Start TC	SG3	24550	Float	4	R W	Percent	0–20	M
49TC	Max Emergency Thermal Capacity	SG3	24552	Float	4	R W	Percent	150–1000	M
49TC	Overload	SG3	24554	Float	4	R W	n/a	0.9–1.2	M
50-7	Mode	SG0	24556	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-7	Source	SG0	24558	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-7	Pickup	SG0	24560	Float	4	R W	Amp	0.5–150	T
50-7	Time Delay	SG0	24562	Float	4	R W	Millisecond	0–60000	T
50-7	Direction	SG0	24564	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-7	Mode	SG1	24566	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-7	Source	SG1	24568	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-7	Pickup	SG1	24570	Float	4	R W	Amp	0.5–150	T
50-7	Time Delay	SG1	24572	Float	4	R W	Millisecond	0–60000	T
50-7	Direction	SG1	24574	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-7	Mode	SG2	24576	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-7	Source	SG2	24578	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-7	Pickup	SG2	24580	Float	4	R W	Amp	0.5–150	T
50-7	Time Delay	SG2	24582	Float	4	R W	Millisecond	0–60000	T
50-7	Direction	SG2	24584	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-7	Mode	SG3	24586	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-7	Source	SG3	24588	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-7	Pickup	SG3	24590	Float	4	R W	Amp	0.5–150	T
50-7	Time Delay	SG3	24592	Float	4	R W	Millisecond	0–60000	T
50-7	Direction	SG3	24594	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-8	Mode	SG0	24596	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-8	Source	SG0	24598	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-8	Pickup	SG0	24600	Float	4	R W	Amp	0.5–150	T
50-8	Time Delay	SG0	24602	Float	4	R W	Millisecond	0–60000	T
50-8	Direction	SG0	24604	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-8	Mode	SG1	24606	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-8	Source	SG1	24608	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-8	Pickup	SG1	24610	Float	4	R W	Amp	0.5–150	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-8	Time Delay	SG1	24612	Float	4	R W	Millisecond	0-60000	T
50-8	Direction	SG1	24614	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-8	Mode	SG2	24616	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-8	Source	SG2	24618	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-8	Pickup	SG2	24620	Float	4	R W	Amp	0.5-150	T
50-8	Time Delay	SG2	24622	Float	4	R W	Millisecond	0-60000	T
50-8	Direction	SG2	24624	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-8	Mode	SG3	24626	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-8	Source	SG3	24628	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-8	Pickup	SG3	24630	Float	4	R W	Amp	0.5-150	T
50-8	Time Delay	SG3	24632	Float	4	R W	Millisecond	0-60000	T
50-8	Direction	SG3	24634	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-9	Mode	SG0	24636	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-9	Source	SG0	24638	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-9	Pickup	SG0	24640	Float	4	R W	Amp	0.5-150	T
50-9	Time Delay	SG0	24642	Float	4	R W	Millisecond	0-60000	T
50-9	Direction	SG0	24644	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-9	Mode	SG1	24646	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-9	Source	SG1	24648	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-9	Pickup	SG1	24650	Float	4	R W	Amp	0.5-150	T
50-9	Time Delay	SG1	24652	Float	4	R W	Millisecond	0-60000	T
50-9	Direction	SG1	24654	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-9	Mode	SG2	24656	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-9	Source	SG2	24658	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-9	Pickup	SG2	24660	Float	4	R W	Amp	0.5–150	T
50-9	Time Delay	SG2	24662	Float	4	R W	Millisecond	0–60000	T
50-9	Direction	SG2	24664	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
50-9	Mode	SG3	24666	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
50-9	Source	SG3	24668	Uint32	4	R W	n/a	CT1=0 CT2=1	T
50-9	Pickup	SG3	24670	Float	4	R W	Amp	0.5–150	T
50-9	Time Delay	SG3	24672	Float	4	R W	Millisecond	0–60000	T
50-9	Direction	SG3	24674	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-8	Mode	SG0	24676	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-8	Source	SG0	24678	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-8	Pickup	SG0	24680	Float	4	R W	Amp	0.5–16	T
51-8	Curve Index	SG0	24682	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-8	Direction	SG0	24684	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-8	Use Instantaneous Reset	SG0	24686	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-8	Constant TD	SG0	24688	Float	4	R W	n/a	0–9.9	T
51-8	Constant A	SG0	24690	Float	4	R W	n/a	0–600	T
51-8	Constant B	SG0	24692	Float	4	R W	n/a	0–25	T
51-8	Constant C	SG0	24694	Float	4	R W	n/a	0–1	T
51-8	Constant N	SG0	24696	Float	4	R W	n/a	0.5–2.5	T
51-8	Constant R	SG0	24698	Float	4	R W	n/a	0–30	T
51-8	Voltage Restraint Setpoint	SG0	24700	Float	4	R W	Volt	30–250	T
51-8	Voltage Restraint Mode	SG0	24702	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-8	Mode	SG1	24704	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-8	Source	SG1	24706	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-8	Pickup	SG1	24708	Float	4	R W	Amp	0.5–16	T
51-8	Curve Index	SG1	24710	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-8	Direction	SG1	24712	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-8	Use Instantaneous Reset	SG1	24714	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-8	Constant TD	SG1	24716	Float	4	R W	n/a	0–9.9	T
51-8	Constant A	SG1	24718	Float	4	R W	n/a	0–600	T
51-8	Constant B	SG1	24720	Float	4	R W	n/a	0–25	T
51-8	Constant C	SG1	24722	Float	4	R W	n/a	0–1	T
51-8	Constant N	SG1	24724	Float	4	R W	n/a	0.5–2.5	T
51-8	Constant R	SG1	24726	Float	4	R W	n/a	0–30	T
51-8	Voltage Restraint Setpoint	SG1	24728	Float	4	R W	Volt	30–250	T
51-8	Voltage Restraint Mode	SG1	24730	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-8	Mode	SG2	24732	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-8	Source	SG2	24734	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-8	Pickup	SG2	24736	Float	4	R W	Amp	0.5–16	T
51-8	Curve Index	SG2	24738	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-8	Direction	SG2	24740	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-8	Use Instantaneous Reset	SG2	24742	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-8	Constant TD	SG2	24744	Float	4	R W	n/a	0–9.9	T
51-8	Constant A	SG2	24746	Float	4	R W	n/a	0–600	T
51-8	Constant B	SG2	24748	Float	4	R W	n/a	0–25	T
51-8	Constant C	SG2	24750	Float	4	R W	n/a	0–1	T
51-8	Constant N	SG2	24752	Float	4	R W	n/a	0.5–2.5	T
51-8	Constant R	SG2	24754	Float	4	R W	n/a	0–30	T
51-8	Voltage Restraint Setpoint	SG2	24756	Float	4	R W	Volt	30–250	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-8	Voltage Restraint Mode	SG2	24758	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-8	Mode	SG3	24760	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-8	Source	SG3	24762	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-8	Pickup	SG3	24764	Float	4	R W	Amp	0.5–16	T
51-8	Curve Index	SG3	24766	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-8	Direction	SG3	24768	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-8	Use Instantaneous Reset	SG3	24770	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-8	Constant TD	SG3	24772	Float	4	R W	n/a	0–9.9	T
51-8	Constant A	SG3	24774	Float	4	R W	n/a	0–600	T
51-8	Constant B	SG3	24776	Float	4	R W	n/a	0–25	T
51-8	Constant C	SG3	24778	Float	4	R W	n/a	0–1	T
51-8	Constant N	SG3	24780	Float	4	R W	n/a	0.5–2.5	T
51-8	Constant R	SG3	24782	Float	4	R W	n/a	0–30	T
51-8	Voltage Restraint Setpoint	SG3	24784	Float	4	R W	Volt	30–250	T
51-8	Voltage Restraint Mode	SG3	24786	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-9	Mode	SG0	24788	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-9	Source	SG0	24790	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-9	Pickup	SG0	24792	Float	4	R W	Amp	0.5–16	T
51-9	Curve Index	SG0	24794	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-9	Direction	SG0	24796	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-9	Use Instantaneous Reset	SG0	24798	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-9	Constant TD	SG0	24800	Float	4	R W	n/a	0–9.9	T
51-9	Constant A	SG0	24802	Float	4	R W	n/a	0–600	T
51-9	Constant B	SG0	24804	Float	4	R W	n/a	0–25	T
51-9	Constant C	SG0	24806	Float	4	R W	n/a	0–1	T
51-9	Constant N	SG0	24808	Float	4	R W	n/a	0.5–2.5	T
51-9	Constant R	SG0	24810	Float	4	R W	n/a	0–30	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-9	Voltage Restraint Setpoint	SG0	24812	Float	4	R W	Volt	30–250	T
51-9	Voltage Restraint Mode	SG0	24814	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-9	Mode	SG1	24816	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-9	Source	SG1	24818	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-9	Pickup	SG1	24820	Float	4	R W	Amp	0.5–16	T
51-9	Curve Index	SG1	24822	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-9	Direction	SG1	24824	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-9	Use Instantaneous Reset	SG1	24826	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-9	Constant TD	SG1	24828	Float	4	R W	n/a	0–9.9	T
51-9	Constant A	SG1	24830	Float	4	R W	n/a	0–600	T
51-9	Constant B	SG1	24832	Float	4	R W	n/a	0–25	T
51-9	Constant C	SG1	24834	Float	4	R W	n/a	0–1	T
51-9	Constant N	SG1	24836	Float	4	R W	n/a	0.5–2.5	T
51-9	Constant R	SG1	24838	Float	4	R W	n/a	0–30	T
51-9	Voltage Restraint Setpoint	SG1	24840	Float	4	R W	Volt	30–250	T
51-9	Voltage Restraint Mode	SG1	24842	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-9	Mode	SG2	24844	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-9	Source	SG2	24846	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-9	Pickup	SG2	24848	Float	4	R W	Amp	0.5–16	T
51-9	Curve Index	SG2	24850	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-9	Direction	SG2	24852	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-9	Use Instantaneous Reset	SG2	24854	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-9	Constant TD	SG2	24856	Float	4	R W	n/a	0–9.9	T
51-9	Constant A	SG2	24858	Float	4	R W	n/a	0–600	T
51-9	Constant B	SG2	24860	Float	4	R W	n/a	0–25	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-9	Constant C	SG2	24862	Float	4	R W	n/a	0-1	T
51-9	Constant N	SG2	24864	Float	4	R W	n/a	0.5-2.5	T
51-9	Constant R	SG2	24866	Float	4	R W	n/a	0-30	T
51-9	Voltage Restraint Setpoint	SG2	24868	Float	4	R W	Volt	30-250	T
51-9	Voltage Restraint Mode	SG2	24870	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
51-9	Mode	SG3	24872	Uint32	4	R W	n/a	Disabled=0 IA=1 IB=2 IC=3 3PH=4 3I0=5 I2=6 IG=7 I1=8 Unbalance=9	T
51-9	Source	SG3	24874	Uint32	4	R W	n/a	CT1=0 CT2=1	T
51-9	Pickup	SG3	24876	Float	4	R W	Amp	0.5-16	T
51-9	Curve Index	SG3	24878	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	T
51-9	Direction	SG3	24880	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	T
51-9	Use Instantaneous Reset	SG3	24882	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	T
51-9	Constant TD	SG3	24884	Float	4	R W	n/a	0-9.9	T
51-9	Constant A	SG3	24886	Float	4	R W	n/a	0-600	T
51-9	Constant B	SG3	24888	Float	4	R W	n/a	0-25	T
51-9	Constant C	SG3	24890	Float	4	R W	n/a	0-1	T
51-9	Constant N	SG3	24892	Float	4	R W	n/a	0.5-2.5	T
51-9	Constant R	SG3	24894	Float	4	R W	n/a	0-30	T
51-9	Voltage Restraint Setpoint	SG3	24896	Float	4	R W	Volt	30-250	T
51-9	Voltage Restraint Mode	SG3	24898	Uint32	4	R W	n/a	Disabled=0 Control=1 Restraint=2	T
87	Transient Operate Time	SG0	24900	Float	4	R W	Millisecond	0-10000	G
87	Transient Delay Time	SG0	24902	Float	4	R W	Millisecond	0-10000	G
87	Transient Operate Time	SG1	24904	Float	4	R W	Millisecond	0-10000	G
87	Transient Delay Time	SG1	24906	Float	4	R W	Millisecond	0-10000	G
87	Transient Operate Time	SG2	24908	Float	4	R W	Millisecond	0-10000	G
87	Transient Delay Time	SG2	24910	Float	4	R W	Millisecond	0-10000	G
87	Transient Operate Time	SG3	24912	Float	4	R W	Millisecond	0-10000	G
87	Transient Delay Time	SG3	24914	Float	4	R W	Millisecond	0-10000	G
55	Mode	SG0	24916	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
Reserved			24918-19						
55	Lagging Pickup	SG0	24920	Float	4	R W	n/a	0.05-0.99	M
55	Leading Pickup	SG0	24922	Float	4	R W	n/a	0.05-0.99	M
55	Time Delay	SG0	24924	Float	4	R W	Millisecond	50-60000	M
55	Mode	SG1	24926	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
Reserved			24928-29						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
55	Lagging Pickup	SG1	24930	Float	4	R W	n/a	0.05–0.99	M
55	Leading Pickup	SG1	24932	Float	4	R W	n/a	0.05–0.99	M
55	Time Delay	SG1	24934	Float	4	R W	Millisecond	50–60000	M
55	Mode	SG2	24936	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
Reserved			24938-39						
55	Lagging Pickup	SG2	24940	Float	4	R W	n/a	0.05–0.99	M
55	Leading Pickup	SG2	24942	Float	4	R W	n/a	0.05–0.99	M
55	Time Delay	SG2	24944	Float	4	R W	Millisecond	50–60000	M
55	Mode	SG3	24946	Uint32	4	R W	n/a	Disabled=0 Enabled=1	M
Reserved			24948-49						
55	Lagging Pickup	SG3	24950	Float	4	R W	n/a	0.05–0.99	M
55	Leading Pickup	SG3	24952	Float	4	R W	n/a	0.05–0.99	M
55	Time Delay	SG3	24954	Float	4	R W	Millisecond	50–60000	M
21-1	Mode	SG0	24956	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			24958-59						
21-1	Diameter	SG0	24960	Float	4	R W	Ohm	0–500	FGT
21-1	Time Delay	SG0	24962	Float	4	R W	Millisecond	0–300000	FGT
21-1	Torque Angle	SG0	24964	Float	4	R W	Degree	0–90	FGT
21-1	Offset	SG0	24966	Float	4	R W	Ohm	(-500)–500	FGT
21-1	Mode	SG1	24968	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			24970-71						
21-1	Diameter	SG1	24972	Float	4	R W	Ohm	0–500	FGT
21-1	Time Delay	SG1	24974	Float	4	R W	Millisecond	0–300000	FGT
21-1	Torque Angle	SG1	24976	Float	4	R W	Degree	0–90	FGT
21-1	Offset	SG1	24978	Float	4	R W	Ohm	(-500)–500	FGT
21-1	Mode	SG2	24980	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			24982-83						
21-1	Diameter	SG2	24984	Float	4	R W	Ohm	0–500	FGT
21-1	Time Delay	SG2	24986	Float	4	R W	Millisecond	0–300000	FGT
21-1	Torque Angle	SG2	24988	Float	4	R W	Degree	0–90	FGT
21-1	Offset	SG2	24990	Float	4	R W	Ohm	(-500)–500	FGT
21-1	Mode	SG3	24992	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			24994-95						
21-1	Diameter	SG3	24996	Float	4	R W	Ohm	0–500	FGT
21-1	Time Delay	SG3	24998	Float	4	R W	Millisecond	0–300000	FGT
21-1	Torque Angle	SG3	25000	Float	4	R W	Degree	0–90	FGT
21-1	Offset	SG3	25002	Float	4	R W	Ohm	(-500)–500	FGT
21-2	Mode	SG0	25004	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			25006-07						
21-2	Diameter	SG0	25008	Float	4	R W	Ohm	0–500	FGT
21-2	Time Delay	SG0	25010	Float	4	R W	Millisecond	0–300000	FGT
21-2	Torque Angle	SG0	25012	Float	4	R W	Degree	0–90	FGT
21-2	Offset	SG0	25014	Float	4	R W	Ohm	(-500)–500	FGT
21-2	Mode	SG1	25016	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			25018-19						
21-2	Diameter	SG1	25020	Float	4	R W	Ohm	0–500	FGT
21-2	Time Delay	SG1	25022	Float	4	R W	Millisecond	0–300000	FGT
21-2	Torque Angle	SG1	25224	Float	4	R W	Degree	0–90	FGT
21-2	Offset	SG1	25026	Float	4	R W	Ohm	(-500)–500	FGT
21-2	Mode	SG2	25028	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			25030-31						
21-2	Diameter	SG2	25032	Float	4	R W	Ohm	0–500	FGT
21-2	Time Delay	SG2	25034	Float	4	R W	Millisecond	0–300000	FGT
21-2	Torque Angle	SG2	25036	Float	4	R W	Degree	0–90	FGT
21-2	Offset	SG2	25038	Float	4	R W	Ohm	(-500)–500	FGT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
21-2	Mode	SG3	25040	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGT
Reserved			25042-43						
21-2	Diameter	SG3	25044	Float	4	R W	Ohm	0-500	FGT
21-2	Time Delay	SG3	25046	Float	4	R W	Millisecond	0-300000	FGT
21-2	Torque Angle	SG3	25048	Float	4	R W	Degree	0-90	FGT
21-2	Offset	SG3	25050	Float	4	R W	Ohm	(-500)-500	FGT
78V	Mode	SG0	25052	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GI
78V	Source	SG0	25054	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	GI
78V	Pickup	SG0	25056	Uint32	4	R W	n/a	2-90	GI
78V	Mode	SG1	25058	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GI
78V	Source	SG1	25060	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	GI
78V	Pickup	SG1	25062	Uint32	4	R W	n/a	2-90	GI
78V	Mode	SG2	25064	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GI
78V	Source	SG2	25066	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	GI
78V	Pickup	SG2	25068	Uint32	4	R W	n/a	2-90	GI
78V	Mode	SG3	25070	Uint32	4	R W	n/a	Disabled=0 Enabled=1	GI
78V	Source	SG3	25072	Uint32	4	R W	n/a	Phase VT=0 Aux VT=1	GI
78V	Pickup	SG3	25074	Uint32	4	R W	n/a	2-90	GI
7800S	Mode	SG0	25076	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
Reserved			25078-77						
7800S	Reverse Reach	SG0	25080	Float	4	R W	Ohm	0-500	G
7800S	Forward Reach	SG0	25082	Float	4	R W	Ohm	0-500	G
7800S	Blinder A Offset	SG0	25084	Float	4	R W	Ohm	0-500	G
7800S	Blinder B Offset	SG0	25086	Float	4	R W	Ohm	0-500	G
7800S	Blinder Angle	SG0	25088	Float	4	R W	Degree	1-90	G
7800S	Blinder Traverse Time Delay	SG0	25090	Float	4	R W	Millisecond	0-10000	G
7800S	Trip Delay	SG0	25092	Float	4	R W	Millisecond	0-5000	G
7800S	Min I1	SG0	25094	Float	4	R W	Percent	5-600	G
7800S	I2 Ratio	SG0	25096	Float	4	R W	Percent	10-200	G
7800S	Mode	SG1	25098	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
Reserved			25100-01						
7800S	Reverse Reach	SG1	25102	Float	4	R W	Ohm	0-500	G
7800S	Forward Reach	SG1	25104	Float	4	R W	Ohm	0-500	G
7800S	Blinder A Offset	SG1	25106	Float	4	R W	Ohm	0-500	G
7800S	Blinder B Offset	SG1	25108	Float	4	R W	Ohm	0-500	G
7800S	Blinder Angle	SG1	25110	Float	4	R W	Degree	1-90	G
7800S	Blinder Traverse Time Delay	SG1	25112	Float	4	R W	Millisecond	0-10000	G
7800S	Trip Delay	SG1	25114	Float	4	R W	Millisecond	0-5000	G
7800S	Min I1	SG1	25116	Float	4	R W	Percent	5-600	G
7800S	I2 Ratio	SG1	25118	Float	4	R W	Percent	10-200	G
7800S	Mode	SG2	25120	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
Reserved			25122-23						
7800S	Reverse Reach	SG2	25124	Float	4	R W	Ohm	0-500	G
7800S	Forward Reach	SG2	25126	Float	4	R W	Ohm	0-500	G
7800S	Blinder A Offset	SG2	25128	Float	4	R W	Ohm	0-500	G
7800S	Blinder B Offset	SG2	25130	Float	4	R W	Ohm	0-500	G
7800S	Blinder Angle	SG2	25132	Float	4	R W	Degree	1-90	G

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
7800S	Blinder Traverse Time Delay	SG2	25134	Float	4	R W	Millisecond	0-10000	G
7800S	Trip Delay	SG2	25136	Float	4	R W	Millisecond	0-5000	G
7800S	Min I1	SG2	25138	Float	4	R W	Percent	5-600	G
7800S	I2 Ratio	SG2	25140	Float	4	R W	Percent	10-200	G
7800S	Mode	SG3	25142	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
Reserved			25144-45						
7800S	Reverse Reach	SG3	25146	Float	4	R W	Ohm	0-500	G
7800S	Forward Reach	SG3	25148	Float	4	R W	Ohm	0-500	G
7800S	Blinder A Offset	SG3	25150	Float	4	R W	Ohm	0-500	G
7800S	Blinder B Offset	SG3	25152	Float	4	R W	Ohm	0-500	G
7800S	Blinder Angle	SG3	25154	Float	4	R W	Degree	1-90	G
7800S	Blinder Traverse Time Delay	SG3	25156	Float	4	R W	Millisecond	0-10000	G
7800S	Trip Delay	SG3	25158	Float	4	R W	Millisecond	0-5000	G
7800S	Min I1	SG3	25160	Float	4	R W	Percent	5-600	G
7800S	I2 Ratio	SG3	25162	Float	4	R W	Percent	10-200	G
25A	Mode	SG0	25164	Uint32	4	R W	n/a	Disabled=0 Phase Lock Loop=1 Anticipatory=2	G
Reserved			25166-69						
25A	Angle Compensation	SG0	25170	Float	4	R W	Degree	0-359.9	G
25A	Slip Frequency	SG0	25172	Float	4	R W	Hertz	0-0.5	G
25A	Min Slip Control	SG0	25174	Float	4	R W	Hertz	0-2	G
25A	Max Slip Control	SG0	25176	Float	4	R W	Hertz	0-2	G
25A	Voltage Difference	SG0	25178	Float	4	R W	Percent	2-15	G
25A	Breaker Close Angle	SG0	25180	Float	4	R W	Degree	3-20	G
25A	Breaker Close Time	SG0	25182	Uint32	4	R W	Millisecond	0-1000	G
25A	Sync Fail Activation Delay	SG0	25184	Float	4	R W	Second	0.1-600	G
Reserved			25186-89						
25A	Freq Source > Freq Destination	SG0	25190	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Volt Source > Volt Destination	SG0	25192	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Output Mode	SG0	25194	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Frequency Output Mode	SG0	25196	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Dead Voltage	SG0	25198	Uint32	4	R W	n/a	10-90	G
25A	Live Voltage	SG0	25200	Uint32	4	R W	n/a	10-90	G
25A	Drop Out Delay	SG0	25202	Uint32	4	R W	Millisecond	50-60000	G
25A	VM Dead Line Dead Aux	SG0	25204	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Dead Line Live Aux	SG0	25206	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Live Line Dead Aux	SG0	25208	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Pulse Width	SG0	25210	Float	4	R W	Second	0.1-5	G
25A	Voltage Pulse Interval	SG0	25212	Float	4	R W	Second	0.2-10	G
25A	Frequency Pulse Width	SG0	25214	Float	4	R W	Second	0-99.9	G
25A	Frequency Pulse Interval	SG0	25216	Float	4	R W	Second	0-99.9	G
25A	Breaker Close Attempts	SG0	25218	Uint32	4	R W	n/a	0-5	G

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
25A	Mode	SG1	25220	Uint32	4	R W	n/a	Disabled=0 Phase Lock Loop=1 Anticipatory=2	G
Reserved			25222-25						
25A	Angle Compensation	SG1	25226	Float	4	R W	Degree	0–359.9	G
25A	Slip Frequency	SG1	25228	Float	4	R W	Hertz	0–0.5	G
25A	Min Slip Control	SG1	25230	Float	4	R W	Hertz	0–2	G
25A	Max Slip Control	SG1	25232	Float	4	R W	Hertz	0–2	G
25A	Voltage Difference	SG1	25234	Float	4	R W	Percent	2–15	G
25A	Breaker Close Angle	SG1	25236	Float	4	R W	Degree	3–20	G
25A	Breaker Close Time	SG1	25238	Uint32	4	R W	Millisecond	0–1000	G
25A	Sync Fail Activation Delay	SG1	25240	Float	4	R W	Second	0.1–600	G
Reserved			25242-45						
25A	Freq Source > Freq Destination	SG1	25246	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Volt Source > Volt Destination	SG1	25248	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Output Mode	SG1	25250	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Frequency Output Mode	SG1	25252	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Dead Voltage	SG1	25254	Uint32	4	R W	n/a	10–90	G
25A	Live Voltage	SG1	25256	Uint32	4	R W	n/a	10–90	G
25A	Drop Out Delay	SG1	25258	Uint32	4	R W	Millisecond	50–60000	G
25A	VM Dead Line Dead Aux	SG1	25260	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Dead Line Live Aux	SG1	25262	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Live Line Dead Aux	SG1	25264	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Pulse Width	SG1	25266	Float	4	R W	Second	0.1–5	G
25A	Voltage Pulse Interval	SG1	25268	Float	4	R W	Second	0.2–10	G
25A	Frequency Pulse Width	SG1	25070	Float	4	R W	Second	0–99.9	G
25A	Frequency Pulse Interval	SG1	25272	Float	4	R W	Second	0–99.9	G
25A	Breaker Close Attempts	SG1	25274	Uint32	4	R W	n/a	0–5	G
25A	Mode	SG2	25276	Uint32	4	R W	n/a	Disabled=0 Phase Lock Loop=1 Anticipatory=2	G
Reserved			25278-81						
25A	Angle Compensation	SG2	25282	Float	4	R W	Degree	0–359.9	G
25A	Slip Frequency	SG2	25284	Float	4	R W	Hertz	0–0.5	G
25A	Min Slip Control	SG2	25286	Float	4	R W	Hertz	0–2	G
25A	Max Slip Control	SG2	25288	Float	4	R W	Hertz	0–2	G
25A	Voltage Difference	SG2	25290	Float	4	R W	Percent	2–15	G
25A	Breaker Close Angle	SG2	25292	Float	4	R W	Degree	3–20	G
25A	Breaker Close Time	SG2	25294	Uint32	4	R W	Millisecond	0–1000	G
25A	Sync Fail Activation Delay	SG2	25296	Float	4	R W	Second	0.1–600	G
Reserved			25298-301						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
25A	Freq Source > Freq Destination	SG2	25302	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Volt Source > Volt Destination	SG2	25304	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Output Mode	SG2	25306	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Frequency Output Mode	SG2	25308	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Dead Voltage	SG2	25310	Uint32	4	R W	n/a	10-90	G
25A	Live Voltage	SG2	25312	Uint32	4	R W	n/a	10-90	G
25A	Drop Out Delay	SG2	25314	Uint32	4	R W	Millisecond	50-60000	G
25A	VM Dead Line Dead Aux	SG2	25316	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Dead Line Live Aux	SG2	25318	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Live Line Dead Aux	SG2	25320	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Pulse Width	SG2	25322	Float	4	R W	Second	0.1-5	G
25A	Voltage Pulse Interval	SG2	25324	Float	4	R W	Second	0.2-10	G
25A	Frequency Pulse Width	SG2	25326	Float	4	R W	Second	0-99.9	G
25A	Frequency Pulse Interval	SG2	25328	Float	4	R W	Second	0-99.9	G
25A	Breaker Close Attempts	SG2	25330	Uint32	4	R W	n/a	0-5	G
25A	Mode	SG3	25332	Uint32	4	R W	n/a	Disabled=0 Phase Lock Loop=1 Anticipatory=2	G
Reserved			25334-37						
25A	Angle Compensation	SG3	25338	Float	4	R W	Degree	0-359.9	G
25A	Slip Frequency	SG3	25340	Float	4	R W	Hertz	0-0.5	G
25A	Min Slip Control	SG3	25342	Float	4	R W	Hertz	0-2	G
25A	Max Slip Control	SG3	25344	Float	4	R W	Hertz	0-2	G
25A	Voltage Difference	SG3	25346	Float	4	R W	Percent	2-15	G
25A	Breaker Close Angle	SG3	25348	Float	4	R W	Degree	3-20	G
25A	Breaker Close Time	SG3	25350	Uint32	4	R W	Millisecond	0-1000	G
25A	Sync Fail Activation Delay	SG3	25352	Float	4	R W	Second	0.1-600	G
Reserved			25354-57						
25A	Freq Source > Freq Destination	SG3	25358	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Volt Source > Volt Destination	SG3	25360	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Output Mode	SG3	25362	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Frequency Output Mode	SG3	25364	Uint32	4	R W	n/a	Continuous=0 Proportional=1	G
25A	Dead Voltage	SG3	25366	Uint32	4	R W	n/a	10-90	G
25A	Live Voltage	SG3	25368	Uint32	4	R W	n/a	10-90	G
25A	Drop Out Delay	SG3	25370	Uint32	4	R W	Millisecond	50-60000	G
25A	VM Dead Line Dead Aux	SG3	25372	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Dead Line Live Aux	SG3	25374	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	VM Live Line Dead Aux	SG3	25376	Uint32	4	R W	n/a	Disabled=0 Enabled=1	G
25A	Voltage Pulse Width	SG3	25378	Float	4	R W	Second	0.1-5	G
25A	Voltage Pulse Interval	SG3	25380	Float	4	R W	Second	0.2-10	G
25A	Frequency Pulse Width	SG3	25382	Float	4	R W	Second	0-99.9	G

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
25A	Frequency Pulse Interval	SG3	25384	Float	4	R W	Second	0–99.9	G
25A	Breaker Close Attempts	SG3	25386	Uint32	4	R W	n/a	0–5	G
67-1 Load Encroachment	Mode	SG0	25388	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-1 Load Encroachment	Minimum Forward Load	SG0	25390	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Minimum Reverse Load	SG0	25392	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Positive Forward Angle	SG0	25394	Float	4	R W	Degree	0–90	FI
67-1 Load Encroachment	Negative Forward Angle	SG0	25396	Float	4	R W	Degree	(-90)–0	FI
67-1 Load Encroachment	Positive Reverse Angle	SG0	25398	Float	4	R W	Degree	90–180	FI
67-1 Load Encroachment	Negative Reverse Angle	SG0	25400	Float	4	R W	Degree	180–270	FI
67-1 Load Encroachment	Mode	SG1	25402	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-1 Load Encroachment	Minimum Forward Load	SG1	25404	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Minimum Reverse Load	SG1	25406	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Positive Forward Angle	SG1	25408	Float	4	R W	Degree	0–90	FI
67-1 Load Encroachment	Negative Forward Angle	SG1	25410	Float	4	R W	Degree	(-90)–0	FI
67-1 Load Encroachment	Positive Reverse Angle	SG1	25412	Float	4	R W	Degree	90–180	FI
67-1 Load Encroachment	Negative Reverse Angle	SG1	25414	Float	4	R W	Degree	180–270	FI
67-1 Load Encroachment	Mode	SG2	25416	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-1 Load Encroachment	Minimum Forward Load	SG2	25418	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Minimum Reverse Load	SG2	25420	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Positive Forward Angle	SG2	25422	Float	4	R W	Degree	0–90	FI
67-1 Load Encroachment	Negative Forward Angle	SG2	25424	Float	4	R W	Degree	(-90)–0	FI
67-1 Load Encroachment	Positive Reverse Angle	SG2	25426	Float	4	R W	Degree	90–180	FI
67-1 Load Encroachment	Negative Reverse Angle	SG2	25428	Float	4	R W	Degree	180–270	FI
67-1 Load Encroachment	Mode	SG3	25430	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-1 Load Encroachment	Minimum Forward Load	SG3	25432	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Minimum Reverse Load	SG3	25434	Float	4	R W	Ohm	0.09–640	FI
67-1 Load Encroachment	Positive Forward Angle	SG3	25436	Float	4	R W	Degree	0–90	FI
67-1 Load Encroachment	Negative Forward Angle	SG3	25438	Float	4	R W	Degree	(-90)–0	FI
67-1 Load Encroachment	Positive Reverse Angle	SG3	25440	Float	4	R W	Degree	90–180	FI
67-1 Load Encroachment	Negative Reverse Angle	SG3	25442	Float	4	R W	Degree	180–270	FI
87N-2	Mode	SG0	25444	Uint32	4	R W	n/a	Disabled=0 Enabled=1	T
87N-2	CT Source	SG0	25446	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	T
Reserved			25448						
87N-2	Iop Minimum	SG0	25450	Float	4	R W	Amp	0.1–5	T
87N-2	Time Delay	SG0	25452	Float	4	R W	Millisecond	0–60000	T
87N-2	Overcorrection Coefficient	SG0	25454	Float	4	R W	n/a	1–1.3	T
87N-2	CT Flip	SG0	25456	Uint32	4	R W	n/a	No=0 Yes=1	T
87N-2	Mode	SG1	25458	Uint32	4	R W	n/a	Disabled=0 Enabled=1	T
87N-2	CT Source	SG1	25460	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	T
Reserved			25462						
87N-2	Iop Minimum	SG1	25464	Float	4	R W	Amp	0.1–5	T
87N-2	Time Delay	SG1	25466	Float	4	R W	Millisecond	0–60000	T
87N-2	Overcorrection Coefficient	SG1	25468	Float	4	R W	n/a	1–1.3	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
87N-2	CT Flip	SG1	25470	Uint32	4	R W	n/a	No=0 Yes=1	T
87N-2	Mode	SG2	25472	Uint32	4	R W	n/a	Disabled=0 Enabled=1	T
87N-2	CT Source	SG2	25474	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	T
Reserved			25476						
87N-2	Iop Minimum	SG2	25478	Float	4	R W	Amp	0.1-5	T
87N-2	Time Delay	SG2	25480	Float	4	R W	Millisecond	0-60000	T
87N-2	Overcorrection Coefficient	SG2	25482	Float	4	R W	n/a	1-1.3	T
87N-2	CT Flip	SG2	25484	Uint32	4	R W	n/a	No=0 Yes=1	T
87N-2	Mode	SG3	25486	Uint32	4	R W	n/a	Disabled=0 Enabled=1	T
87N-2	CT Source	SG3	25488	Uint32	4	R W	n/a	CT1 - IG1=0 CT2 - IG2=1	T
Reserved			25490						
87N-2	Iop Minimum	SG3	25492	Float	4	R W	Amp	0.1-5	T
87N-2	Time Delay	SG3	25494	Float	4	R W	Millisecond	0-60000	T
87N-2	Overcorrection Coefficient	SG3	25496	Float	4	R W	n/a	1-1.3	T
87N-2	CT Flip	SG3	25498	Uint32	4	R W	n/a	No=0 Yes=1	T
49RTD-7	Mode	SG0	25500	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-7	Source	SG0	25502	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-7	Over Pickup	SG0	25504	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Under Pickup	SG0	25506	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Time Delay	SG0	25508	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-7	Voting	SG0	25510	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-7	Mode	SG1	25512	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-7	Source	SG1	25514	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-7	Over Pickup	SG1	25516	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Under Pickup	SG1	25518	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Time Delay	SG1	25520	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-7	Voting	SG1	25522	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-7	Mode	SG2	25524	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-7	Source	SG2	25526	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-7	Over Pickup	SG2	25528	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Under Pickup	SG2	25530	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Time Delay	SG2	25532	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-7	Voting	SG2	25534	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-7	Mode	SG3	25536	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-7	Source	SG3	25538	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-7	Over Pickup	SG3	25540	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Under Pickup	SG3	25542	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-7	Time Delay	SG3	25544	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-7	Voting	SG3	25546	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-8	Mode	SG0	25548	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-8	Source	SG0	25550	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-8	Over Pickup	SG0	25552	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Under Pickup	SG0	25554	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Time Delay	SG0	25556	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-8	Voting	SG0	25558	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-8	Mode	SG1	25560	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-8	Source	SG1	25562	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-8	Over Pickup	SG1	25564	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Under Pickup	SG1	25566	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-8	Time Delay	SG1	25568	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-8	Voting	SG1	25570	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-8	Mode	SG2	25572	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-8	Source	SG2	25574	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-8	Over Pickup	SG2	25576	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Under Pickup	SG2	25578	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Time Delay	SG2	25580	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-8	Voting	SG2	25582	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-8	Mode	SG3	25584	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-8	Source	SG3	25586	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-8	Over Pickup	SG3	25588	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Under Pickup	SG3	25590	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-8	Time Delay	SG3	25592	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-8	Voting	SG3	25594	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-9	Mode	SG0	25596	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-9	Source	SG0	25598	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-9	Over Pickup	SG0	25600	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Under Pickup	SG0	25602	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Time Delay	SG0	25604	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-9	Voting	SG0	25606	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-9	Mode	SG1	25608	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-9	Source	SG1	25610	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-9	Over Pickup	SG1	25612	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Under Pickup	SG1	25614	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Time Delay	SG1	25616	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-9	Voting	SG1	25618	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-9	Mode	SG2	25620	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-9	Source	SG2	25622	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-9	Over Pickup	SG2	25624	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Under Pickup	SG2	25626	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Time Delay	SG2	25628	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-9	Voting	SG2	25630	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-9	Mode	SG3	25632	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-9	Source	SG3	25634	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-9	Over Pickup	SG3	25636	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Under Pickup	SG3	25638	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-9	Time Delay	SG3	25640	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-9	Voting	SG3	25642	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-10	Mode	SG0	25644	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-10	Source	SG0	25646	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-10	Over Pickup	SG0	25648	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Under Pickup	SG0	25650	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-10	Time Delay	SG0	25652	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-10	Voting	SG0	25654	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-10	Mode	SG1	25656	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-10	Source	SG1	25658	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-10	Over Pickup	SG1	25660	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Under Pickup	SG1	25662	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Time Delay	SG1	25664	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-10	Voting	SG1	25666	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-10	Mode	SG2	25668	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-10	Source	SG2	25670	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-10	Over Pickup	SG2	25672	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Under Pickup	SG2	25674	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Time Delay	SG2	25676	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-10	Voting	SG2	25678	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-10	Mode	SG3	25680	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-10	Source	SG3	25682	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-10	Over Pickup	SG3	25684	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Under Pickup	SG3	25686	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-10	Time Delay	SG3	25688	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-10	Voting	SG3	25690	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-11	Mode	SG0	25692	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-11	Source	SG0	25694	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-11	Over Pickup	SG0	25696	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Under Pickup	SG0	25698	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Time Delay	SG0	25700	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-11	Voting	SG0	25702	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-11	Mode	SG1	25704	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-11	Source	SG1	25706	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-11	Over Pickup	SG1	25708	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Under Pickup	SG1	25710	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Time Delay	SG1	25712	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-11	Voting	SG1	25714	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-11	Mode	SG2	25716	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-11	Source	SG2	25718	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-11	Over Pickup	SG2	25720	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Under Pickup	SG2	25722	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Time Delay	SG2	25724	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-11	Voting	SG2	25726	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-11	Mode	SG3	25728	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-11	Source	SG3	25730	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-11	Over Pickup	SG3	25732	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-11	Under Pickup	SG3	25734	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-11	Time Delay	SG3	25736	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-11	Voting	SG3	25738	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-12	Mode	SG0	25740	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-12	Source	SG0	25742	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-12	Over Pickup	SG0	25744	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Under Pickup	SG0	25746	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Time Delay	SG0	25748	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-12	Voting	SG0	25750	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-12	Mode	SG1	25752	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-12	Source	SG1	25754	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-12	Over Pickup	SG1	25756	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Under Pickup	SG1	25758	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Time Delay	SG1	25760	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-12	Voting	SG1	25762	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-12	Mode	SG2	25764	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-12	Source	SG2	25766	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-12	Over Pickup	SG2	25768	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Under Pickup	SG2	25770	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Time Delay	SG2	25772	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-12	Voting	SG2	25774	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-12	Mode	SG3	25776	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-12	Source	SG3	25778	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-12	Over Pickup	SG3	25780	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Under Pickup	SG3	25782	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-12	Time Delay	SG3	25784	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-12	Voting	SG3	25786	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-13	Mode	SG0	25788	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-13	Source	SG0	25790	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-13	Over Pickup	SG0	25792	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Under Pickup	SG0	25794	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Time Delay	SG0	25796	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-13	Voting	SG0	25798	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-13	Mode	SG1	25800	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-13	Source	SG1	25802	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-13	Over Pickup	SG1	25804	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Under Pickup	SG1	25806	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Time Delay	SG1	25808	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-13	Voting	SG1	25810	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-13	Mode	SG2	25812	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-13	Source	SG2	25814	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-13	Over Pickup	SG2	25816	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Under Pickup	SG2	25818	Float	4	R W	Degrees C	0-250	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-13	Time Delay	SG2	25820	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-13	Voting	SG2	25822	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-13	Mode	SG3	25824	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-13	Source	SG3	25826	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-13	Over Pickup	SG3	25828	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Under Pickup	SG3	25830	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-13	Time Delay	SG3	25832	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-13	Voting	SG3	25834	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-14	Mode	SG0	25836	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-14	Source	SG0	25838	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-14	Over Pickup	SG0	25840	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Under Pickup	SG0	25842	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Time Delay	SG0	25844	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-14	Voting	SG0	25846	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-14	Mode	SG1	25848	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-14	Source	SG1	25850	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-14	Over Pickup	SG1	25852	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Under Pickup	SG1	25854	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Time Delay	SG1	25856	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-14	Voting	SG1	25858	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-14	Mode	SG2	25860	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
49RTD-14	Source	SG2	25862	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-14	Over Pickup	SG2	25864	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Under Pickup	SG2	25866	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Time Delay	SG2	25868	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-14	Voting	SG2	25870	Uint32	4	R W	n/a	1-24	DFGIMT
49RTD-14	Mode	SG3	25872	Uint32	4	R W	n/a	Disabled=0 Over=1 Under=2 Over/Under=3	DFGIMT
49RTD-14	Source	SG3	25874	Uint32	4	R W	n/a	Configuration Group 1=0 Configuration Group 2=1 Configuration Group 3=2 Configuration Group 4=3 Configuration Group 5=4 Configuration Group 6=5 Configuration Group 7=6	DFGIMT
49RTD-14	Over Pickup	SG3	25876	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Under Pickup	SG3	25878	Float	4	R W	Degrees C	0-250	DFGIMT
49RTD-14	Time Delay	SG3	25880	Float	4	R W	Millisecond	0-600000	DFGIMT
49RTD-14	Voting	SG3	25882	Uint32	4	R W	n/a	1-24	DFGIMT
78V	Hold Time	SG0	25884	Uint32	4	R W	Millisecond	0-60000	GI
78V	Hold Time	SG1	25886	Uint32	4	R W	Millisecond	0-60000	GI
78V	Hold Time	SG2	25888	Uint32	4	R W	Millisecond	0-60000	GI
78V	Hold Time	SG3	25890	Uint32	4	R W	Millisecond	0-60000	GI
67-1	Current Source	SG0	25892	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-1	Current Source	SG1	25894	Unit32	4	RW	n/a	CT1=0 CT2=1	FGIT
67-1	Current Source	SG2	25896	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-1	Current Source	SG3	25898	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-2	Mode	SG0	25900	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			25902-03						
67-2	Zero Sequence Mode	SG0	25904	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-2	QVI Mode	SG0	25906	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-2	Load Encroachment Enabled	SG0	25908	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-2	Minimum Forward Load Impedance	SG0	25910	Float	4	R W	Ohm	0.09-640	FGIT
67-2	Minimum Reverse Load Impedance	SG0	25912	Float	4	R W	Ohm	0.09-640	FGIT
67-2	Positive Forward Angle	SG0	25914	Float	4	R W	Degree	0-90	FGIT
67-2	Negative Forward Angle	SG0	25916	Float	4	R W	Degree	(-90)-0	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
67-2	Positive Reverse Angle	SG0	25918	Float	4	R W	Degree	90–180	FGIT
67-2	Negative Reverse Angle	SG0	25920	Float	4	R W	Degree	180–270	FGIT
67-2	Current Source	SG0	25922	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-2	Mode	SG1	25924	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			25926-27						
67-2	Zero Sequence Mode	SG1	25928	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-2	QVI Mode	SG1	25930	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-2	Load Encroachment Enabled	SG1	25932	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-2	Minimum Forward Load Impedance	SG1	25934	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Minimum Reverse Load Impedance	SG1	25936	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Positive Forward Angle	SG1	25938	Float	4	R W	Degree	0–90	FGIT
67-2	Negative Forward Angle	SG1	25840	Float	4	R W	Degree	(-90)–0	FGIT
67-2	Positive Reverse Angle	SG1	25942	Float	4	R W	Degree	90–180	FGIT
67-2	Negative Reverse Angle	SG1	25944	Float	4	R W	Degree	180–270	FGIT
67-2	Current Source	SG1	25946	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-2	Mode	SG2	25948	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			25950-51						
67-2	Zero Sequence Mode	SG2	25952	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-2	QVI Mode	SG2	25954	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-2	Load Encroachment Enabled	SG2	25956	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-2	Minimum Forward Load Impedance	SG2	25958	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Minimum Reverse Load Impedance	SG2	25960	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Positive Forward Angle	SG2	25962	Float	4	R W	Degree	0–90	FGIT
67-2	Negative Forward Angle	SG2	25964	Float	4	R W	Degree	(-90)–0	FGIT
67-2	Positive Reverse Angle	SG2	25966	Float	4	R W	Degree	90–180	FGIT
67-2	Negative Reverse Angle	SG2	25968	Float	4	R W	Degree	180–270	FGIT
67-2	Current Source	SG2	25970	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
67-2	Mode	SG3	25972	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FGIT
Reserved			25974-75						

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
67-2	Zero Sequence Mode	SG3	25976	Uint32	4	R W	n/a	VOIN=0 VOIG=1 VXIN=2 VXIG=3	FGIT
67-2	QVI Mode	SG3	25978	Uint32	4	R W	n/a	I=1 V=2 VI=3 Q=4 QI=5 QV=6 QVI=7	FGIT
67-2	Load Encroachment Enabled	SG3	25980	Uint32	4	R W	n/a	Disabled=0 Enabled=1	FI
67-2	Minimum Forward Load Impedance	SG3	25982	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Minimum Reverse Load Impedance	SG3	25984	Float	4	R W	Ohm	0.09–640	FGIT
67-2	Positive Forward Angle	SG3	25986	Float	4	R W	Degree	0–90	FGIT
67-2	Negative Forward Angle	SG3	25988	Float	4	R W	Degree	(-90)–0	FGIT
67-2	Positive Reverse Angle	SG3	25990	Float	4	R W	Degree	90–180	FGIT
67-2	Negative Reverse Angle	SG3	25992	Float	4	R W	Degree	180–270	FGIT
67-2	Current Source	SG3	25994	Uint32	4	R W	n/a	CT1=0 CT2=1	FGIT
50-1	Directional Source	SG0	25996	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-1	Directional Source	SG1	25998	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-1	Directional Source	SG2	26000	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-1	Directional Source	SG3	26002	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-2	Directional Source	SG0	26004	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-2	Directional Source	SG1	26006	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-2	Directional Source	SG2	26008	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-2	Directional Source	SG3	26010	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-3	Directional Source	SG0	26012	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-3	Directional Source	SG1	26014	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-3	Directional Source	SG2	26016	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-3	Directional Source	SG3	26018	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-4	Directional Source	SG0	26020	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-4	Directional Source	SG1	26022	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-4	Directional Source	SG2	26024	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-4	Directional Source	SG3	26026	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-5	Directional Source	SG0	26028	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-5	Directional Source	SG1	26030	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-5	Directional Source	SG2	26032	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-5	Directional Source	SG3	26034	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-6	Directional Source	SG0	26036	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-6	Directional Source	SG1	26038	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-6	Directional Source	SG2	26040	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-6	Directional Source	SG3	26042	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-7	Directional Source	SG0	26044	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-7	Directional Source	SG1	26046	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-7	Directional Source	SG2	26048	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-7	Directional Source	SG3	26050	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-8	Directional Source	SG0	26052	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-8	Directional Source	SG1	26054	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-8	Directional Source	SG2	26056	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-8	Directional Source	SG3	26058	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
50-9	Directional Source	SG0	26060	Uint32	4	R W	n/a	67-1=0 67-2=1	T
50-9	Directional Source	SG1	26062	Uint32	4	R W	n/a	67-1=0 67-2=1	T
50-9	Directional Source	SG2	26064	Uint32	4	R W	n/a	67-1=0 67-2=1	T
50-9	Directional Source	SG3	26066	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-1	Directional Source	SG0	26068	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-1	Directional Source	SG1	26070	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-1	Directional Source	SG2	26072	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-1	Directional Source	SG3	26074	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-2	Directional Source	SG0	26076	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-2	Directional Source	SG1	26078	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-2	Directional Source	SG2	26080	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-2	Directional Source	SG3	26082	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-3	Directional Source	SG0	26084	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-3	Directional Source	SG1	26086	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-3	Directional Source	SG2	26088	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-3	Directional Source	SG3	26090	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-4	Directional Source	SG0	26092	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-4	Directional Source	SG1	26094	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-4	Directional Source	SG2	26096	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-4	Directional Source	SG3	26098	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-5	Directional Source	SG0	26100	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-5	Directional Source	SG1	26102	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-5	Directional Source	SG2	26104	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-5	Directional Source	SG3	26106	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-6	Directional Source	SG0	26108	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-6	Directional Source	SG1	26110	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-6	Directional Source	SG2	26112	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-6	Directional Source	SG3	26114	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-7	Directional Source	SG0	26116	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-7	Directional Source	SG1	26118	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-7	Directional Source	SG2	26120	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-7	Directional Source	SG3	26122	Uint32	4	R W	n/a	67-1=0 67-2=1	FGIT
51-8	Directional Source	SG0	26124	Uint32	4	R W	n/a	67-1=0 67-2=1	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
51-8	Directional Source	SG1	26126	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-8	Directional Source	SG2	26128	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-8	Directional Source	SG3	26130	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-9	Directional Source	SG0	26132	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-9	Directional Source	SG1	26134	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-9	Directional Source	SG2	26136	Uint32	4	R W	n/a	67-1=0 67-2=1	T
51-9	Directional Source	SG3	26138	Uint32	4	R W	n/a	67-1=0 67-2=1	T
37	Directional Source	SG0	26140	Uint32	4	R W	n/a	67-1=0 67-2=1	M
37	Directional Source	SG1	26142	Uint32	4	R W	n/a	67-1=0 67-2=1	M
37	Directional Source	SG2	26144	Uint32	4	R W	n/a	67-1=0 67-2=1	M
37	Directional Source	SG3	26146	Uint32	4	R W	n/a	67-1=0 67-2=1	M
Analog Input 1	Inhibit Mode	SG0	26148	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 1	Inhibit Mode	SG1	26150	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 1	Inhibit Mode	SG2	26152	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 1	Inhibit Mode	SG3	26154	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 2	Inhibit Mode	SG0	26156	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 2	Inhibit Mode	SG1	26158	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 2	Inhibit Mode	SG2	26160	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 2	Inhibit Mode	SG3	26162	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 3	Inhibit Mode	SG0	26164	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 3	Inhibit Mode	SG1	26166	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 3	Inhibit Mode	SG2	26168	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 3	Inhibit Mode	SG3	26170	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 4	Inhibit Mode	SG0	26172	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 4	Inhibit Mode	SG1	26174	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 4	Inhibit Mode	SG2	26176	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 4	Inhibit Mode	SG3	26178	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 5	Inhibit Mode	SG0	26180	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 5	Inhibit Mode	SG1	26182	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 5	Inhibit Mode	SG2	26184	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 5	Inhibit Mode	SG3	26186	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 6	Inhibit Mode	SG0	26188	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 6	Inhibit Mode	SG1	26190	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 6	Inhibit Mode	SG2	26192	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 6	Inhibit Mode	SG3	26194	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 7	Inhibit Mode	SG0	26196	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 7	Inhibit Mode	SG1	26198	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 7	Inhibit Mode	SG2	26200	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 7	Inhibit Mode	SG3	26202	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 8	Inhibit Mode	SG0	26204	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Analog Input 8	Inhibit Mode	SG1	26206	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 8	Inhibit Mode	SG2	26208	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
Analog Input 8	Inhibit Mode	SG3	26210	Uint32	4	R W	n/a	Disabled=0 Enabled=1	DFGIMT
50-1	Reset Delay	SG0	26212	Float	4	R W	Millisecond	0–60000	FGIMT
50-1	Reset Delay	SG1	26214	Float	4	R W	Millisecond	0–60000	FGIMT
50-1	Reset Delay	SG2	26216	Float	4	R W	Millisecond	0–60000	FGIMT
50-1	Reset Delay	SG3	26218	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Reset Delay	SG0	26220	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Reset Delay	SG1	26222	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Reset Delay	SG2	26224	Float	4	R W	Millisecond	0–60000	FGIMT
50-2	Reset Delay	SG3	26226	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Reset Delay	SG0	26228	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Reset Delay	SG1	26230	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Reset Delay	SG2	26232	Float	4	R W	Millisecond	0–60000	FGIMT
50-3	Reset Delay	SG3	26234	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Reset Delay	SG0	26236	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Reset Delay	SG1	26238	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Reset Delay	SG2	26240	Float	4	R W	Millisecond	0–60000	FGIMT
50-4	Reset Delay	SG3	26242	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Reset Delay	SG0	26244	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Reset Delay	SG1	26246	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Reset Delay	SG2	26248	Float	4	R W	Millisecond	0–60000	FGIMT
50-5	Reset Delay	SG3	26250	Float	4	R W	Millisecond	0–60000	FGIMT
50-6	Reset Delay	SG0	26252	Float	4	R W	Millisecond	0–60000	FGIMT
50-6	Reset Delay	SG1	26254	Float	4	R W	Millisecond	0–60000	FGIMT
50-6	Reset Delay	SG2	26256	Float	4	R W	Millisecond	0–60000	FGIMT
50-6	Reset Delay	SG3	26258	Float	4	R W	Millisecond	0–60000	FGIMT
50-7	Reset Delay	SG0	26260	Float	4	R W	Millisecond	0–60000	T
50-7	Reset Delay	SG1	26262	Float	4	R W	Millisecond	0–60000	T
50-7	Reset Delay	SG2	26264	Float	4	R W	Millisecond	0–60000	T
50-7	Reset Delay	SG3	26266	Float	4	R W	Millisecond	0–60000	T
50-8	Reset Delay	SG0	26268	Float	4	R W	Millisecond	0–60000	T
50-8	Reset Delay	SG1	26270	Float	4	R W	Millisecond	0–60000	T
50-8	Reset Delay	SG2	26272	Float	4	R W	Millisecond	0–60000	T
50-8	Reset Delay	SG3	26274	Float	4	R W	Millisecond	0–60000	T
50-9	Reset Delay	SG0	26276	Float	4	R W	Millisecond	0–60000	T
50-9	Reset Delay	SG1	26278	Float	4	R W	Millisecond	0–60000	T
50-9	Reset Delay	SG2	26280	Float	4	R W	Millisecond	0–60000	T
50-9	Reset Delay	SG3	26282	Float	4	R W	Millisecond	0–60000	T
25A	Breaker Close Pulse Time	SG0	26284	Uint32	4	R W	Millisecond	10–10000	G
25A	Breaker Close Pulse Time	SG1	26286	Uint32	4	R W	Millisecond	10–10000	G
25A	Breaker Close Pulse Time	SG2	26288	Uint32	4	R W	Millisecond	10–10000	G
25A	Breaker Close Pulse Time	SG3	26290	Uint32	4	R W	Millisecond	10–10000	G
27P-1	Curve Index	SG0	26292	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-1	Use Instantaneous Reset	SG0	26294	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-1	Constant A	SG0	26296	Float	4	R W	n/a	0–600	FGIMT
27P-1	Constant B	SG0	26298	Float	4	R W	n/a	0–25	FGIMT
27P-1	Constant C	SG0	26300	Float	4	R W	n/a	1–3	FGIMT
27P-1	Constant N	SG0	26302	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-1	Constant R	SG0	26304	Float	4	R W	n/a	0–30	FGIMT
27P-1	Curve Index	SG1	26306	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-1	Use Instantaneous Reset	SG1	26308	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-1	Constant A	SG1	26310	Float	4	R W	n/a	0–600	FGIMT
27P-1	Constant B	SG1	26312	Float	4	R W	n/a	0–25	FGIMT
27P-1	Constant C	SG1	26314	Float	4	R W	n/a	1–3	FGIMT
27P-1	Constant N	SG1	26316	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-1	Constant R	SG1	26318	Float	4	R W	n/a	0–30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-1	Curve Index	SG2	26320	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-1	Use Instantaneous Reset	SG2	26322	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-1	Constant A	SG2	26324	Float	4	R W	n/a	0-600	FGIMT
27P-1	Constant B	SG2	26326	Float	4	R W	n/a	0-25	FGIMT
27P-1	Constant C	SG2	26328	Float	4	R W	n/a	1-3	FGIMT
27P-1	Constant N	SG2	26330	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-1	Constant R	SG2	26332	Float	4	R W	n/a	0-30	FGIMT
27P-1	Curve Index	SG3	26334	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-1	Use Instantaneous Reset	SG3	26336	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-1	Constant A	SG3	26338	Float	4	R W	n/a	0-600	FGIMT
27P-1	Constant B	SG3	26340	Float	4	R W	n/a	0-25	FGIMT
27P-1	Constant C	SG3	26342	Float	4	R W	n/a	1-3	FGIMT
27P-1	Constant N	SG3	26344	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-1	Constant R	SG3	26346	Float	4	R W	n/a	0-30	FGIMT
27P-2	Curve Index	SG0	26348	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-2	Use Instantaneous Reset	SG0	26350	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-2	Constant A	SG0	26352	Float	4	R W	n/a	0-600	FGIMT
27P-2	Constant B	SG0	26354	Float	4	R W	n/a	0-25	FGIMT
27P-2	Constant C	SG0	26356	Float	4	R W	n/a	1-3	FGIMT
27P-2	Constant N	SG0	26358	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-2	Constant R	SG0	26360	Float	4	R W	n/a	0-30	FGIMT
27P-2	Curve Index	SG1	26362	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-2	Use Instantaneous Reset	SG1	26364	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-2	Constant A	SG1	26366	Float	4	R W	n/a	0-600	FGIMT
27P-2	Constant B	SG1	26368	Float	4	R W	n/a	0-25	FGIMT
27P-2	Constant C	SG1	26370	Float	4	R W	n/a	1-3	FGIMT
27P-2	Constant N	SG1	26372	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-2	Constant R	SG1	26374	Float	4	R W	n/a	0-30	FGIMT
27P-2	Curve Index	SG2	26376	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-2	Use Instantaneous Reset	SG2	26378	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-2	Constant A	SG2	26380	Float	4	R W	n/a	0-600	FGIMT
27P-2	Constant B	SG2	26382	Float	4	R W	n/a	0-25	FGIMT
27P-2	Constant C	SG2	26384	Float	4	R W	n/a	1-3	FGIMT
27P-2	Constant N	SG2	26386	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-2	Constant R	SG2	26388	Float	4	R W	n/a	0-30	FGIMT
27P-2	Curve Index	SG3	26390	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-2	Use Instantaneous Reset	SG3	26392	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-2	Constant A	SG3	26394	Float	4	R W	n/a	0-600	FGIMT
27P-2	Constant B	SG3	26396	Float	4	R W	n/a	0-25	FGIMT
27P-2	Constant C	SG3	26398	Float	4	R W	n/a	1-3	FGIMT
27P-2	Constant N	SG3	26400	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-2	Constant R	SG3	26402	Float	4	R W	n/a	0-30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-3	Curve Index	SG0	26404	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-3	Use Instantaneous Reset	SG0	26406	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-3	Constant A	SG0	26408	Float	4	R W	n/a	0–600	FGIMT
27P-3	Constant B	SG0	26410	Float	4	R W	n/a	0–25	FGIMT
27P-3	Constant C	SG0	26412	Float	4	R W	n/a	1–3	FGIMT
27P-3	Constant N	SG0	26414	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-3	Constant R	SG0	26416	Float	4	R W	n/a	0–30	FGIMT
27P-3	Curve Index	SG1	26418	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-3	Use Instantaneous Reset	SG1	26420	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-3	Constant A	SG1	26422	Float	4	R W	n/a	0–600	FGIMT
27P-3	Constant B	SG1	26424	Float	4	R W	n/a	0–25	FGIMT
27P-3	Constant C	SG1	26426	Float	4	R W	n/a	1–3	FGIMT
27P-3	Constant N	SG1	26428	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-3	Constant R	SG1	26430	Float	4	R W	n/a	0–30	FGIMT
27P-3	Curve Index	SG2	26432	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-3	Use Instantaneous Reset	SG2	26434	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-3	Constant A	SG2	26436	Float	4	R W	n/a	0–600	FGIMT
27P-3	Constant B	SG2	26438	Float	4	R W	n/a	0–25	FGIMT
27P-3	Constant C	SG2	26440	Float	4	R W	n/a	1–3	FGIMT
27P-3	Constant N	SG2	26442	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-3	Constant R	SG2	26444	Float	4	R W	n/a	0–30	FGIMT
27P-3	Curve Index	SG3	26446	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-3	Use Instantaneous Reset	SG3	26448	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-3	Constant A	SG3	26450	Float	4	R W	n/a	0 - 600	FGIMT
27P-3	Constant B	SG3	26452	Float	4	R W	n/a	0 - 25	FGIMT
27P-3	Constant C	SG3	26454	Float	4	R W	n/a	1 - 3	FGIMT
27P-3	Constant N	SG3	26456	Float	4	R W	n/a	0.5 - 2.5	FGIMT
27P-3	Constant R	SG3	26458	Float	4	R W	n/a	0 - 30	FGIMT
27P-4	Curve Index	SG0	26460	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-4	Use Instantaneous Reset	SG0	26462	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-4	Constant A	SG0	26464	Float	4	R W	n/a	0–600	FGIMT
27P-4	Constant B	SG0	26466	Float	4	R W	n/a	0–25	FGIMT
27P-4	Constant C	SG0	26468	Float	4	R W	n/a	1–3	FGIMT
27P-4	Constant N	SG0	26470	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-4	Constant R	SG0	26472	Float	4	R W	n/a	0–30	FGIMT
27P-4	Curve Index	SG1	26474	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-4	Use Instantaneous Reset	SG1	26476	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-4	Constant A	SG1	26478	Float	4	R W	n/a	0–600	FGIMT
27P-4	Constant B	SG1	26480	Float	4	R W	n/a	0–25	FGIMT
27P-4	Constant C	SG1	26482	Float	4	R W	n/a	1–3	FGIMT
27P-4	Constant N	SG1	26484	Float	4	R W	n/a	0.5–2.5	FGIMT
27P-4	Constant R	SG1	26486	Float	4	R W	n/a	0–30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27P-4	Curve Index	SG2	26488	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-4	Use Instantaneous Reset	SG2	26490	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-4	Constant A	SG2	26492	Float	4	R W	n/a	0-600	FGIMT
27P-4	Constant B	SG2	26494	Float	4	R W	n/a	0-25	FGIMT
27P-4	Constant C	SG2	26496	Float	4	R W	n/a	1-3	FGIMT
27P-4	Constant N	SG2	26498	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-4	Constant R	SG2	26500	Float	4	R W	n/a	0-30	FGIMT
27P-4	Curve Index	SG3	26502	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-4	Use Instantaneous Reset	SG3	26504	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-4	Constant A	SG3	26506	Float	4	R W	n/a	0-600	FGIMT
27P-4	Constant B	SG3	26508	Float	4	R W	n/a	0-25	FGIMT
27P-4	Constant C	SG3	26510	Float	4	R W	n/a	1-3	FGIMT
27P-4	Constant N	SG3	26512	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-4	Constant R	SG3	26514	Float	4	R W	n/a	0-30	FGIMT
27P-5	Curve Index	SG0	26516	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-5	Use Instantaneous Reset	SG0	26518	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-5	Constant A	SG0	26520	Float	4	R W	n/a	0-600	FGIMT
27P-5	Constant B	SG0	26522	Float	4	R W	n/a	0-25	FGIMT
27P-5	Constant C	SG0	26524	Float	4	R W	n/a	1-3	FGIMT
27P-5	Constant N	SG0	26526	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-5	Constant R	SG0	26528	Float	4	R W	n/a	0-30	FGIMT
27P-5	Curve Index	SG1	26530	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-5	Use Instantaneous Reset	SG1	26532	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-5	Constant A	SG1	26534	Float	4	R W	n/a	0-600	FGIMT
27P-5	Constant B	SG1	26536	Float	4	R W	n/a	0-25	FGIMT
27P-5	Constant C	SG1	26538	Float	4	R W	n/a	1-3	FGIMT
27P-5	Constant N	SG1	26540	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-5	Constant R	SG1	26542	Float	4	R W	n/a	0-30	FGIMT
27P-5	Curve Index	SG2	26544	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-5	Use Instantaneous Reset	SG2	26546	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-5	Constant A	SG2	26548	Float	4	R W	n/a	0-600	FGIMT
27P-5	Constant B	SG2	26550	Float	4	R W	n/a	0-25	FGIMT
27P-5	Constant C	SG2	26552	Float	4	R W	n/a	1-3	FGIMT
27P-5	Constant N	SG2	26554	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-5	Constant R	SG2	26556	Float	4	R W	n/a	0-30	FGIMT
27P-5	Curve Index	SG3	26558	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
27P-5	Use Instantaneous Reset	SG3	26560	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
27P-5	Constant A	SG3	26562	Float	4	R W	n/a	0-600	FGIMT
27P-5	Constant B	SG3	26564	Float	4	R W	n/a	0-25	FGIMT
27P-5	Constant C	SG3	26566	Float	4	R W	n/a	1-3	FGIMT
27P-5	Constant N	SG3	26568	Float	4	R W	n/a	0.5-2.5	FGIMT
27P-5	Constant R	SG3	26570	Float	4	R W	n/a	0-30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-1	Curve Index	SG0	26572	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-1	Use Instantaneous Reset	SG0	26574	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-1	Constant A	SG0	26576	Float	4	R W	n/a	0–600	FGIT
27X-1	Constant B	SG0	26578	Float	4	R W	n/a	0–25	FGIT
27X-1	Constant C	SG0	26580	Float	4	R W	n/a	1–3	FGIT
27X-1	Constant N	SG0	26582	Float	4	R W	n/a	0.5–2.5	FGIT
27X-1	Constant R	SG0	26584	Float	4	R W	n/a	0–30	FGIT
27X-1	Curve Index	SG1	26586	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-1	Use Instantaneous Reset	SG1	26588	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-1	Constant A	SG1	26590	Float	4	R W	n/a	0–600	FGIT
27X-1	Constant B	SG1	26592	Float	4	R W	n/a	0–25	FGIT
27X-1	Constant C	SG1	26594	Float	4	R W	n/a	1–3	FGIT
27X-1	Constant N	SG1	26596	Float	4	R W	n/a	0.5–2.5	FGIT
27X-1	Constant R	SG1	26598	Float	4	R W	n/a	0–30	FGIT
27X-1	Curve Index	SG2	26600	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-1	Use Instantaneous Reset	SG2	26602	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-1	Constant A	SG2	26604	Float	4	R W	n/a	0–600	FGIT
27X-1	Constant B	SG2	26606	Float	4	R W	n/a	0–25	FGIT
27X-1	Constant C	SG2	26608	Float	4	R W	n/a	1–3	FGIT
27X-1	Constant N	SG2	26610	Float	4	R W	n/a	0.5–2.5	FGIT
27X-1	Constant R	SG2	26612	Float	4	R W	n/a	0–30	FGIT
27X-1	Curve Index	SG3	26614	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-1	Use Instantaneous Reset	SG3	26616	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-1	Constant A	SG3	26618	Float	4	R W	n/a	0–600	FGIT
27X-1	Constant B	SG3	26620	Float	4	R W	n/a	0–25	FGIT
27X-1	Constant C	SG3	26622	Float	4	R W	n/a	1–3	FGIT
27X-1	Constant N	SG3	26624	Float	4	R W	n/a	0.5–2.5	FGIT
27X-1	Constant R	SG3	26626	Float	4	R W	n/a	0–30	FGIT
27X-2	Curve Index	SG0	26628	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-2	Use Instantaneous Reset	SG0	26630	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-2	Constant A	SG0	26632	Float	4	R W	n/a	0–600	FGIT
27X-2	Constant B	SG0	26634	Float	4	R W	n/a	0–25	FGIT
27X-2	Constant C	SG0	26636	Float	4	R W	n/a	1–3	FGIT
27X-2	Constant N	SG0	26638	Float	4	R W	n/a	0.5–2.5	FGIT
27X-2	Constant R	SG0	26640	Float	4	R W	n/a	0–30	FGIT
27X-2	Curve Index	SG1	26642	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-2	Use Instantaneous Reset	SG1	26644	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-2	Constant A	SG1	26646	Float	4	R W	n/a	0–600	FGIT
27X-2	Constant B	SG1	26648	Float	4	R W	n/a	0–25	FGIT
27X-2	Constant C	SG1	26650	Float	4	R W	n/a	1–3	FGIT
27X-2	Constant N	SG1	26652	Float	4	R W	n/a	0.5–2.5	FGIT
27X-2	Constant R	SG1	26654	Float	4	R W	n/a	0–30	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-2	Curve Index	SG2	26656	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-2	Use Instantaneous Reset	SG2	26658	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-2	Constant A	SG2	26660	Float	4	R W	n/a	0-600	FGIT
27X-2	Constant B	SG2	26662	Float	4	R W	n/a	0-25	FGIT
27X-2	Constant C	SG2	26664	Float	4	R W	n/a	1-3	FGIT
27X-2	Constant N	SG2	26666	Float	4	R W	n/a	0.5-2.5	FGIT
27X-2	Constant R	SG2	26668	Float	4	R W	n/a	0-30	FGIT
27X-2	Curve Index	SG3	26670	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-2	Use Instantaneous Reset	SG3	26672	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-2	Constant A	SG3	26674	Float	4	R W	n/a	0-600	FGIT
27X-2	Constant B	SG3	26676	Float	4	R W	n/a	0-25	FGIT
27X-2	Constant C	SG3	26678	Float	4	R W	n/a	1-3	FGIT
27X-2	Constant N	SG3	26680	Float	4	R W	n/a	0.5-2.5	FGIT
27X-2	Constant R	SG3	26682	Float	4	R W	n/a	0-30	FGIT
27X-3	Curve Index	SG0	26684	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-3	Use Instantaneous Reset	SG0	26686	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-3	Constant A	SG0	26688	Float	4	R W	n/a	0-600	FGIT
27X-3	Constant B	SG0	26690	Float	4	R W	n/a	0-25	FGIT
27X-3	Constant C	SG0	26692	Float	4	R W	n/a	1-3	FGIT
27X-3	Constant N	SG0	26694	Float	4	R W	n/a	0.5-2.5	FGIT
27X-3	Constant R	SG0	26696	Float	4	R W	n/a	0-30	FGIT
27X-3	Curve Index	SG1	26698	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-3	Use Instantaneous Reset	SG1	26700	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-3	Constant A	SG1	26702	Float	4	R W	n/a	0-600	FGIT
27X-3	Constant B	SG1	26704	Float	4	R W	n/a	0-25	FGIT
27X-3	Constant C	SG1	26706	Float	4	R W	n/a	1-3	FGIT
27X-3	Constant N	SG1	26708	Float	4	R W	n/a	0.5-2.5	FGIT
27X-3	Constant R	SG1	26710	Float	4	R W	n/a	0-30	FGIT
27X-3	Curve Index	SG2	26712	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-3	Use Instantaneous Reset	SG2	26714	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-3	Constant A	SG2	26716	Float	4	R W	n/a	0-600	FGIT
27X-3	Constant B	SG2	26718	Float	4	R W	n/a	0-25	FGIT
27X-3	Constant C	SG2	26720	Float	4	R W	n/a	1-3	FGIT
27X-3	Constant N	SG2	26722	Float	4	R W	n/a	0.5-2.5	FGIT
27X-3	Constant R	SG2	26724	Float	4	R W	n/a	0-30	FGIT
27X-3	Curve Index	SG3	26726	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-3	Use Instantaneous Reset	SG3	26728	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-3	Constant A	SG3	26730	Float	4	R W	n/a	0-600	FGIT
27X-3	Constant B	SG3	26732	Float	4	R W	n/a	0-25	FGIT
27X-3	Constant C	SG3	26734	Float	4	R W	n/a	1-3	FGIT
27X-3	Constant N	SG3	26736	Float	4	R W	n/a	0.5-2.5	FGIT
27X-3	Constant R	SG3	26738	Float	4	R W	n/a	0-30	FGIT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27X-4	Curve Index	SG0	26740	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-4	Use Instantaneous Reset	SG0	26742	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-4	Constant A	SG0	26744	Float	4	R W	n/a	0–600	FGIT
27X-4	Constant B	SG0	26746	Float	4	R W	n/a	0–25	FGIT
27X-4	Constant C	SG0	26748	Float	4	R W	n/a	1–3	FGIT
27X-4	Constant N	SG0	26750	Float	4	R W	n/a	0.5–2.5	FGIT
27X-4	Constant R	SG0	26752	Float	4	R W	n/a	0–30	FGIT
27X-4	Curve Index	SG1	26754	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-4	Use Instantaneous Reset	SG1	26756	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-4	Constant A	SG1	26758	Float	4	R W	n/a	0–600	FGIT
27X-4	Constant B	SG1	26760	Float	4	R W	n/a	0–25	FGIT
27X-4	Constant C	SG1	26762	Float	4	R W	n/a	1–3	FGIT
27X-4	Constant N	SG1	26764	Float	4	R W	n/a	0.5–2.5	FGIT
27X-4	Constant R	SG1	26766	Float	4	R W	n/a	0–30	FGIT
27X-4	Curve Index	SG2	26768	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-4	Use Instantaneous Reset	SG2	26770	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-4	Constant A	SG2	26772	Float	4	R W	n/a	0–600	FGIT
27X-4	Constant B	SG2	26774	Float	4	R W	n/a	0–25	FGIT
27X-4	Constant C	SG2	26776	Float	4	R W	n/a	1–3	FGIT
27X-4	Constant N	SG2	26778	Float	4	R W	n/a	0.5–2.5	FGIT
27X-4	Constant R	SG2	26780	Float	4	R W	n/a	0–30	FGIT
27X-4	Curve Index	SG3	26782	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIT
27X-4	Use Instantaneous Reset	SG3	26784	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIT
27X-4	Constant A	SG3	26786	Float	4	R W	n/a	0–600	FGIT
27X-4	Constant B	SG3	26788	Float	4	R W	n/a	0–25	FGIT
27X-4	Constant C	SG3	26790	Float	4	R W	n/a	1–3	FGIT
27X-4	Constant N	SG3	26792	Float	4	R W	n/a	0.5–2.5	FGIT
27X-4	Constant R	SG3	26794	Float	4	R W	n/a	0–30	FGIT
59P-1	Curve Index	SG0	26796	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-1	Use Instantaneous Reset	SG0	26798	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-1	Constant A	SG0	26800	Float	4	R W	n/a	0–600	FGIMT
59P-1	Constant B	SG0	26802	Float	4	R W	n/a	0–25	FGIMT
59P-1	Constant C	SG0	26804	Float	4	R W	n/a	1–3	FGIMT
59P-1	Constant N	SG0	26806	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-1	Constant R	SG0	26808	Float	4	R W	n/a	0–30	FGIMT
59P-1	Curve Index	SG1	26810	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-1	Use Instantaneous Reset	SG1	26812	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-1	Constant A	SG1	26814	Float	4	R W	n/a	0–600	FGIMT
59P-1	Constant B	SG1	26816	Float	4	R W	n/a	0–25	FGIMT
59P-1	Constant C	SG1	26818	Float	4	R W	n/a	1–3	FGIMT
59P-1	Constant N	SG1	26820	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-1	Constant R	SG1	26822	Float	4	R W	n/a	0–30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59P-1	Curve Index	SG2	26824	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-1	Use Instantaneous Reset	SG2	26826	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-1	Constant A	SG2	26828	Float	4	R W	n/a	0-600	FGIMT
59P-1	Constant B	SG2	26830	Float	4	R W	n/a	0-25	FGIMT
59P-1	Constant C	SG2	26832	Float	4	R W	n/a	1-3	FGIMT
59P-1	Constant N	SG2	26834	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-1	Constant R	SG2	26836	Float	4	R W	n/a	0-30	FGIMT
59P-1	Curve Index	SG3	26838	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-1	Use Instantaneous Reset	SG3	26840	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-1	Constant A	SG3	26842	Float	4	R W	n/a	0-600	FGIMT
59P-1	Constant B	SG3	26844	Float	4	R W	n/a	0-25	FGIMT
59P-1	Constant C	SG3	26846	Float	4	R W	n/a	1-3	FGIMT
59P-1	Constant N	SG3	26848	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-1	Constant R	SG3	26850	Float	4	R W	n/a	0-30	FGIMT
59P-2	Curve Index	SG0	26852	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-2	Use Instantaneous Reset	SG0	26854	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-2	Constant A	SG0	26856	Float	4	R W	n/a	0-600	FGIMT
59P-2	Constant B	SG0	26858	Float	4	R W	n/a	0-25	FGIMT
59P-2	Constant C	SG0	26860	Float	4	R W	n/a	1-3	FGIMT
59P-2	Constant N	SG0	26862	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-2	Constant R	SG0	26864	Float	4	R W	n/a	0-30	FGIMT
59P-2	Curve Index	SG1	26866	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-2	Use Instantaneous Reset	SG1	26868	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-2	Constant A	SG1	26870	Float	4	R W	n/a	0-600	FGIMT
59P-2	Constant B	SG1	26872	Float	4	R W	n/a	0-25	FGIMT
59P-2	Constant C	SG1	26874	Float	4	R W	n/a	1-3	FGIMT
59P-2	Constant N	SG1	26876	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-2	Constant R	SG1	26878	Float	4	R W	n/a	0-30	FGIMT
59P-2	Curve Index	SG2	26880	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-2	Use Instantaneous Reset	SG2	26882	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-2	Constant A	SG2	26884	Float	4	R W	n/a	0-600	FGIMT
59P-2	Constant B	SG2	26886	Float	4	R W	n/a	0-25	FGIMT
59P-2	Constant C	SG2	26888	Float	4	R W	n/a	1-3	FGIMT
59P-2	Constant N	SG2	26890	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-2	Constant R	SG2	26892	Float	4	R W	n/a	0-30	FGIMT
59P-2	Curve Index	SG3	26894	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-2	Use Instantaneous Reset	SG3	26896	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-2	Constant A	SG3	26898	Float	4	R W	n/a	0-600	FGIMT
59P-2	Constant B	SG3	26900	Float	4	R W	n/a	0-25	FGIMT
59P-2	Constant C	SG3	26902	Float	4	R W	n/a	1-3	FGIMT
59P-2	Constant N	SG3	26904	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-2	Constant R	SG3	26906	Float	4	R W	n/a	0-30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59P-3	Curve Index	SG0	26908	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-3	Use Instantaneous Reset	SG0	26910	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-3	Constant A	SG0	26912	Float	4	R W	n/a	0–600	FGIMT
59P-3	Constant B	SG0	26914	Float	4	R W	n/a	0–25	FGIMT
59P-3	Constant C	SG0	26916	Float	4	R W	n/a	1–3	FGIMT
59P-3	Constant N	SG0	26918	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-3	Constant R	SG0	26920	Float	4	R W	n/a	0–30	FGIMT
59P-3	Curve Index	SG1	26922	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-3	Use Instantaneous Reset	SG1	26924	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-3	Constant A	SG1	26926	Float	4	R W	n/a	0–600	FGIMT
59P-3	Constant B	SG1	26928	Float	4	R W	n/a	0–25	FGIMT
59P-3	Constant C	SG1	26930	Float	4	R W	n/a	1–3	FGIMT
59P-3	Constant N	SG1	26932	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-3	Constant R	SG1	26934	Float	4	R W	n/a	0–30	FGIMT
59P-3	Curve Index	SG2	26936	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-3	Use Instantaneous Reset	SG2	26938	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-3	Constant A	SG2	26940	Float	4	R W	n/a	0–600	FGIMT
59P-3	Constant B	SG2	26942	Float	4	R W	n/a	0–25	FGIMT
59P-3	Constant C	SG2	26944	Float	4	R W	n/a	1–3	FGIMT
59P-3	Constant N	SG2	26946	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-3	Constant R	SG2	26948	Float	4	R W	n/a	0–30	FGIMT
59P-3	Curve Index	SG3	26950	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-3	Use Instantaneous Reset	SG3	26952	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-3	Constant A	SG3	26954	Float	4	R W	n/a	0–600	FGIMT
59P-3	Constant B	SG3	26956	Float	4	R W	n/a	0–25	FGIMT
59P-3	Constant C	SG3	26958	Float	4	R W	n/a	1–3	FGIMT
59P-3	Constant N	SG3	26960	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-3	Constant R	SG3	26962	Float	4	R W	n/a	0–30	FGIMT
59P-4	Curve Index	SG0	26964	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-4	Use Instantaneous Reset	SG0	26966	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-4	Constant A	SG0	26968	Float	4	R W	n/a	0–600	FGIMT
59P-4	Constant B	SG0	26970	Float	4	R W	n/a	0–25	FGIMT
59P-4	Constant C	SG0	26972	Float	4	R W	n/a	1–3	FGIMT
59P-4	Constant N	SG0	26974	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-4	Constant R	SG0	26976	Float	4	R W	n/a	0–30	FGIMT
59P-4	Curve Index	SG1	26978	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-4	Use Instantaneous Reset	SG1	26980	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-4	Constant A	SG1	26982	Float	4	R W	n/a	0–600	FGIMT
59P-4	Constant B	SG1	26984	Float	4	R W	n/a	0–25	FGIMT
59P-4	Constant C	SG1	26986	Float	4	R W	n/a	1–3	FGIMT
59P-4	Constant N	SG1	26988	Float	4	R W	n/a	0.5–2.5	FGIMT
59P-4	Constant R	SG1	26990	Float	4	R W	n/a	0–30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59P-4	Curve Index	SG2	26992	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-4	Use Instantaneous Reset	SG2	26994	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-4	Constant A	SG2	26996	Float	4	R W	n/a	0-600	FGIMT
59P-4	Constant B	SG2	26998	Float	4	R W	n/a	0-25	FGIMT
59P-4	Constant C	SG2	27000	Float	4	R W	n/a	1-3	FGIMT
59P-4	Constant N	SG2	27002	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-4	Constant R	SG2	27004	Float	4	R W	n/a	0-30	FGIMT
59P-4	Curve Index	SG3	27006	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59P-4	Use Instantaneous Reset	SG3	27008	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59P-4	Constant A	SG3	27010	Float	4	R W	n/a	0-600	FGIMT
59P-4	Constant B	SG3	27012	Float	4	R W	n/a	0-25	FGIMT
59P-4	Constant C	SG3	27014	Float	4	R W	n/a	1-3	FGIMT
59P-4	Constant N	SG3	27016	Float	4	R W	n/a	0.5-2.5	FGIMT
59P-4	Constant R	SG3	27018	Float	4	R W	n/a	0-30	FGIMT
59X-1	Curve Index	SG0	27020	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-1	Use Instantaneous Reset	SG0	27022	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-1	Constant A	SG0	27024	Float	4	R W	n/a	0-600	FGIMT
59X-1	Constant B	SG0	27026	Float	4	R W	n/a	0-25	FGIMT
59X-1	Constant C	SG0	27028	Float	4	R W	n/a	1-3	FGIMT
59X-1	Constant N	SG0	27030	Float	4	R W	n/a	0.5-2.5	FGIMT
59X-1	Constant R	SG0	27032	Float	4	R W	n/a	0-30	FGIMT
59X-1	Curve Index	SG1	27034	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-1	Use Instantaneous Reset	SG1	27036	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-1	Constant A	SG1	27038	Float	4	R W	n/a	0-600	FGIMT
59X-1	Constant B	SG1	27040	Float	4	R W	n/a	0-25	FGIMT
59X-1	Constant C	SG1	27042	Float	4	R W	n/a	1-3	FGIMT
59X-1	Constant N	SG1	27044	Float	4	R W	n/a	0.5-2.5	FGIMT
59X-1	Constant R	SG1	27046	Float	4	R W	n/a	0-30	FGIMT
59X-1	Curve Index	SG2	27048	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-1	Use Instantaneous Reset	SG2	27050	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-1	Constant A	SG2	27052	Float	4	R W	n/a	0-600	FGIMT
59X-1	Constant B	SG2	27054	Float	4	R W	n/a	0-25	FGIMT
59X-1	Constant C	SG2	27056	Float	4	R W	n/a	1-3	FGIMT
59X-1	Constant N	SG2	27058	Float	4	R W	n/a	0.5-2.5	FGIMT
59X-1	Constant R	SG2	27060	Float	4	R W	n/a	0-30	FGIMT
59X-1	Curve Index	SG3	27062	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-1	Use Instantaneous Reset	SG3	27064	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-1	Constant A	SG3	27066	Float	4	R W	n/a	0-600	FGIMT
59X-1	Constant B	SG3	27068	Float	4	R W	n/a	0-25	FGIMT
59X-1	Constant C	SG3	27070	Float	4	R W	n/a	1-3	FGIMT
59X-1	Constant N	SG3	27072	Float	4	R W	n/a	0.5-2.5	FGIMT
59X-1	Constant R	SG3	27074	Float	4	R W	n/a	0-30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59X-2	Curve Index	SG0	27076	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-2	Use Instantaneous Reset	SG0	27078	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-2	Constant A	SG0	27080	Float	4	R W	n/a	0–600	FGIMT
59X-2	Constant B	SG0	27082	Float	4	R W	n/a	0–25	FGIMT
59X-2	Constant C	SG0	27084	Float	4	R W	n/a	1–3	FGIMT
59X-2	Constant N	SG0	27086	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-2	Constant R	SG0	27088	Float	4	R W	n/a	0–30	FGIMT
59X-2	Curve Index	SG1	27090	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-2	Use Instantaneous Reset	SG1	27092	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-2	Constant A	SG1	27094	Float	4	R W	n/a	0–600	FGIMT
59X-2	Constant B	SG1	27096	Float	4	R W	n/a	0–25	FGIMT
59X-2	Constant C	SG1	27098	Float	4	R W	n/a	1–3	FGIMT
59X-2	Constant N	SG1	27100	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-2	Constant R	SG1	27102	Float	4	R W	n/a	0–30	FGIMT
59X-2	Curve Index	SG2	27104	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-2	Use Instantaneous Reset	SG2	27106	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-2	Constant A	SG2	27108	Float	4	R W	n/a	0–600	FGIMT
59X-2	Constant B	SG2	27110	Float	4	R W	n/a	0–25	FGIMT
59X-2	Constant C	SG2	27112	Float	4	R W	n/a	1–3	FGIMT
59X-2	Constant N	SG2	27114	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-2	Constant R	SG2	27116	Float	4	R W	n/a	0–30	FGIMT
59X-2	Curve Index	SG3	27118	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-2	Use Instantaneous Reset	SG3	27120	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-2	Constant A	SG3	27122	Float	4	R W	n/a	0–600	FGIMT
59X-2	Constant B	SG3	27124	Float	4	R W	n/a	0–25	FGIMT
59X-2	Constant C	SG3	27126	Float	4	R W	n/a	1–3	FGIMT
59X-2	Constant N	SG3	27128	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-2	Constant R	SG3	27130	Float	4	R W	n/a	0–30	FGIMT
59X-3	Curve Index	SG0	27132	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-3	Use Instantaneous Reset	SG0	27134	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-3	Constant A	SG0	27136	Float	4	R W	n/a	0–600	FGIMT
59X-3	Constant B	SG0	27138	Float	4	R W	n/a	0–25	FGIMT
59X-3	Constant C	SG0	27140	Float	4	R W	n/a	1–3	FGIMT
59X-3	Constant N	SG0	27142	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-3	Constant R	SG0	27144	Float	4	R W	n/a	0–30	FGIMT
59X-3	Curve Index	SG1	27146	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-3	Use Instantaneous Reset	SG1	27148	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-3	Constant A	SG1	27150	Float	4	R W	n/a	0–600	FGIMT
59X-3	Constant B	SG1	27152	Float	4	R W	n/a	0–25	FGIMT
59X-3	Constant C	SG1	27154	Float	4	R W	n/a	1–3	FGIMT
59X-3	Constant N	SG1	27156	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-3	Constant R	SG1	27158	Float	4	R W	n/a	0–30	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59X-3	Curve Index	SG2	27160	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-3	Use Instantaneous Reset	SG2	27162	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-3	Constant A	SG2	27164	Float	4	R W	n/a	0–600	FGIMT
59X-3	Constant B	SG2	27166	Float	4	R W	n/a	0–25	FGIMT
59X-3	Constant C	SG2	27168	Float	4	R W	n/a	1–3	FGIMT
59X-3	Constant N	SG2	27170	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-3	Constant R	SG2	27172	Float	4	R W	n/a	0–30	FGIMT
59X-3	Curve Index	SG3	27174	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-3	Use Instantaneous Reset	SG3	27176	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-3	Constant A	SG3	27178	Float	4	R W	n/a	0–600	FGIMT
59X-3	Constant B	SG3	27180	Float	4	R W	n/a	0–25	FGIMT
59X-3	Constant C	SG3	27182	Float	4	R W	n/a	1–3	FGIMT
59X-3	Constant N	SG3	27184	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-3	Constant R	SG3	27186	Float	4	R W	n/a	0–30	FGIMT
59X-4	Curve Index	SG0	27188	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-4	Use Instantaneous Reset	SG0	27190	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-4	Constant A	SG0	27192	Float	4	R W	n/a	0–600	FGIMT
59X-4	Constant B	SG0	27194	Float	4	R W	n/a	0–25	FGIMT
59X-4	Constant C	SG0	27196	Float	4	R W	n/a	1–3	FGIMT
59X-4	Constant N	SG0	27198	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-4	Constant R	SG0	27200	Float	4	R W	n/a	0–30	FGIMT
59X-4	Curve Index	SG1	27202	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-4	Use Instantaneous Reset	SG1	27204	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-4	Constant A	SG1	27206	Float	4	R W	n/a	0–600	FGIMT
59X-4	Constant B	SG1	27208	Float	4	R W	n/a	0–25	FGIMT
59X-4	Constant C	SG1	27210	Float	4	R W	n/a	1–3	FGIMT
59X-4	Constant N	SG1	27212	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-4	Constant R	SG1	27214	Float	4	R W	n/a	0–30	FGIMT
59X-4	Curve Index	SG2	27216	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-4	Use Instantaneous Reset	SG2	27218	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-4	Constant A	SG2	27220	Float	4	R W	n/a	0–600	FGIMT
59X-4	Constant B	SG2	27222	Float	4	R W	n/a	0–25	FGIMT
59X-4	Constant C	SG2	27224	Float	4	R W	n/a	1–3	FGIMT
59X-4	Constant N	SG2	27226	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-4	Constant R	SG2	27228	Float	4	R W	n/a	0–30	FGIMT
59X-4	Curve Index	SG3	27230	Uint32	4	R W	n/a	P=0 T1=1 T2=2 T3=3 T4=4	FGIMT
59X-4	Use Instantaneous Reset	SG3	27232	Uint32	4	R W	n/a	Integrating=0 Instantaneous =1	FGIMT
59X-4	Constant A	SG3	27234	Float	4	R W	n/a	0–600	FGIMT
59X-4	Constant B	SG3	27236	Float	4	R W	n/a	0–25	FGIMT
59X-4	Constant C	SG3	27238	Float	4	R W	n/a	1–3	FGIMT
59X-4	Constant N	SG3	27240	Float	4	R W	n/a	0.5–2.5	FGIMT
59X-4	Constant R	SG3	27242	Float	4	R W	n/a	0–30	FGIMT
Undervoltage Table Curves	Input Trip Mop Point	GG	27244	Float	4	R W	n/a	0–1	FGIMT

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Undervoltage Table Curves	Input Trip Time Point	GG	27246	Float	4	R W	n/a	0-9999999	FGIMT
Undervoltage Table Curves	Input Reset Mop Point	GG	27248	Float	4	R W	n/a	0-40	FGIMT
Undervoltage Table Curves	Input Reset Time Point	GG	27250	Float	4	R W	n/a	0-9999999	FGIMT
Undervoltage Table Curves	Start Curve Input	GG	27252	Uint32	4	R W	n/a	1-4	FGIMT
Undervoltage Table Curves	Start Curve Output	GG	27254	Uint32	4	R W	n/a	1-4	FGIMT
Overvoltage Table Curves	Input Trip Mop Point	GG	27256	Float	4	R W	n/a	0-40	FGIMT
Overvoltage Table Curves	Input Trip Mop Point	GG	27258	Float	4	R W	n/a	0-9999999	FGIMT
Overvoltage Table Curves	Input Trip Time Point	GG	27260	Float	4	R W	n/a	0-1	FGIMT
Overvoltage Table Curves	Input Reset Mop Point	GG	27262	Float	4	R W	n/a	0-9999999	FGIMT
Overvoltage Table Curves	Input Reset Time Point	GG	27264	Float	4	R W	n/a	1-4	FGIMT
Overvoltage Table Curves	Start Curve Input	GG	27266	Uint32	4	R W	n/a	1-4	FGIMT
50-1	Calculation Type	SG0	27268	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-1	Calculation Type	SG1	27270	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-1	Calculation Type	SG2	27272	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-1	Calculation Type	SG3	27274	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-2	Calculation Type	SG0	27276	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-2	Calculation Type	SG1	27278	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-2	Calculation Type	SG2	27280	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-2	Calculation Type	SG3	27282	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-3	Calculation Type	SG0	27284	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-3	Calculation Type	SG1	27286	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-3	Calculation Type	SG2	27288	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-3	Calculation Type	SG3	27290	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-4	Calculation Type	SG0	27292	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-4	Calculation Type	SG1	27294	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-4	Calculation Type	SG2	27296	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-4	Calculation Type	SG3	27298	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-5	Calculation Type	SG0	27300	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-5	Calculation Type	SG1	27302	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-5	Calculation Type	SG2	27304	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-5	Calculation Type	SG3	27306	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-6	Calculation Type	SG0	27308	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-6	Calculation Type	SG1	27310	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-6	Calculation Type	SG2	27312	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-6	Calculation Type	SG3	27314	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	FGIMT
50-7	Calculation Type	SG0	27316	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-7	Calculation Type	SG1	27318	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-7	Calculation Type	SG2	27320	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-7	Calculation Type	SG3	27322	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG0	27324	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
50-8	Calculation Type	SG1	27326	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG2	27328	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG3	27330	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG0	27332	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG1	27334	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG2	27336	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG3	27338	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-7	Calculation Type	SG2	27320	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-7	Calculation Type	SG3	27322	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG0	27324	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG1	27326	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG2	27328	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-8	Calculation Type	SG3	27330	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG0	27332	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG1	27334	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG2	27336	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
50-9	Calculation Type	SG3	27338	Uint32	4	R W	n/a	Fundamental=1 Peak Detect=11	T
27-1	Enable	SG0	27340	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-1	Source	SG0	27342	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-1	Enable	SG1	27344	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-1	Source	SG1	27346	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-1	Enable	SG2	27348	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-1	Source	SG2	27350	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-1	Enable	SG3	27352	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-1	Source	SG3	27354	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-2	Enable	SG0	27356	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-2	Source	SG0	27358	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-2	Enable	SG1	27360	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-2	Source	SG1	27362	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-2	Enable	SG2	27364	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-2	Source	SG2	27366	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-2	Enable	SG3	27368	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-2	Source	SG3	27370	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-3	Enable	SG0	27372	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-3	Source	SG0	27374	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-3	Enable	SG1	27376	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
27-3	Source	SG1	27378	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-3	Enable	SG2	27380	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-3	Source	SG2	27382	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-3	Enable	SG3	27384	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-3	Source	SG3	27386	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-4	Enable	SG0	27388	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-4	Source	SG0	27390	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-4	Enable	SG1	27392	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-4	Source	SG1	27394	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-4	Enable	SG2	27396	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-4	Source	SG2	27398	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
27-4	Enable	SG3	27400	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
27-4	Source	SG3	27402	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-1	Enable	SG0	27404	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-1	Source	SG0	27406	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-1	Enable	SG1	27408	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-1	Source	SG1	27410	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-1	Enable	SG2	27412	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-1	Source	SG2	27414	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-1	Enable	SG3	27416	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-1	Source	SG3	27418	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-2	Enable	SG0	27420	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-2	Source	SG0	27422	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-2	Enable	SG1	27424	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-2	Source	SG1	27426	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-2	Enable	SG2	27428	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-2	Source	SG2	27430	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-2	Enable	SG3	27432	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-2	Source	SG3	27434	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-3	Enable	SG0	27436	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-3	Source	SG0	27438	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-3	Enable	SG1	27440	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
59-3	Source	SG1	27442	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-3	Enable	SG2	27444	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-3	Source	SG2	27446	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-3	Enable	SG3	27448	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-3	Source	SG3	27450	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-4	Enable	SG0	27452	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-4	Source	SG0	27454	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-4	Enable	SG1	27456	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-4	Source	SG1	27458	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-4	Enable	SG2	27460	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-4	Source	SG2	27462	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
59-4	Enable	SG3	27464	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
59-4	Source	SG3	27466	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
76-1	Mode	SG0	27468	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27470						
76-1	Pickup	SG0	27472	Float	4	R W	Millivolt	2.5–1000	D
76-1	Direction	SG0	27474	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-1	Timing Mode	SG0	27476	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-1	Time Delay	SG0	27478	Float	4	R W	Millisecond	0–6000000	D
76-1	Reset Delay	SG0	27480	Float	4	R W	Millisecond	0–6000000	D
76-1	Time Dial	SG0	27482	Float	4	R W	n/a	0–9.9	D
76-1	Curve	SG0	27484	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-1	Reset Timing	SG0	27486	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-1	A Constant	SG0	27488	Float	4	R W	n/a	0–600	D
76-1	B Constant	SG0	27490	Float	4	R W	n/a	0–25	D
76-1	C Constant	SG0	27492	Float	4	R W	n/a	0–1	D
76-1	N Constant	SG0	27494	Float	4	R W	n/a	0.5–2.5	D
76-1	R Constant	SG0	27496	Float	4	R W	n/a	0–30	D
76-1	Mode	SG1	27498	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27500						
76-1	Pickup	SG1	27502	Float	4	R W	Millivolt	2.5–1000	D
76-1	Direction	SG1	27504	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-1	Timing Mode	SG1	27506	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-1	Time Delay	SG1	27508	Float	4	R W	Millisecond	0–6000000	D
76-1	Reset Delay	SG1	27510	Float	4	R W	Millisecond	0–6000000	D
76-1	Time Dial	SG1	27512	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-1	Curve	SG1	27514	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-1	Reset Timing	SG1	27516	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-1	A Constant	SG1	27518	Float	4	R W	n/a	0-600	D
76-1	B Constant	SG1	27520	Float	4	R W	n/a	0-25	D
76-1	C Constant	SG1	27522	Float	4	R W	n/a	0-1	D
76-1	N Constant	SG1	27524	Float	4	R W	n/a	0.5-2.5	D
76-1	R Constant	SG1	27526	Float	4	R W	n/a	0-30	D
76-1	Mode	SG2	27528	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27530						
76-1	Pickup	SG2	27532	Float	4	R W	Millivolt	2.5-1000	D
76-1	Direction	SG2	27534	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-1	Timing Mode	SG2	27536	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-1	Time Delay	SG2	27538	Float	4	R W	Millisecond	0-6000000	D
76-1	Reset Delay	SG2	27540	Float	4	R W	Millisecond	0-6000000	D
76-1	Time Dial	SG2	27542	Float	4	R W	n/a	0-9.9	D
76-1	Curve	SG2	27544	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-1	Reset Timing	SG2	27546	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-1	A Constant	SG2	27548	Float	4	R W	n/a	0-600	D
76-1	B Constant	SG2	27550	Float	4	R W	n/a	0-25	D
76-1	C Constant	SG2	27552	Float	4	R W	n/a	0-1	D
76-1	N Constant	SG2	27554	Float	4	R W	n/a	0.5-2.5	D
76-1	R Constant	SG2	27556	Float	4	R W	n/a	0-30	D
76-1	Mode	SG3	27558	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27560						
76-1	Pickup	SG3	27562	Float	4	R W	Millivolt	2.5-1000	D
76-1	Direction	SG3	27564	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-1	Timing Mode	SG3	27566	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-1	Time Delay	SG3	27568	Float	4	R W	Millisecond	0-6000000	D
76-1	Reset Delay	SG3	27570	Float	4	R W	Millisecond	0-6000000	D
76-1	Time Dial	SG3	27572	Float	4	R W	n/a	0-9.9	D
76-1	Curve	SG3	27574	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-1	Reset Timing	SG3	27576	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-1	A Constant	SG3	27578	Float	4	R W	n/a	0-600	D
76-1	B Constant	SG3	27580	Float	4	R W	n/a	0-25	D
76-1	C Constant	SG3	27582	Float	4	R W	n/a	0-1	D
76-1	N Constant	SG3	27584	Float	4	R W	n/a	0.5-2.5	D
76-1	R Constant	SG3	27586	Float	4	R W	n/a	0-30	D
76-2	Mode	SG0	27588	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			27590						
76-2	Pickup	SG0	27592	Float	4	R W	Millivolt	2.5–1000	D
76-2	Direction	SG0	27594	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-2	Timing Mode	SG0	27596	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-2	Time Delay	SG0	27598	Float	4	R W	Millisecond	0–6000000	D
76-2	Reset Delay	SG0	27600	Float	4	R W	Millisecond	0–6000000	D
76-2	Time Dial	SG0	27602	Float	4	R W	n/a	0–9.9	D
76-2	Curve	SG0	27604	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-2	Reset Timing	SG0	27606	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-2	A Constant	SG0	27608	Float	4	R W	n/a	0–600	D
76-2	B Constant	SG0	27610	Float	4	R W	n/a	0–25	D
76-2	C Constant	SG0	27612	Float	4	R W	n/a	0–1	D
76-2	N Constant	SG0	27614	Float	4	R W	n/a	0.5–2.5	D
76-2	R Constant	SG0	27616	Float	4	R W	n/a	0–30	D
76-2	Mode	SG1	27618	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27620						
76-2	Pickup	SG1	27622	Float	4	R W	Millivolt	2.5–1000	D
76-2	Direction	SG1	27624	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-2	Timing Mode	SG1	27626	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-2	Time Delay	SG1	27628	Float	4	R W	Millisecond	0–6000000	D
76-2	Reset Delay	SG1	27630	Float	4	R W	Millisecond	0–6000000	D
76-2	Time Dial	SG1	27632	Float	4	R W	n/a	0–9.9	D
76-2	Curve	SG1	27634	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-2	Reset Timing	SG1	37636	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-2	A Constant	SG1	27638	Float	4	R W	n/a	0–600	D
76-2	B Constant	SG1	27640	Float	4	R W	n/a	0–25	D
76-2	C Constant	SG1	27642	Float	4	R W	n/a	0–1	D
76-2	N Constant	SG1	27644	Float	4	R W	n/a	0.5–2.5	D
76-2	R Constant	SG1	27646	Float	4	R W	n/a	0–30	D
76-2	Mode	SG2	27648	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27650						
76-2	Pickup	SG2	27652	Float	4	R W	Millivolt	2.5–1000	D
76-2	Direction	SG2	27654	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-2	Timing Mode	SG2	27656	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-2	Time Delay	SG2	27658	Float	4	R W	Millisecond	0–6000000	D
76-2	Reset Delay	SG2	27660	Float	4	R W	Millisecond	0–6000000	D
76-2	Time Dial	SG2	27662	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-2	Curve	SG2	27664	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-2	Reset Timing	SG2	27666	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-2	A Constant	SG2	27668	Float	4	R W	n/a	0-600	D
76-2	B Constant	SG2	27670	Float	4	R W	n/a	0-25	D
76-2	C Constant	SG2	27672	Float	4	R W	n/a	0-1	D
76-2	N Constant	SG2	27674	Float	4	R W	n/a	0.5-2.5	D
76-2	R Constant	SG2	27676	Float	4	R W	n/a	0-30	D
76-2	Mode	SG3	27678	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27680						
76-2	Pickup	SG3	27682	Float	4	R W	Millivolt	2.5-1000	D
76-2	Direction	SG3	27684	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-2	Timing Mode	SG3	27686	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-2	Time Delay	SG3	27688	Float	4	R W	Millisecond	0-6000000	D
76-2	Reset Delay	SG3	27690	Float	4	R W	Millisecond	0-6000000	D
76-2	Time Dial	SG3	27692	Float	4	R W	n/a	0-9.9	D
76-2	Curve	SG3	27694	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-2	Reset Timing	SG3	27696	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-2	A Constant	SG3	27698	Float	4	R W	n/a	0-600	D
76-2	B Constant	SG3	27700	Float	4	R W	n/a	0-25	D
76-2	C Constant	SG3	27702	Float	4	R W	n/a	0-1	D
76-2	N Constant	SG3	27704	Float	4	R W	n/a	0.5-2.5	D
76-2	R Constant	SG3	27706	Float	4	R W	n/a	0-30	D
76-3	Mode	SG0	27708	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27710						
76-3	Pickup	SG0	27712	Float	4	R W	Millivolt	2.5-1000	D
76-3	Direction	SG0	27714	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-3	Timing Mode	SG0	27716	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-3	Time Delay	SG0	27718	Float	4	R W	Millisecond	0-6000000	D
76-3	Reset Delay	SG0	27720	Float	4	R W	Millisecond	0-6000000	D
76-3	Time Dial	SG0	27722	Float	4	R W	n/a	0-9.9	D
76-3	Curve	SG0	27724	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-3	Reset Timing	SG0	27726	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-3	A Constant	SG0	27728	Float	4	R W	n/a	0-600	D
76-3	B Constant	SG0	27730	Float	4	R W	n/a	0-25	D
76-3	C Constant	SG0	27732	Float	4	R W	n/a	0-1	D
76-3	N Constant	SG0	27734	Float	4	R W	n/a	0.5-2.5	D
76-3	R Constant	SG0	27736	Float	4	R W	n/a	0-30	D
76-3	Mode	SG1	27738	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved		SG1	27740						
76-3	Pickup	SG1	27742	Float	4	R W	Millivolt	2.5–1000	D
76-3	Direction	SG1	27744	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-3	Timing Mode	SG1	27746	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-3	Time Delay	SG1	27748	Float	4	R W	Millisecond	0–6000000	D
76-3	Reset Delay	SG1	27750	Float	4	R W	Millisecond	0–6000000	D
76-3	Time Dial	SG1	27752	Float	4	R W	n/a	0–9.9	D
76-3	Curve	SG1	27754	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-3	Reset Timing	SG1	27756	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-3	A Constant	SG1	27758	Float	4	R W	n/a	0–600	D
76-3	B Constant	SG1	27760	Float	4	R W	n/a	0–25	D
76-3	C Constant	SG1	27762	Float	4	R W	n/a	0–1	D
76-3	N Constant	SG1	27764	Float	4	R W	n/a	0.5–2.5	D
76-3	R Constant	SG1	27766	Float	4	R W	n/a	0–30	D
76-3	Mode	SG2	27768	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27770						
76-3	Pickup	SG2	27772	Float	4	R W	Millivolt	2.5–1000	D
76-3	Direction	SG2	27774	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-3	Timing Mode	SG2	27776	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-3	Time Delay	SG2	27778	Float	4	R W	Millisecond	0–6000000	D
76-3	Reset Delay	SG2	27780	Float	4	R W	Millisecond	0–6000000	D
76-3	Time Dial	SG2	27782	Float	4	R W	n/a	0–9.9	D
76-3	Curve	SG2	27784	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-3	Reset Timing	SG2	27786	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-3	A Constant	SG2	27788	Float	4	R W	n/a	0–600	D
76-3	B Constant	SG2	27790	Float	4	R W	n/a	0–25	D
76-3	C Constant	SG2	27792	Float	4	R W	n/a	0–1	D
76-3	N Constant	SG2	27794	Float	4	R W	n/a	0.5–2.5	D
76-3	R Constant	SG2	27796	Float	4	R W	n/a	0–30	D
76-3	Mode	SG3	27798	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27800						
76-3	Pickup	SG3	27802	Float	4	R W	Millivolt	2.5–1000	D
76-3	Direction	SG3	27804	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-3	Timing Mode	SG3	27806	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-3	Time Delay	SG3	27808	Float	4	R W	Millisecond	0–6000000	D
76-3	Reset Delay	SG3	27810	Float	4	R W	Millisecond	0–6000000	D
76-3	Time Dial	SG3	27812	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-3	Curve	SG3	27814	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-3	Reset Timing	SG3	27816	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-3	A Constant	SG3	27818	Float	4	R W	n/a	0-600	D
76-3	B Constant	SG3	27820	Float	4	R W	n/a	0-25	D
76-3	C Constant	SG3	27822	Float	4	R W	n/a	0-1	D
76-3	N Constant	SG3	27824	Float	4	R W	n/a	0.5-2.5	D
76-3	R Constant	SG3	27826	Float	4	R W	n/a	0-30	D
76-4	Mode	SG0	27828	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27830						
76-4	Pickup	SG0	27832	Float	4	R W	Millivolt	2.5-1000	D
76-4	Direction	SG0	27834	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-4	Timing Mode	SG0	27836	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-4	Time Delay	SG0	27838	Float	4	R W	Millisecond	0-6000000	D
76-4	Reset Delay	SG0	27840	Float	4	R W	Millisecond	0-6000000	D
76-4	Time Dial	SG0	27842	Float	4	R W	n/a	0-9.9	D
76-4	Curve	SG0	27844	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-4	Reset Timing	SG0	27846	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-4	A Constant	SG0	27848	Float	4	R W	n/a	0-600	D
76-4	B Constant	SG0	27850	Float	4	R W	n/a	0-25	D
76-4	C Constant	SG0	27852	Float	4	R W	n/a	0-1	D
76-4	N Constant	SG0	27854	Float	4	R W	n/a	0.5-2.5	D
76-4	R Constant	SG0	27856	Float	4	R W	n/a	0-30	D
76-4	Mode	SG1	27858	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	27860						
76-4	Pickup	SG1	27862	Float	4	R W	Millivolt	2.5-1000	D
76-4	Direction	SG1	27864	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-4	Timing Mode	SG1	27866	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-4	Time Delay	SG1	27868	Float	4	R W	Millisecond	0-6000000	D
76-4	Reset Delay	SG1	27870	Float	4	R W	Millisecond	0-6000000	D
76-4	Time Dial	SG1	27872	Float	4	R W	n/a	0-9.9	D
76-4	Curve	SG1	27874	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-4	Reset Timing	SG1	27876	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-4	A Constant	SG1	27878	Float	4	R W	n/a	0-600	D
76-4	B Constant	SG1	27880	Float	4	R W	n/a	0-25	D
76-4	C Constant	SG1	27882	Float	4	R W	n/a	0-1	D
76-4	N Constant	SG1	27884	Float	4	R W	n/a	0.5-2.5	D
76-4	R Constant	SG1	27886	Float	4	R W	n/a	0-30	D
76-4	Mode	SG2	27888	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			27890						
76-4	Pickup	SG2	27892	Float	4	R W	Millivolt	2.5–1000	D
76-4	Direction	SG2	27894	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-4	Timing Mode	SG2	27896	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-4	Time Delay	SG2	27898	Float	4	R W	Millisecond	0–6000000	D
76-4	Reset Delay	SG2	27900	Float	4	R W	Millisecond	0–6000000	D
76-4	Time Dial	SG2	27902	Float	4	R W	n/a	0–9.9	D
76-4	Curve	SG2	27904	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-4	Reset Timing	SG2	27906	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-4	A Constant	SG2	27908	Float	4	R W	n/a	0–600	D
76-4	B Constant	SG2	27910	Float	4	R W	n/a	0–25	D
76-4	C Constant	SG2	27912	Float	4	R W	n/a	0–1	D
76-4	N Constant	SG2	27914	Float	4	R W	n/a	0.5–2.5	D
76-4	R Constant	SG2	27916	Float	4	R W	n/a	0–30	D
76-4	Mode	SG3	27918	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27920						
76-4	Pickup	SG3	27922	Float	4	R W	Millivolt	2.5–1000	D
76-4	Direction	SG3	27924	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-4	Timing Mode	SG3	27926	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-4	Time Delay	SG3	27928	Float	4	R W	Millisecond	0–6000000	D
76-4	Reset Delay	SG3	27930	Float	4	R W	Millisecond	0–6000000	D
76-4	Time Dial	SG3	27932	Float	4	R W	n/a	0–9.9	D
76-4	Curve	SG3	27934	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-4	Reset Timing	SG3	27936	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-4	A Constant	SG3	27938	Float	4	R W	n/a	0–600	D
76-4	B Constant	SG3	27940	Float	4	R W	n/a	0–25	D
76-4	C Constant	SG3	27942	Float	4	R W	n/a	0–1	D
76-4	N Constant	SG3	27944	Float	4	R W	n/a	0.5–2.5	D
76-4	R Constant	SG3	27946	Float	4	R W	n/a	0–30	D
76-5	Mode	SG0	27948	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			27950						
76-5	Pickup	SG0	27952	Float	4	R W	Millivolt	2.5–1000	D
76-5	Direction	SG0	27954	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-5	Timing Mode	SG0	27956	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-5	Time Delay	SG0	27958	Float	4	R W	Millisecond	0–6000000	D
76-5	Reset Delay	SG0	27960	Float	4	R W	Millisecond	0–6000000	D
76-5	Time Dial	SG0	27962	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-5	Curve	SG0	27964	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-5	Reset Timing	SG0	27966	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-5	A Constant	SG0	27968	Float	4	R W	n/a	0-600	D
76-5	B Constant	SG0	27970	Float	4	R W	n/a	0-25	D
76-5	C Constant	SG0	27972	Float	4	R W	n/a	0-1	D
76-5	N Constant	SG0	27974	Float	4	R W	n/a	0.5-2.5	D
76-5	R Constant	SG0	27976	Float	4	R W	n/a	0-30	D
76-5	Mode	SG1	27978	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	27980						
76-5	Pickup	SG1	27982	Float	4	R W	Millivolt	2.5-1000	D
76-5	Direction	SG1	27984	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-5	Timing Mode	SG1	27986	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-5	Time Delay	SG1	27988	Float	4	R W	Millisecond	0-6000000	D
76-5	Reset Delay	SG1	27990	Float	4	R W	Millisecond	0-6000000	D
76-5	Time Dial	SG1	27992	Float	4	R W	n/a	0-9.9	D
76-5	Curve	SG1	27994	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-5	Reset Timing	SG1	27996	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-5	A Constant	SG1	27998	Float	4	R W	n/a	0-600	D
76-5	B Constant	SG1	28000	Float	4	R W	n/a	0-25	D
76-5	C Constant	SG1	28002	Float	4	R W	n/a	0-1	D
76-5	N Constant	SG1	28004	Float	4	R W	n/a	0.5-2.5	D
76-5	R Constant	SG1	28006	Float	4	R W	n/a	0-30	D
76-5	Mode	SG2	28008	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28010						
76-5	Pickup	SG2	28012	Float	4	R W	Millivolt	2.5-1000	D
76-5	Direction	SG2	28014	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-5	Timing Mode	SG2	28016	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-5	Time Delay	SG2	28018	Float	4	R W	Millisecond	0-6000000	D
76-5	Reset Delay	SG2	28020	Float	4	R W	Millisecond	0-6000000	D
76-5	Time Dial	SG2	28022	Float	4	R W	n/a	0-9.9	D
76-5	Curve	SG2	28024	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-5	Reset Timing	SG2	28026	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-5	A Constant	SG2	28028	Float	4	R W	n/a	0-600	D
76-5	B Constant	SG2	28030	Float	4	R W	n/a	0-25	D
76-5	C Constant	SG2	28032	Float	4	R W	n/a	0-1	D
76-5	N Constant	SG2	28034	Float	4	R W	n/a	0.5-2.5	D
76-5	R Constant	SG2	28036	Float	4	R W	n/a	0-30	D
76-5	Mode	SG3	28038	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			28040						
76-5	Pickup	SG3	28042	Float	4	R W	Millivolt	2.5–1000	D
76-5	Direction	SG3	28044	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-5	Timing Mode	SG3	28046	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-5	Time Delay	SG3	28048	Float	4	R W	Millisecond	0–6000000	D
76-5	Reset Delay	SG3	28050	Float	4	R W	Millisecond	0–6000000	D
76-5	Time Dial	SG3	28052	Float	4	R W	n/a	0–9.9	D
76-5	Curve	SG3	28054	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-5	Reset Timing	SG3	28056	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-5	A Constant	SG3	28058	Float	4	R W	n/a	0–600	D
76-5	B Constant	SG3	28060	Float	4	R W	n/a	0–25	D
76-5	C Constant	SG3	28062	Float	4	R W	n/a	0–1	D
76-5	N Constant	SG3	28064	Float	4	R W	n/a	0.5–2.5	D
76-5	R Constant	SG3	28066	Float	4	R W	n/a	0–30	D
76-6	Mode	SG0	28068	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28070						
76-6	Pickup	SG0	28072	Float	4	R W	Millivolt	2.5–1000	D
76-6	Direction	SG0	28074	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-6	Timing Mode	SG0	28076	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-6	Time Delay	SG0	28078	Float	4	R W	Millisecond	0–6000000	D
76-6	Reset Delay	SG0	28080	Float	4	R W	Millisecond	0–6000000	D
76-6	Time Dial	SG0	28082	Float	4	R W	n/a	0–9.9	D
76-6	Curve	SG0	28084	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-6	Reset Timing	SG0	28086	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-6	A Constant	SG0	28088	Float	4	R W	n/a	0–600	D
76-6	B Constant	SG0	28090	Float	4	R W	n/a	0–25	D
76-6	C Constant	SG0	28092	Float	4	R W	n/a	0–1	D
76-6	N Constant	SG0	28094	Float	4	R W	n/a	0.5–2.5	D
76-6	R Constant	SG0	28096	Float	4	R W	n/a	0–30	D
76-6	Mode	SG1	28098	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28100						
76-6	Pickup	SG1	28102	Float	4	R W	Millivolt	2.5–1000	D
76-6	Direction	SG1	28104	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-6	Timing Mode	SG1	28106	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-6	Time Delay	SG1	28108	Float	4	R W	Millisecond	0–6000000	D
76-6	Reset Delay	SG1	28110	Float	4	R W	Millisecond	0–6000000	D
76-6	Time Dial	SG1	28112	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-6	Curve	SG1	28114	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-6	Reset Timing	SG1	28116	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-6	A Constant	SG1	28118	Float	4	R W	n/a	0-600	D
76-6	B Constant	SG1	28120	Float	4	R W	n/a	0-25	D
76-6	C Constant	SG1	28122	Float	4	R W	n/a	0-1	D
76-6	N Constant	SG1	28124	Float	4	R W	n/a	0.5-2.5	D
76-6	R Constant	SG1	28126	Float	4	R W	n/a	0-30	D
76-6	Mode	SG2	28128	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28130						
76-6	Pickup	SG2	28132	Float	4	R W	Millivolt	2.5-1000	D
76-6	Direction	SG2	28134	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-6	Timing Mode	SG2	28136	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-6	Time Delay	SG2	28138	Float	4	R W	Millisecond	0-6000000	D
76-6	Reset Delay	SG2	28140	Float	4	R W	Millisecond	0-6000000	D
76-6	Time Dial	SG2	28142	Float	4	R W	n/a	0-9.9	D
76-6	Curve	SG2	28144	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-6	Reset Timing	SG2	28146	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-6	A Constant	SG2	28148	Float	4	R W	n/a	0-600	D
76-6	B Constant	SG2	28150	Float	4	R W	n/a	0-25	D
76-6	C Constant	SG2	28152	Float	4	R W	n/a	0-1	D
76-6	N Constant	SG2	28154	Float	4	R W	n/a	0.5-2.5	D
76-6	R Constant	SG2	28156	Float	4	R W	n/a	0-30	D
76-6	Mode	SG3	28158	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28160						
76-6	Pickup	SG3	28162	Float	4	R W	Millivolt	2.5-1000	D
76-6	Direction	SG3	28164	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-6	Timing Mode	SG3	28166	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-6	Time Delay	SG3	28168	Float	4	R W	Millisecond	0-6000000	D
76-6	Reset Delay	SG3	28170	Float	4	R W	Millisecond	0-6000000	D
76-6	Time Dial	SG3	28172	Float	4	R W	n/a	0-9.9	D
76-6	Curve	SG3	28174	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-6	Reset Timing	SG3	28176	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-6	A Constant	SG3	28178	Float	4	R W	n/a	0-600	D
76-6	B Constant	SG3	28180	Float	4	R W	n/a	0-25	D
76-6	C Constant	SG3	28182	Float	4	R W	n/a	0-1	D
76-6	N Constant	SG3	28184	Float	4	R W	n/a	0.5-2.5	D
76-6	R Constant	SG3	28186	Float	4	R W	n/a	0-30	D
76-7	Mode	SG0	28188	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			28190						
76-7	Pickup	SG0	28192	Float	4	R W	Millivolt	2.5–1000	D
76-7	Direction	SG0	28194	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-7	Timing Mode	SG0	28196	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-7	Time Delay	SG0	28198	Float	4	R W	Millisecond	0–6000000	D
76-7	Reset Delay	SG0	28200	Float	4	R W	Millisecond	0–6000000	D
76-7	Time Dial	SG0	28202	Float	4	R W	n/a	0–9.9	D
76-7	Curve	SG0	28204	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-7	Reset Timing	SG0	28206	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-7	A Constant	SG0	28208	Float	4	R W	n/a	0–600	D
76-7	B Constant	SG0	28210	Float	4	R W	n/a	0–25	D
76-7	C Constant	SG0	28212	Float	4	R W	n/a	0–1	D
76-7	N Constant	SG0	28214	Float	4	R W	n/a	0.5–2.5	D
76-7	R Constant	SG0	28216	Float	4	R W	n/a	0–30	D
76-7	Mode	SG1	28218	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28220						
76-7	Pickup	SG1	28222	Float	4	R W	Millivolt	2.5–1000	D
76-7	Direction	SG1	28224	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-7	Timing Mode	SG1	28226	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-7	Time Delay	SG1	28228	Float	4	R W	Millisecond	0–6000000	D
76-7	Reset Delay	SG1	28230	Float	4	R W	Millisecond	0–6000000	D
76-7	Time Dial	SG1	28232	Float	4	R W	n/a	0–9.9	D
76-7	Curve	SG1	28234	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-7	Reset Timing	SG1	28236	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-7	A Constant	SG1	28238	Float	4	R W	n/a	0–600	D
76-7	B Constant	SG1	28240	Float	4	R W	n/a	0–25	D
76-7	C Constant	SG1	28242	Float	4	R W	n/a	0–1	D
76-7	N Constant	SG1	28244	Float	4	R W	n/a	0.5–2.5	D
76-7	R Constant	SG1	28246	Float	4	R W	n/a	0–30	D
76-7	Mode	SG2	28248	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28250						
76-7	Pickup	SG2	28252	Float	4	R W	Millivolt	2.5–1000	D
76-7	Direction	SG2	28254	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-7	Timing Mode	SG2	28256	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-7	Time Delay	SG2	28258	Float	4	R W	Millisecond	0–6000000	D
76-7	Reset Delay	SG2	28260	Float	4	R W	Millisecond	0–6000000	D
76-7	Time Dial	SG2	28262	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-7	Curve	SG2	28264	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-7	Reset Timing	SG2	28266	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-7	A Constant	SG2	28268	Float	4	R W	n/a	0-600	D
76-7	B Constant	SG2	28270	Float	4	R W	n/a	0-25	D
76-7	C Constant	SG2	28272	Float	4	R W	n/a	0-1	D
76-7	N Constant	SG2	28274	Float	4	R W	n/a	0.5-2.5	D
76-7	R Constant	SG2	28276	Float	4	R W	n/a	0-30	D
76-7	Mode	SG3	28278	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28280						
76-7	Pickup	SG3	28282	Float	4	R W	Millivolt	2.5-1000	D
76-7	Direction	SG3	28284	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-7	Timing Mode	SG3	28286	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-7	Time Delay	SG3	28288	Float	4	R W	Millisecond	0-6000000	D
76-7	Reset Delay	SG3	28290	Float	4	R W	Millisecond	0-6000000	D
76-7	Time Dial	SG3	28292	Float	4	R W	n/a	0-9.9	D
76-7	Curve	SG3	28294	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-7	Reset Timing	SG3	28296	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-7	A Constant	SG3	28298	Float	4	R W	n/a	0-600	D
76-7	B Constant	SG3	28300	Float	4	R W	n/a	0-25	D
76-7	C Constant	SG3	28302	Float	4	R W	n/a	0-1	D
76-7	N Constant	SG3	28304	Float	4	R W	n/a	0.5-2.5	D
76-7	R Constant	SG3	28306	Float	4	R W	n/a	0-30	D
76-8	Mode	SG0	28308	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28310						
76-8	Pickup	SG0	28312	Float	4	R W	Millivolt	2.5-1000	D
76-8	Direction	SG0	28314	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-8	Timing Mode	SG0	28316	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-8	Time Delay	SG0	28318	Float	4	R W	Millisecond	0-6000000	D
76-8	Reset Delay	SG0	28320	Float	4	R W	Millisecond	0-6000000	D
76-8	Time Dial	SG0	28322	Float	4	R W	n/a	0-9.9	D
76-8	Curve	SG0	28324	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-8	Reset Timing	SG0	28326	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-8	A Constant	SG0	28328	Float	4	R W	n/a	0-600	D
76-8	B Constant	SG0	28330	Float	4	R W	n/a	0-25	D
76-8	C Constant	SG0	28332	Float	4	R W	n/a	0-1	D
76-8	N Constant	SG0	28334	Float	4	R W	n/a	0.5-2.5	D
76-8	R Constant	SG0	28336	Float	4	R W	n/a	0-30	D
76-8	Mode	SG1	28338	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved		SG1	28340						
76-8	Pickup	SG1	28342	Float	4	R W	Millivolt	2.5–1000	D
76-8	Direction	SG1	28344	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-8	Timing Mode	SG1	28346	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-8	Time Delay	SG1	28348	Float	4	R W	Millisecond	0–6000000	D
76-8	Reset Delay	SG1	28350	Float	4	R W	Millisecond	0–6000000	D
76-8	Time Dial	SG1	28352	Float	4	R W	n/a	0–9.9	D
76-8	Curve	SG1	28354	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-8	Reset Timing	SG1	28356	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-8	A Constant	SG1	28358	Float	4	R W	n/a	0–600	D
76-8	B Constant	SG1	28360	Float	4	R W	n/a	0–25	D
76-8	C Constant	SG1	28362	Float	4	R W	n/a	0–1	D
76-8	N Constant	SG1	28364	Float	4	R W	n/a	0.5–2.5	D
76-8	R Constant	SG1	28366	Float	4	R W	n/a	0–30	D
76-8	Mode	SG2	28368	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28370						
76-8	Pickup	SG2	28372	Float	4	R W	Millivolt	2.5–1000	D
76-8	Direction	SG2	28374	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-8	Timing Mode	SG2	28376	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-8	Time Delay	SG2	28378	Float	4	R W	Millisecond	0–6000000	D
76-8	Reset Delay	SG2	28380	Float	4	R W	Millisecond	0–6000000	D
76-8	Time Dial	SG2	28382	Float	4	R W	n/a	0–9.9	D
76-8	Curve	SG2	28384	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-8	Reset Timing	SG2	28386	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-8	A Constant	SG2	28388	Float	4	R W	n/a	0–600	D
76-8	B Constant	SG2	28390	Float	4	R W	n/a	0–25	D
76-8	C Constant	SG2	28392	Float	4	R W	n/a	0–1	D
76-8	N Constant	SG2	28394	Float	4	R W	n/a	0.5–2.5	D
76-8	R Constant	SG2	28396	Float	4	R W	n/a	0–30	D
76-8	Mode	SG3	28398	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28400						
76-8	Pickup	SG3	28402	Float	4	R W	Millivolt	2.5–1000	D
76-8	Direction	SG3	28404	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-8	Timing Mode	SG3	28406	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-8	Time Delay	SG3	28408	Float	4	R W	Millisecond	0–6000000	D
76-8	Reset Delay	SG3	28410	Float	4	R W	Millisecond	0–6000000	D
76-8	Time Dial	SG3	28412	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-8	Curve	SG3	28414	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-8	Reset Timing	SG3	28416	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-8	A Constant	SG3	28418	Float	4	R W	n/a	0-600	D
76-8	B Constant	SG3	28420	Float	4	R W	n/a	0-25	D
76-8	C Constant	SG3	28422	Float	4	R W	n/a	0-1	D
76-8	N Constant	SG3	28424	Float	4	R W	n/a	0.5-2.5	D
76-8	R Constant	SG3	28426	Float	4	R W	n/a	0-30	D
76-9	Mode	SG0	28428	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28430						
76-9	Pickup	SG0	28432	Float	4	R W	Millivolt	2.5-1000	D
76-9	Direction	SG0	28434	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-9	Timing Mode	SG0	28436	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-9	Time Delay	SG0	28438	Float	4	R W	Millisecond	0-6000000	D
76-9	Reset Delay	SG0	28440	Float	4	R W	Millisecond	0-6000000	D
76-9	Time Dial	SG0	28442	Float	4	R W	n/a	0-9.9	D
76-9	Curve	SG0	28444	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-9	Reset Timing	SG0	28446	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-9	A Constant	SG0	28448	Float	4	R W	n/a	0-600	D
76-9	B Constant	SG0	28450	Float	4	R W	n/a	0-25	D
76-9	C Constant	SG0	28452	Float	4	R W	n/a	0-1	D
76-9	N Constant	SG0	28454	Float	4	R W	n/a	0.5-2.5	D
76-9	R Constant	SG0	28456	Float	4	R W	n/a	0-30	D
76-9	Mode	SG1	28458	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28460						
76-9	Pickup	SG1	28462	Float	4	R W	Millivolt	2.5-1000	D
76-9	Direction	SG1	28464	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-9	Timing Mode	SG1	28466	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-9	Time Delay	SG1	28468	Float	4	R W	Millisecond	0-6000000	D
76-9	Reset Delay	SG1	28470	Float	4	R W	Millisecond	0-6000000	D
76-9	Time Dial	SG1	28472	Float	4	R W	n/a	0-9.9	D
76-9	Curve	SG1	28474	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-9	Reset Timing	SG1	28476	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-9	A Constant	SG1	28478	Float	4	R W	n/a	0-600	D
76-9	B Constant	SG1	28480	Float	4	R W	n/a	0-25	D
76-9	C Constant	SG1	28482	Float	4	R W	n/a	0-1	D
76-9	N Constant	SG1	28484	Float	4	R W	n/a	0.5-2.5	D
76-9	R Constant	SG1	28486	Float	4	R W	n/a	0-30	D
76-9	Mode	SG2	28488	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			28490						
76-9	Pickup	SG2	28492	Float	4	R W	Millivolt	2.5–1000	D
76-9	Direction	SG2	28494	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-9	Timing Mode	SG2	28496	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-9	Time Delay	SG2	28498	Float	4	R W	Millisecond	0–6000000	D
76-9	Reset Delay	SG2	28500	Float	4	R W	Millisecond	0–6000000	D
76-9	Time Dial	SG2	28502	Float	4	R W	n/a	0–9.9	D
76-9	Curve	SG2	28504	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-9	Reset Timing	SG2	28506	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-9	A Constant	SG2	28508	Float	4	R W	n/a	0–600	D
76-9	B Constant	SG2	28510	Float	4	R W	n/a	0–25	D
76-9	C Constant	SG2	28512	Float	4	R W	n/a	0–1	D
76-9	N Constant	SG2	28514	Float	4	R W	n/a	0.5–2.5	D
76-9	R Constant	SG2	28516	Float	4	R W	n/a	0–30	D
76-9	Mode	SG3	28518	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28520						
76-9	Pickup	SG3	28522	Float	4	R W	Millivolt	2.5–1000	D
76-9	Direction	SG3	28524	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-9	Timing Mode	SG3	28526	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-9	Time Delay	SG3	28528	Float	4	R W	Millisecond	0–6000000	D
76-9	Reset Delay	SG3	28530	Float	4	R W	Millisecond	0–6000000	D
76-9	Time Dial	SG3	28532	Float	4	R W	n/a	0–9.9	D
76-9	Curve	SG3	28534	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-9	Reset Timing	SG3	28536	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-9	A Constant	SG3	28538	Float	4	R W	n/a	0–600	D
76-9	B Constant	SG3	28540	Float	4	R W	n/a	0–25	D
76-9	C Constant	SG3	28542	Float	4	R W	n/a	0–1	D
76-9	N Constant	SG3	28544	Float	4	R W	n/a	0.5–2.5	D
76-9	R Constant	SG3	28546	Float	4	R W	n/a	0–30	D
76-10	Mode	SG0	28548	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28550						
76-10	Pickup	SG0	28552	Float	4	R W	Millivolt	2.5–1000	D
76-10	Direction	SG0	28554	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-10	Timing Mode	SG0	28556	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-10	Time Delay	SG0	28558	Float	4	R W	Millisecond	0–6000000	D
76-10	Reset Delay	SG0	28560	Float	4	R W	Millisecond	0–6000000	D
76-10	Time Dial	SG0	28562	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-10	Curve	SG0	28564	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-10	Reset Timing	SG0	28566	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-10	A Constant	SG0	28568	Float	4	R W	n/a	0-600	D
76-10	B Constant	SG0	28570	Float	4	R W	n/a	0-25	D
76-10	C Constant	SG0	28572	Float	4	R W	n/a	0-1	D
76-10	N Constant	SG0	28574	Float	4	R W	n/a	0.5-2.5	D
76-10	R Constant	SG0	28576	Float	4	R W	n/a	0-30	D
76-10	Mode	SG1	28578	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28580						
76-10	Pickup	SG1	28582	Float	4	R W	Millivolt	2.5-1000	D
76-10	Direction	SG1	28584	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-10	Timing Mode	SG1	28586	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-10	Time Delay	SG1	28588	Float	4	R W	Millisecond	0-6000000	D
76-10	Reset Delay	SG1	28590	Float	4	R W	Millisecond	0-6000000	D
76-10	Time Dial	SG1	28592	Float	4	R W	n/a	0-9.9	D
76-10	Curve	SG1	28594	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-10	Reset Timing	SG1	28596	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-10	A Constant	SG1	28598	Float	4	R W	n/a	0-600	D
76-10	B Constant	SG1	28600	Float	4	R W	n/a	0-25	D
76-10	C Constant	SG1	28602	Float	4	R W	n/a	0-1	D
76-10	N Constant	SG1	28604	Float	4	R W	n/a	0.5-2.5	D
76-10	R Constant	SG1	28606	Float	4	R W	n/a	0-30	D
76-10	Mode	SG2	28608	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28610						
76-10	Pickup	SG2	28612	Float	4	R W	Millivolt	2.5-1000	D
76-10	Direction	SG2	28614	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-10	Timing Mode	SG2	28616	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-10	Time Delay	SG2	28618	Float	4	R W	Millisecond	0-6000000	D
76-10	Reset Delay	SG2	28620	Float	4	R W	Millisecond	0-6000000	D
76-10	Time Dial	SG2	28622	Float	4	R W	n/a	0-9.9	D
76-10	Curve	SG2	28624	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-10	Reset Timing	SG2	28626	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-10	A Constant	SG2	28628	Float	4	R W	n/a	0-600	D
76-10	B Constant	SG2	28630	Float	4	R W	n/a	0-25	D
76-10	C Constant	SG2	28632	Float	4	R W	n/a	0-1	D
76-10	N Constant	SG2	28634	Float	4	R W	n/a	0.5-2.5	D
76-10	R Constant	SG2	28636	Float	4	R W	n/a	0-30	D
76-10	Mode	SG3	28638	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			28640						
76-10	Pickup	SG3	28642	Float	4	R W	Millivolt	2.5–1000	D
76-10	Direction	SG3	28644	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-10	Timing Mode	SG3	28646	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-10	Time Delay	SG3	28648	Float	4	R W	Millisecond	0–6000000	D
76-10	Reset Delay	SG3	28650	Float	4	R W	Millisecond	0–6000000	D
76-10	Time Dial	SG3	28652	Float	4	R W	n/a	0–9.9	D
76-10	Curve	SG3	28654	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-10	Reset Timing	SG3	28656	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-10	A Constant	SG3	28658	Float	4	R W	n/a	0–600	D
76-10	B Constant	SG3	28660	Float	4	R W	n/a	0–25	D
76-10	C Constant	SG3	28662	Float	4	R W	n/a	0–1	D
76-10	N Constant	SG3	28664	Float	4	R W	n/a	0.5–2.5	D
76-10	R Constant	SG3	28666	Float	4	R W	n/a	0–30	D
76-11	Mode	SG0	28668	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28670						
76-11	Pickup	SG0	28672	Float	4	R W	Millivolt	2.5–1000	D
76-11	Direction	SG0	28674	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-11	Timing Mode	SG0	28676	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-11	Time Delay	SG0	28678	Float	4	R W	Millisecond	0–6000000	D
76-11	Reset Delay	SG0	28680	Float	4	R W	Millisecond	0–6000000	D
76-11	Time Dial	SG0	28682	Float	4	R W	n/a	0–9.9	D
76-11	Curve	SG0	28684	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-11	Reset Timing	SG0	28686	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-11	A Constant	SG0	28688	Float	4	R W	n/a	0–600	D
76-11	B Constant	SG0	28690	Float	4	R W	n/a	0–25	D
76-11	C Constant	SG0	28692	Float	4	R W	n/a	0–1	D
76-11	N Constant	SG0	28694	Float	4	R W	n/a	0.5–2.5	D
76-11	R Constant	SG0	28696	Float	4	R W	n/a	0–30	D
76-11	Mode	SG1	28698	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28700						
76-11	Pickup	SG1	28702	Float	4	R W	Millivolt	2.5–1000	D
76-11	Direction	SG1	28704	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-11	Timing Mode	SG1	28706	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-11	Time Delay	SG1	28708	Float	4	R W	Millisecond	0–6000000	D
76-11	Reset Delay	SG1	28710	Float	4	R W	Millisecond	0–6000000	D
76-11	Time Dial	SG1	28712	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-11	Curve	SG1	28714	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-11	Reset Timing	SG1	28716	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-11	A Constant	SG1	28718	Float	4	R W	n/a	0-600	D
76-11	B Constant	SG1	28720	Float	4	R W	n/a	0-25	D
76-11	C Constant	SG1	28722	Float	4	R W	n/a	0-1	D
76-11	N Constant	SG1	28724	Float	4	R W	n/a	0.5-2.5	D
76-11	R Constant	SG1	28726	Float	4	R W	n/a	0-30	D
76-11	Mode	SG2	28728	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28730						
76-11	Pickup	SG2	28732	Float	4	R W	Millivolt	2.5-1000	D
76-11	Direction	SG2	28734	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-11	Timing Mode	SG2	28736	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-11	Time Delay	SG2	28738	Float	4	R W	Millisecond	0-6000000	D
76-11	Reset Delay	SG2	28740	Float	4	R W	Millisecond	0-6000000	D
76-11	Time Dial	SG2	28742	Float	4	R W	n/a	0-9.9	D
76-11	Curve	SG2	28744	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-11	Reset Timing	SG2	28746	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-11	A Constant	SG2	28748	Float	4	R W	n/a	0-600	D
76-11	B Constant	SG2	28750	Float	4	R W	n/a	0-25	D
76-11	C Constant	SG2	28752	Float	4	R W	n/a	0-1	D
76-11	N Constant	SG2	28754	Float	4	R W	n/a	0.5-2.5	D
76-11	R Constant	SG2	28756	Float	4	R W	n/a	0-30	D
76-11	Mode	SG3	28758	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28760						
76-11	Pickup	SG3	28762	Float	4	R W	Millivolt	2.5-1000	D
76-11	Direction	SG3	28764	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-11	Timing Mode	SG3	28766	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-11	Time Delay	SG3	28768	Float	4	R W	Millisecond	0-6000000	D
76-11	Reset Delay	SG3	28770	Float	4	R W	Millisecond	0-6000000	D
76-11	Time Dial	SG3	28772	Float	4	R W	n/a	0-9.9	D
76-11	Curve	SG3	28774	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-11	Reset Timing	SG3	28776	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-11	A Constant	SG3	28778	Float	4	R W	n/a	0-600	D
76-11	B Constant	SG3	28780	Float	4	R W	n/a	0-25	D
76-11	C Constant	SG3	28782	Float	4	R W	n/a	0-1	D
76-11	N Constant	SG3	28784	Float	4	R W	n/a	0.5-2.5	D
76-11	R Constant	SG3	28786	Float	4	R W	n/a	0-30	D
76-12	Mode	SG0	28788	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved			28790						
76-12	Pickup	SG0	28792	Float	4	R W	Millivolt	2.5–1000	D
76-12	Direction	SG0	28794	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-12	Timing Mode	SG0	28796	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-12	Time Delay	SG0	28798	Float	4	R W	Millisecond	0–6000000	D
76-12	Reset Delay	SG0	28800	Float	4	R W	Millisecond	0–6000000	D
76-12	Time Dial	SG0	28802	Float	4	R W	n/a	0–9.9	D
76-12	Curve	SG0	28804	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-12	Reset Timing	SG0	28806	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-12	A Constant	SG0	28808	Float	4	R W	n/a	0–600	D
76-12	B Constant	SG0	28810	Float	4	R W	n/a	0–25	D
76-12	C Constant	SG0	28812	Float	4	R W	n/a	0–1	D
76-12	N Constant	SG0	28814	Float	4	R W	n/a	0.5–2.5	D
76-12	R Constant	SG0	28816	Float	4	R W	n/a	0–30	D
76-12	Mode	SG1	28818	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved		SG1	28820						
76-12	Pickup	SG1	28822	Float	4	R W	Millivolt	2.5–1000	D
76-12	Direction	SG1	28824	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-12	Timing Mode	SG1	28826	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-12	Time Delay	SG1	28828	Float	4	R W	Millisecond	0–6000000	D
76-12	Reset Delay	SG1	28830	Float	4	R W	Millisecond	0–6000000	D
76-12	Time Dial	SG1	28832	Float	4	R W	n/a	0–9.9	D
76-12	Curve	SG1	28834	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-12	Reset Timing	SG1	28836	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-12	A Constant	SG1	28838	Float	4	R W	n/a	0–600	D
76-12	B Constant	SG1	28840	Float	4	R W	n/a	0–25	D
76-12	C Constant	SG1	28842	Float	4	R W	n/a	0–1	D
76-12	N Constant	SG1	28844	Float	4	R W	n/a	0.5–2.5	D
76-12	R Constant	SG1	28846	Float	4	R W	n/a	0–30	D
76-12	Mode	SG2	28848	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28850						
76-12	Pickup	SG2	28852	Float	4	R W	Millivolt	2.5–1000	D
76-12	Direction	SG2	28854	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-12	Timing Mode	SG2	28856	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-12	Time Delay	SG2	28858	Float	4	R W	Millisecond	0–6000000	D
76-12	Reset Delay	SG2	28860	Float	4	R W	Millisecond	0–6000000	D
76-12	Time Dial	SG2	28862	Float	4	R W	n/a	0–9.9	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-12	Curve	SG2	28864	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-12	Reset Timing	SG2	28866	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-12	A Constant	SG2	28868	Float	4	R W	n/a	0-600	D
76-12	B Constant	SG2	28870	Float	4	R W	n/a	0-25	D
76-12	C Constant	SG2	28872	Float	4	R W	n/a	0-1	D
76-12	N Constant	SG2	28874	Float	4	R W	n/a	0.5-2.5	D
76-12	R Constant	SG2	28876	Float	4	R W	n/a	0-30	D
76-12	Mode	SG3	28878	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28880						
76-12	Pickup	SG3	28882	Float	4	R W	Millivolt	2.5-1000	D
76-12	Direction	SG3	28884	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-12	Timing Mode	SG3	28886	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-12	Time Delay	SG3	28888	Float	4	R W	Millisecond	0-6000000	D
76-12	Reset Delay	SG3	28890	Float	4	R W	Millisecond	0-6000000	D
76-12	Time Dial	SG3	28892	Float	4	R W	n/a	0-9.9	D
76-12	Curve	SG3	28894	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-12	Reset Timing	SG3	28896	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-12	A Constant	SG3	28898	Float	4	R W	n/a	0-600	D
76-12	B Constant	SG3	28900	Float	4	R W	n/a	0-25	D
76-12	C Constant	SG3	28902	Float	4	R W	n/a	0-1	D
76-12	N Constant	SG3	28904	Float	4	R W	n/a	0.5-2.5	D
76-12	R Constant	SG3	28906	Float	4	R W	n/a	0-30	D
76-13	Mode	SG0	28908	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28910						
76-13	Pickup	SG0	28912	Float	4	R W	Millivolt	2.5-1000	D
76-13	Direction	SG0	28914	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-13	Timing Mode	SG0	28916	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-13	Time Delay	SG0	28918	Float	4	R W	Millisecond	0-6000000	D
76-13	Reset Delay	SG0	28920	Float	4	R W	Millisecond	0-6000000	D
76-13	Time Dial	SG0	28922	Float	4	R W	n/a	0-9.9	D
76-13	Curve	SG0	28924	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-13	Reset Timing	SG0	28926	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-13	A Constant	SG0	28928	Float	4	R W	n/a	0-600	D
76-13	B Constant	SG0	28930	Float	4	R W	n/a	0-25	D
76-13	C Constant	SG0	28932	Float	4	R W	n/a	0-1	D
76-13	N Constant	SG0	28934	Float	4	R W	n/a	0.5-2.5	D
76-13	R Constant	SG0	28936	Float	4	R W	n/a	0-30	D
76-13	Mode	SG1	28938	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Reserved		SG1	28940						
76-13	Pickup	SG1	28942	Float	4	R W	Millivolt	2.5–1000	D
76-13	Direction	SG1	28944	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-13	Timing Mode	SG1	28946	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-13	Time Delay	SG1	28948	Float	4	R W	Millisecond	0–6000000	D
76-13	Reset Delay	SG1	28950	Float	4	R W	Millisecond	0–6000000	D
76-13	Time Dial	SG1	28952	Float	4	R W	n/a	0–9.9	D
76-13	Curve	SG1	28954	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-13	Reset Timing	SG1	28956	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-13	A Constant	SG1	28958	Float	4	R W	n/a	0–600	D
76-13	B Constant	SG1	28960	Float	4	R W	n/a	0–25	D
76-13	C Constant	SG1	28962	Float	4	R W	n/a	0–1	D
76-13	N Constant	SG1	28964	Float	4	R W	n/a	0.5–2.5	D
76-13	R Constant	SG1	28966	Float	4	R W	n/a	0–30	D
76-13	Mode	SG2	28968	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			28970						
76-13	Pickup	SG2	28972	Float	4	R W	Millivolt	2.5–1000	D
76-13	Direction	SG2	28974	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-13	Timing Mode	SG2	28976	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-13	Time Delay	SG2	28978	Float	4	R W	Millisecond	0–6000000	D
76-13	Reset Delay	SG2	28980	Float	4	R W	Millisecond	0–6000000	D
76-13	Time Dial	SG2	28982	Float	4	R W	n/a	0–9.9	D
76-13	Curve	SG2	28984	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-13	Reset Timing	SG2	28986	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-13	A Constant	SG2	28988	Float	4	R W	n/a	0–600	D
76-13	B Constant	SG2	28990	Float	4	R W	n/a	0–25	D
76-13	C Constant	SG2	28992	Float	4	R W	n/a	0–1	D
76-13	N Constant	SG2	28994	Float	4	R W	n/a	0.5–2.5	D
76-13	R Constant	SG2	28996	Float	4	R W	n/a	0–30	D
76-13	Mode	SG3	28998	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Reserved			29000						
76-13	Pickup	SG3	29002	Float	4	R W	Millivolt	2.5–1000	D
76-13	Direction	SG3	29004	Uint32	4	R W	n/a	Forward=0 Reverse=1 Non-Directional=2	D
76-13	Timing Mode	SG3	29006	Uint32	4	R W	n/a	Definite Timing=0 Inverse Timing=1	D
76-13	Time Delay	SG3	29008	Float	4	R W	Millisecond	0–6000000	D
76-13	Reset Delay	SG3	29010	Float	4	R W	Millisecond	0–6000000	D
76-13	Time Dial	SG3	29012	Float	4	R W	n/a	0–9.9	D

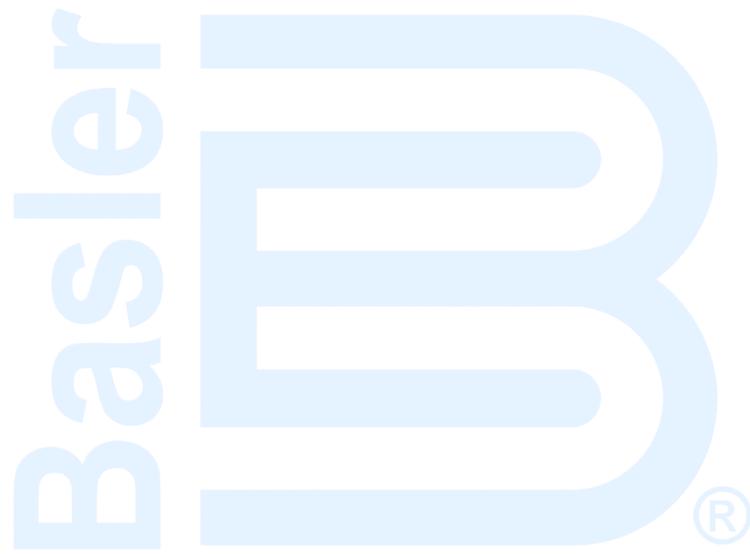
Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
76-13	Curve	SG3	29014	Uint32	4	R W	n/a	S1=0, S2=1, L1=2, L2=3, D=4, M=5, I1=6, I2=7, V1=8, V2=9, E1=10, E2=11, A=12, B=13, C=14, G=15, F=16, 46=17, P=18, T1=19, T2=20, T3=21, T4=22, A1=23, B1=24, C1=25, D1=26, E3=27, F1=28	D
76-13	Reset Timing	SG3	29016	Uint32	4	R W	n/a	Integrating=0 Instantaneous=1	D
76-13	A Constant	SG3	29018	Float	4	R W	n/a	0-600	D
76-13	B Constant	SG3	29020	Float	4	R W	n/a	0-25	D
76-13	C Constant	SG3	29022	Float	4	R W	n/a	0-1	D
76-13	N Constant	SG3	29024	Float	4	R W	n/a	0.5-2.5	D
76-13	R Constant	SG3	29026	Float	4	R W	n/a	0-30	D
ROR-1	ROR Enable	SG0	29028	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-1	ROC Trigger	SG0	29030	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	ROC Minimum	SG0	29032	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	Delta Current Timer Max	SG0	29034	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Delta Timer Max	SG0	29036	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Current Max	SG0	29038	Float	4	R W	Millivolt	2.5-500	D
ROR-1	Current Min	SG0	29040	Float	4	R W	Millivolt	0-500	D
ROR-1	ROR Enable	SG1	29042	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-1	ROC Trigger	SG1	29044	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	ROC Minimum	SG1	29046	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	Delta Current Timer Max	SG1	29048	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Delta Timer Max	SG1	29050	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Current Max	SG1	29052	Float	4	R W	Millivolt	2.5-500	D
ROR-1	Current Min	SG1	29054	Float	4	R W	Millivolt	0-500	D
ROR-1	ROR Enable	SG2	29056	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-1	ROC Trigger	SG2	29058	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	ROC Minimum	SG2	29060	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	Delta Current Timer Max	SG2	29062	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Delta Timer Max	SG2	29064	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Current Max	SG2	29066	Float	4	R W	Millivolt	2.5-500	D
ROR-1	Current Min	SG2	29068	Float	4	R W	Millivolt	0-500	D
ROR-1	ROR Enable	SG3	29070	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-1	ROC Trigger	SG3	29072	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	ROC Minimum	SG3	29074	Float	4	R W	Millivolts per second	25-2500	D
ROR-1	Delta Current Timer Max	SG3	29076	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Delta Timer Max	SG3	29078	Uint32	4	R W	Millisecond	0-500	D
ROR-1	Current Max	SG3	29080	Float	4	R W	Millivolt	2.5-500	D
ROR-1	Current Min	SG3	29082	Float	4	R W	Millivolt	0-500	D
ROR-2	ROR Enable	SG0	29084	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-2	ROC Trigger	SG0	29086	Float	4	R W	Millivolts per second	25-2500	D
ROR-2	ROC Minimum	SG0	29088	Float	4	R W	Millivolts per second	25-2500	D
ROR-2	Delta Current Timer Max	SG0	29090	Uint32	4	R W	Millisecond	0-500	D
ROR-2	Delta Timer Max	SG0	29092	Uint32	4	R W	Millisecond	0-500	D
ROR-2	Current Max	SG0	29094	Float	4	R W	Millivolt	2.5-500	D
ROR-2	Current Min	SG0	29096	Float	4	R W	Millivolt	0-500	D
ROR-2	ROR Enable	SG1	29098	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
ROR-2	ROC Trigger	SG1	29100	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	ROC Minimum	SG1	29102	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	Delta Current Timer Max	SG1	29104	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Delta Timer Max	SG1	29106	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Current Max	SG1	29108	Float	4	R W	Millivolt	2.5–500	D
ROR-2	Current Min	SG1	29110	Float	4	R W	Millivolt	0–500	D
ROR-2	ROR Enable	SG2	29112	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-2	ROC Trigger	SG2	29114	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	ROC Minimum	SG2	29116	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	Delta Current Timer Max	SG2	29118	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Delta Timer Max	SG2	29120	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Current Max	SG2	29122	Float	4	R W	Millivolt	2.5–500	D
ROR-2	Current Min	SG2	29124	Float	4	R W	Millivolt	0–500	D
ROR-2	ROR Enable	SG3	29126	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
ROR-2	ROC Trigger	SG3	29128	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	ROC Minimum	SG3	29130	Float	4	R W	Millivolts per second	25–2500	D
ROR-2	Delta Current Timer Max	SG3	29132	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Delta Timer Max	SG3	29134	Uint32	4	R W	Millisecond	0–500	D
ROR-2	Current Max	SG3	29136	Float	4	R W	Millivolt	2.5–500	D
ROR-2	Current Min	SG3	29138	Float	4	R W	Millivolt	0–500	D
82	Mode	SG0	29140	Uint32	4	R W	n/a	Disabled=0 Power to Lockout=1 Power to Reclose=2	D
82	Reclose 1 Time	SG0	29142	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 2 Time	SG0	29144	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 3 Time	SG0	29146	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 4 Time	SG0	29148	Uint32	4	R W	Millisecond	100–600000	D
82	Reset Time	SG0	29150	Uint32	4	R W	Millisecond	100–600000	D
82	Fail Time	SG0	29152	Uint32	4	R W	Millisecond	100–600000	D
82	Max Time	SG0	29154	Uint32	4	R W	Millisecond	100–600000	D
82	Load Measurement Time	SG0	29156	Uint32	4	R W	Millisecond	500–990000	D
82	R Sense	SG0	29158	Float	4	R W	Ohm	10–9999	D
82	R Limit	SG0	29160	Float	4	R W	Ohm	0.01–5	D
82	Mode	SG1	29162	Uint32	4	R W	n/a	Disabled=0 Power to Lockout=1 Power to Reclose=2	D
82	Reclose 1 Time	SG1	29164	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 2 Time	SG1	29166	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 3 Time	SG1	29168	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 4 Time	SG1	29170	Uint32	4	R W	Millisecond	100–600000	D
82	Reset Time	SG1	29172	Uint32	4	R W	Millisecond	100–600000	D
82	Fail Time	SG1	29174	Uint32	4	R W	Millisecond	100–600000	D
82	Max Time	SG1	29176	Uint32	4	R W	Millisecond	100–600000	D
82	Load Measurement Time	SG1	29178	Uint32	4	R W	Millisecond	500–990000	D
82	R Sense	SG1	29180	Float	4	R W	Ohm	10–9999	D
82	R Limit	SG1	29182	Float	4	R W	Ohm	0.01–5	D
82	Mode	SG2	29184	Uint32	4	R W	n/a	Disabled=0 Power to Lockout=1 Power to Reclose=2	D
82	Reclose 1 Time	SG2	29186	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 2 Time	SG2	29188	Uint32	4	R W	Millisecond	100–600000	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
82	Reclose 3 Time	SG2	29190	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 4 Time	SG2	29192	Uint32	4	R W	Millisecond	100–600000	D
82	Reset Time	SG2	29194	Uint32	4	R W	Millisecond	100–600000	D
82	Fail Time	SG2	29196	Uint32	4	R W	Millisecond	100–600000	D
82	Max Time	SG2	29198	Uint32	4	R W	Millisecond	100–600000	D
82	Load Measurement Time	SG2	29200	Uint32	4	R W	Millisecond	500–990000	D
82	R Sense	SG2	29202	Float	4	R W	Ohm	10–9999	D
82	R Limit	SG2	29204	Float	4	R W	Ohm	0.01–5	D
82	Mode	SG3	29206	Uint32	4	R W	n/a	Disabled=0 Power to Lockout=1 Power to Reclose=2	D
82	Reclose 1 Time	SG3	29208	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 2 Time	SG3	29210	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 3 Time	SG3	29212	Uint32	4	R W	Millisecond	100–600000	D
82	Reclose 4 Time	SG3	29214	Uint32	4	R W	Millisecond	100–600000	D
82	Reset Time	SG3	29216	Uint32	4	R W	Millisecond	100–600000	D
82	Fail Time	SG3	29218	Uint32	4	R W	Millisecond	100–600000	D
82	Max Time	SG3	29220	Uint32	4	R W	Millisecond	100–600000	D
82	Load Measurement Time	SG3	29222	Uint32	4	R W	Millisecond	500–990000	D
82	R Sense	SG3	29224	Float	4	R W	Ohm	10–9999	D
82	R Limit	SG3	29226	Float	4	R W	Ohm	0.01–5	D
72	Mode	GG	29228	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
49	Mode	SG0	29230	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
49	Overload	SG0	29232	Float	4	R W	Millivolt	0.5–300	D
49	Trip Time Constant	SG0	29234	Float	4	R W	Minute	0–1000	D
49	Reset Time Constant	SG0	29236	Float	4	R W	Minute	0–1000	D
49	Minimum Reset Time	SG0	29238	Float	4	R W	Minute	0–1000	D
49	Alarm Level	SG0	29240	Float	4	R W	Percent	10–100	D
Reserved			29242						
49	Mode	SG1	29244	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
49	Overload	SG1	29246	Float	4	R W	Millivolt	0.5–300	D
49	Trip Time Constant	SG1	29248	Float	4	R W	Minute	0–1000	D
49	Reset Time Constant	SG1	29250	Float	4	R W	Minute	0–1000	D
49	Minimum Reset Time	SG1	29252	Float	4	R W	Minute	0–1000	D
49	Alarm Level	SG1	29254	Float	4	R W	Percent	10–100	D
Reserved			29256						
49	Mode	SG2	29258	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
49	Overload	SG2	29260	Float	4	R W	Millivolt	0.5–300	D
49	Trip Time Constant	SG2	29262	Float	4	R W	Minute	0–1000	D
49	Reset Time Constant	SG2	29264	Float	4	R W	Minute	0–1000	D
49	Minimum Reset Time	SG2	29266	Float	4	R W	Minute	0–1000	D
49	Alarm Level	SG2	29268	Float	4	R W	Percent	10–100	D
Reserved			29270						
49	Mode	SG3	29272	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
49	Overload	SG3	29274	Float	4	R W	Millivolt	0.5–300	D
49	Trip Time Constant	SG3	29276	Float	4	R W	Minute	0–1000	D
49	Reset Time Constant	SG3	29278	Float	4	R W	Minute	0–1000	D
49	Minimum Reset Time	SG3	29280	Float	4	R W	Minute	0–1000	D
49	Alarm Level	SG3	29282	Float	4	R W	Percent	10–100	D
Reserved			29284						
Voltage Monitor	Mode	SG0	29286	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D

Name	Description	Group	Register	Type	Bytes	Writable	Unit	Range	Style
Voltage Monitor	Line Source	SG0	29288	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Bus Source	SG0	29290	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Live Line Threshold	SG0	29292	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Live Bus Threshold	SG0	29294	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Line Threshold	SG0	29296	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Bus Threshold	SG0	29298	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Mode	SG1	29300	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Voltage Monitor	Line Source	SG1	29302	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Bus Source	SG1	29304	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Live Line Threshold	SG1	29306	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Live Bus Threshold	SG1	29308	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Line Threshold	SG1	29310	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Bus Threshold	SG1	29312	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Mode	SG2	29314	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Voltage Monitor	Line Source	SG2	29316	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Bus Source	SG2	29318	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Live Line Threshold	SG2	29320	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Live Bus Threshold	SG2	29322	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Line Threshold	SG2	29324	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Bus Threshold	SG2	29326	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Mode	SG3	29328	Uint32	4	R W	n/a	Disabled=0 Enabled=1	D
Voltage Monitor	Line Source	SG3	29330	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Bus Source	SG3	29332	Uint32	4	R W	n/a	Voltage 1=0 Voltage 2=1 Voltage 3=2	D
Voltage Monitor	Live Line Threshold	SG3	29334	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Live Bus Threshold	SG3	29336	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Line Threshold	SG3	29338	Float	4	R W	Volt	50–1500	D
Voltage Monitor	Dead Bus Threshold	SG3	29340	Float	4	R W	Volt	50–1500	D

* Legacy register



20 • User Labels Register Table

Name	Description	Register	Type	Bytes	Writable	Range
Programmable Labels	Device ID Label	30100	String	64	R W	0–64 Characters
Programmable Labels	Station Name Label	30132	String	64	R W	0–64 Characters
Programmable Labels	43-1 Name	30164	String	64	R W	0–64 Characters
Programmable Labels	43-1 On	30196	String	64	R W	0–64 Characters
Programmable Labels	43-1 Off	30228	String	64	R W	0–64 Characters
Programmable Labels	43-2 Name	30260	String	64	R W	0–64 Characters
Programmable Labels	43-2 On	30292	String	64	R W	0–64 Characters
Programmable Labels	43-2 Off	30324	String	64	R W	0–64 Characters
Programmable Labels	43-3 Name	30356	String	64	R W	0–64 Characters
Programmable Labels	43-3 On	30388	String	64	R W	0–64 Characters
Programmable Labels	43-3 Off	30420	String	64	R W	0–64 Characters
Programmable Labels	43-4 Name	30452	String	64	R W	0–64 Characters
Programmable Labels	43-4 On	30484	String	64	R W	0–64 Characters
Programmable Labels	43-4 Off	30516	String	64	R W	0–64 Characters
Programmable Labels	43-5 Name	30548	String	64	R W	0–64 Characters
Programmable Labels	43-5 On	30580	String	64	R W	0–64 Characters
Programmable Labels	43-5 Off	30612	String	64	R W	0–64 Characters
Programmable Labels	Input Contact 1	30644	String	64	R W	0–64 Characters
Programmable Labels	Input Contact 2	30676	String	64	R W	0–64 Characters
Programmable Labels	Input Contact 3	30708	String	64	R W	0–64 Characters
Programmable Labels	Input Contact 4	30740	String	64	R W	0–64 Characters
Programmable Labels	Output Contact 1	30772	String	64	R W	0–64 Characters
Programmable Labels	Output Contact 2	30804	String	64	R W	0–64 Characters
Programmable Labels	Output Contact 3	30836	String	64	R W	0–64 Characters
Programmable Labels	Output Contact 4	30868	String	64	R W	0–64 Characters
Programmable Labels	Output Contact 5	30900	String	64	R W	0–64 Characters
Programmable Labels	Output Contact A	30932	String	64	R W	0–64 Characters
Programmable Labels	Input 1 On	30964	String	64	R W	0–64 Characters
Programmable Labels	Input 2 On	30996	String	64	R W	0–64 Characters
Programmable Labels	Input 3 On	31028	String	64	R W	0–64 Characters
Programmable Labels	Input 4 On	31060	String	64	R W	0–64 Characters
Programmable Labels	Output 1 On	31092	String	64	R W	0–64 Characters
Programmable Labels	Output 2 On	31124	String	64	R W	0–64 Characters
Programmable Labels	Output 3 On	31156	String	64	R W	0–64 Characters
Programmable Labels	Output 4 On	31188	String	64	R W	0–64 Characters
Programmable Labels	Output 5 On	31220	String	64	R W	0–64 Characters
Programmable Labels	Output A On	31252	String	64	R W	0–64 Characters
Programmable Labels	Input 1 Off	31284	String	64	R W	0–64 Characters
Programmable Labels	Input 2 Off	31316	String	64	R W	0–64 Characters
Programmable Labels	Input 3 Off	31348	String	64	R W	0–64 Characters
Programmable Labels	Input 4 Off	31380	String	64	R W	0–64 Characters
Programmable Labels	Output 1 Off	31412	String	64	R W	0–64 Characters
Programmable Labels	Output 2 Off	31444	String	64	R W	0–64 Characters
Programmable Labels	Output 3 Off	31476	String	64	R W	0–64 Characters
Programmable Labels	Output 4 Off	31508	String	64	R W	0–64 Characters
Programmable Labels	Output 5 Off	31540	String	64	R W	0–64 Characters
Programmable Labels	Output A Off	31572	String	64	R W	0–64 Characters
Programmable Labels	User ID	31604	String	64	R W	0–64 Characters
Programmable Labels	Settings File Name	31636	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 1 Name	31668	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 2 Name	31700	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 3 Name	31732	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 4 Name	31764	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 5 Name	31796	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 6 Name	31828	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 7 Name	31860	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 8 Name	31892	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 9 Name	31924	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 10 Name	31956	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 11 Name	31988	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 12 Name	32020	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 13 Name	32052	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 14 Name	32084	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 15 Name	32116	String	64	R W	0–64 Characters
Programmable Labels	Logic Timer 16 Name	32148	String	64	R W	0–64 Characters
Programmable Labels	Counter 1 Name	32180	String	64	R W	0–64 Characters
Programmable Labels	Counter 2 Name	32212	String	64	R W	0–64 Characters
Programmable Labels	Counter 3 Name	32244	String	64	R W	0–64 Characters
Programmable Labels	Counter 4 Name	32276	String	64	R W	0–64 Characters
Programmable Labels	Counter 5 Name	32308	String	64	R W	0–64 Characters
Programmable Labels	Counter 6 Name	32340	String	64	R W	0–64 Characters
Programmable Labels	Counter 7 Name	32372	String	64	R W	0–64 Characters
Programmable Labels	Counter 8 Name	32404	String	64	R W	0–64 Characters

Name	Description	Register	Type	Bytes	Writable	Range
Programmable Labels	Programmable Alarm 1 Name	32436	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 2 Name	32468	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 3 Name	32500	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 4 Name	32532	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 5 Name	32564	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 6 Name	32596	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 7 Name	32628	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 8 Name	32660	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 9 Name	32692	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 10 Name	32724	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 11 Name	32756	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 12 Name	32788	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 13 Name	32820	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 14 Name	32852	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 15 Name	32884	String	64	R W	0-64 Characters
Programmable Labels	Programmable Alarm 16 Name	32916	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 5	32948	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 6	32980	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 7	33012	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 8	33044	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 9	33076	String	64	R W	0-64 Characters
Programmable Labels	Input Contact 10	33108	String	64	R W	0-64 Characters
Programmable Labels	Output Contact 6	33140	String	64	R W	0-64 Characters
Programmable Labels	Output Contact 7	33172	String	64	R W	0-64 Characters
Programmable Labels	Output Contact 8	33204	String	64	R W	0-64 Characters
Reserved		33236-331				
Programmable Labels	Input 5 On	33332	String	64	R W	0-64 Characters
Programmable Labels	Input 6 On	33364	String	64	R W	0-64 Characters
Programmable Labels	Input 7 On	33396	String	64	R W	0-64 Characters
Programmable Labels	Input 8 On	33428	String	64	R W	0-64 Characters
Programmable Labels	Input 9 On	33460	String	64	R W	0-64 Characters
Programmable Labels	Input 10 On	33492	String	64	R W	0-64 Characters
Programmable Labels	Output 6 On	33524	String	64	R W	0-64 Characters
Programmable Labels	Output 7 On	33556	String	64	R W	0-64 Characters
Programmable Labels	Output 8 On	33588	String	64	R W	0-64 Characters
Reserved		33620-715				
Programmable Labels	Input 5 Off	33716	String	64	R W	0-64 Characters
Programmable Labels	Input 6 Off	33748	String	64	R W	0-64 Characters
Programmable Labels	Input 7 Off	33780	String	64	R W	0-64 Characters
Programmable Labels	Input 8 Off	33812	String	64	R W	0-64 Characters
Programmable Labels	Input 9 Off	33844	String	64	R W	0-64 Characters
Programmable Labels	Input 10 Off	33876	String	64	R W	0-64 Characters
Programmable Labels	Output 6 Off	33908	String	64	R W	0-64 Characters
Programmable Labels	Output 7 Off	33940	String	64	R W	0-64 Characters
Programmable Labels	Output 8 Off	33972	String	64	R W	0-64 Characters
Reserved		34004-99				
Programmable Labels	RTD Group 1	34100	String	64	R W	0-64 Characters
Programmable Labels	RTD Group 2	34132	String	64	R W	0-64 Characters
Programmable Labels	RTD Group 3	34164	String	64	R W	0-64 Characters
Programmable Labels	RTD Group 4	34196	String	64	R W	0-64 Characters
Logic Labels	Logic Label 1	34228	String	64	R W	0-64 Characters
Logic Labels	Logic Label 2	34260	String	64	R W	0-64 Characters
Logic Labels	Logic Label 3	34292	String	64	R W	0-64 Characters
Logic Labels	Logic Label 4	34324	String	64	R W	0-64 Characters
Logic Labels	Logic Label 5	34356	String	64	R W	0-64 Characters
Logic Labels	Logic Label 6	34388	String	64	R W	0-64 Characters
Logic Labels	Logic Label 7	34420	String	64	R W	0-64 Characters
Logic Labels	Logic Label 8	34452	String	64	R W	0-64 Characters
Logic Labels	Logic Label 9	34484	String	64	R W	0-64 Characters
Logic Labels	Logic Label 10	34516	String	64	R W	0-64 Characters
Logic Labels	Logic Label 11	34548	String	64	R W	0-64 Characters
Logic Labels	Logic Label 12	34580	String	64	R W	0-64 Characters
Analog Input Labels	Label 1-1	34612	String	64	R W	0-64 Characters
Analog Input Labels	Label 1-2	34644	String	64	R W	0-64 Characters
Analog Input Labels	Label 1-3	34676	String	64	R W	0-64 Characters
Analog Input Labels	Label 1-4	34708	String	64	R W	0-64 Characters
Analog Input Labels	Label 2-1	34740	String	64	R W	0-64 Characters
Analog Input Labels	Label 2-2	34772	String	64	R W	0-64 Characters
Analog Input Labels	Label 2-3	34804	String	64	R W	0-64 Characters
Analog Input Labels	Label 2-4	34836	String	64	R W	0-64 Characters
Programmable Labels	LED 1	34868	String	64	R W	0-64 Characters
Programmable Labels	LED 2	34900	String	64	R W	0-64 Characters
Programmable Labels	LED 3	34932	String	64	R W	0-64 Characters
Programmable Labels	LED 4	34964	String	64	R W	0-64 Characters
Programmable Labels	LED 5	34996	String	64	R W	0-64 Characters
Programmable Labels	LED 6	35028	String	64	R W	0-64 Characters
Programmable Labels	LED 7	35060	String	64	R W	0-64 Characters
Programmable Labels	RTD Group 5	35092	String	64	R W	0-64 Characters

Name	Description	Register	Type	Bytes	Writable	Range
Programmable Labels	RTD Group 6	35124	String	64	R W	0-64 Characters
Programmable Labels	RTD Group 7	35156	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 1	35188	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 2	35220	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 3	35252	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 4	35284	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 5	35316	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 6	35348	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 7	35380	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 8	35412	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 9	35444	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 10	35476	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 11	35508	String	64	R W	0-64 Characters
Programmable Targets	Programmable Target 12	35540	String	64	R W	0-64 Characters



21 • Alarms and Targets Reporting

Alarm and target information is obtained by reading the appropriate register. If an alarm or target is present, the register will return numeric a value. In a case where more than one alarm or target is present, you must repeat reading of the register until a value of zero (0) is returned.

Table 21-1 lists the reporting registers. Table 21-2 provides a list of possible alarms and targets reported by these registers.

Table 21-1. Reporting Registers

Name	Description	Register	Type	Bytes
Major Alarm	Read Alarms	1206	Uint32	4
Minor Alarm	Read Alarms	1208	Uint32	4
Targets	Read Targets	1210	Uint32	4
Relay Alarms	Read Alarms	1212	Uint32	4
Logic Alarms	Read Alarms	1214	Uint32	4

Table 21-2. Alarms and Targets

Index	Description
64	60FL Alarm
69	60FL Target
99	24 Target
100	24 Alarm
163	27-1 Target A
164	27-1 Target B
165	27-1 Target C
195	27-2 Target A
196	27-2 Target B
197	27-2 Target C
227	27-3 Target A
228	27-3 Target B
229	27-3 Target C
259	27-4 Target A
260	27-4 Target B
261	27-4 Target C
291	27-5 Target A
292	27-5 Target B
293	27-5 Target C
323	27X-1 Target 3V0
324	27X-1 Target V2
325	27X-1 Target Aux
326	27X-1 Target Vx 3rd Harmonic
327	27X-1 Target V1
330	27-1 Target
355	27X-2 Target 3V0
356	27X-2 Target V2
357	27X-2 Target Aux

Index	Description
358	27X-2 Target Vx 3rd Harmonic
359	27X-2 Target V1
362	27-2 Target
387	27X-3 Target 3V0
388	27X-3 Target V2
389	27X-3 Target Aux
390	27X-3 Target Vx 3rd Harmonic
391	27X-3 Target V1
394	27-3 Target
419	27X-4 Target 3V0
420	27X-4 Target V2
421	27X-4 Target Aux
422	27X-4 Target Vx 3rd Harmonic
423	27X-4 Target V1
426	27-4 Target
451	59-1 Target A
452	59-1 Target B
453	59-1 Target C
483	59-2 Target A
484	59-2 Target B
485	59-2 Target C
515	59-3 Target A
516	59-3 Target B
517	59-3 Target C
547	59-4 Target A
548	59-4 Target B
549	59-4 Target C
579	59X-1 Target 3V0
580	59X-1 Target V2
581	59X-1 Target Aux
582	59X-1 Target Vx 3rd Harmonic
583	59X-1 Target V1
586	59-1 Target
611	59X-2 Target 3V0
612	59X-2 Target V2
613	59X-2 Target Aux
614	59X-2 Target Vx 3rd Harmonic
615	59X-2 Target V1
618	59-2 Target
643	59X-3 Target 3V0
644	59X-3 Target V2
645	59X-3 Target Aux
646	59X-3 Target Vx 3rd Harmonic
647	59X-3 Target V1

Index	Description
650	59-3 Target
675	59X-4 Target 3V0
676	59X-4 Target V2
677	59X-4 Target Aux
678	59X-4 Target Vx 3rd Harmonic
679	59X-4 Target V1
682	59-4 Target
739	50-1 Target A
740	50-1 Target B
741	50-1 Target C
742	50-1 Target Negative Sequence
743	50-1 Target Residual
744	50-1 Target Independent Ground
745	50-1 Target 67 A
746	50-1 Target 67 B
747	50-1 Target 67 C
748	50-1 Target 67 Negative Sequence
749	50-1 Target 67 Residual
750	50-1 Target 67 Independent Ground
751	50-1 Target Positive Sequence
752	50-1 Target 67 Positive Sequence
753	50-1 Target Unbalance
754	50-1 Target 67 Unbalance
771	50-2 Target A
772	50-2 Target B
773	50-2 Target C
774	50-2 Target Negative Sequence
775	50-2 Target Residual
776	50-2 Target Independent Ground
777	50-2 Target 67 A
778	50-2 Target 67 B
779	50-2 Target 67 C
780	50-2 Target 67 Negative Sequence
781	50-2 Target 67 Residual
782	50-2 Target 67 Independent Ground
783	50-2 Target Positive Sequence
784	50-2 Target 67 Positive Sequence
785	50-2 Target Unbalance
786	50-2 Target 67 Unbalance
803	50-3 Target A
804	50-3 Target B
805	50-3 Target C
806	50-3 Target Negative Sequence
807	50-3 Target Residual

Index	Description
808	50-3 Target Independent Ground
809	50-3 Target 67 A
810	50-3 Target 67 B
811	50-3 Target 67 C
812	50-3 Target 67 Negative Sequence
813	50-3 Target 67 Residual
814	50-3 Target 67 Independent Ground
815	50-3 Target Positive Sequence
816	50-3 Target 67 Positive Sequence
817	50-3 Target Unbalance
818	50-3 Target 67 Unbalance
835	50-4 Target A
836	50-4 Target B
837	50-4 Target C
838	50-4 Target Negative Sequence
839	50-4 Target Residual
840	50-4 Target Independent Ground
841	50-4 Target 67 A
842	50-4 Target 67 B
843	50-4 Target 67 C
844	50-4 Target 67 Negative Sequence
845	50-4 Target 67 Residual
846	50-4 Target 67 Independent Ground
847	50-4 Target Positive Sequence
848	50-4 Target 67 Positive Sequence
849	50-4 Target Unbalance
850	50-4 Target 67 Unbalance
867	50-5 Target A
868	50-5 Target B
869	50-5 Target C
870	50-5 Target Negative Sequence
871	50-5 Target Residual
872	50-5 Target Independent Ground
873	50-5 Target 67 A
874	50-5 Target 67 B
875	50-5 Target 67 C
876	50-5 Target 67 Negative Sequence
877	50-5 Target 67 Residual
878	50-5 Target 67 Independent Ground
879	50-5 Target Positive Sequence
880	50-5 Target 67 Positive Sequence
881	50-5 Target Unbalance
882	50-5 Target 67 Unbalance
899	50-6 Target A

Index	Description
900	50-6 Target B
901	50-6 Target C
902	50-6 Target Negative Sequence
903	50-6 Target Residual
904	50-6 Target Independent Ground
905	50-6 Target 67 A
906	50-6 Target 67 B
907	50-6 Target 67 C
908	50-6 Target 67 Negative Sequence
909	50-6 Target 67 Residual
910	50-6 Target 67 Independent Ground
911	50-6 Target Positive Sequence
912	50-6 Target 67 Positive Sequence
913	50-6 Target Unbalance
914	50-6 Target 67 Unbalance
931	51-1 Target A
932	51-1 Target B
933	51-1 Target C
934	51-1 Target Negative Sequence
935	51-1 Target Residual
936	51-1 Target Independent Ground
937	51-1 Target 67 A
938	51-1 Target 67 B
939	51-1 Target 67 C
940	51-1 Target 67 Negative Sequence
941	51-1 Target 67 Residual
942	51-1 Target 67 Independent Ground
943	51-1 Target Positive Sequence
944	51-1 Target 67 Positive Sequence
945	51-1 Target Unbalance
946	51-1 Target 67 Unbalance
963	51-2 Target A
964	51-2 Target B
965	51-2 Target C
966	51-2 Target Negative Sequence
967	51-2 Target Residual
968	51-2 Target Independent Ground
969	51-2 Target 67 A
970	51-2 Target 67 B
971	51-2 Target 67 C
972	51-2 Target 67 Negative Sequence
973	51-2 Target 67 Residual
974	51-2 Target 67 Independent Ground
975	51-2 Target Positive Sequence

Index	Description
976	51-2 Target 67 Positive Sequence
977	51-2 Target Unbalance
978	51-2 Target 67 Unbalance
995	51-3 Target A
996	51-3 Target B
997	51-3 Target C
998	51-3 Target Negative Sequence
999	51-3 Target Residual
1000	51-3 Target Independent Ground
1001	51-3 Target 67 A
1002	51-3 Target 67 B
1003	51-3 Target 67 C
1004	51-3 Target 67 Negative Sequence
1005	51-3 Target 67 Residual
1006	51-3 Target 67 Independent Ground
1007	51-3 Target Positive Sequence
1008	51-3 Target 67 Positive Sequence
1009	51-3 Target Unbalance
1010	51-3 Target 67 Unbalance
1027	51-4 Target A
1028	51-4 Target B
1029	51-4 Target C
1030	51-4 Target Negative Sequence
1031	51-4 Target Residual
1032	51-4 Target Independent Ground
1033	51-4 Target 67 A
1034	51-4 Target 67 B
1035	51-4 Target 67 C
1036	51-4 Target 67 Negative Sequence
1037	51-4 Target 67 Residual
1038	51-4 Target 67 Independent Ground
1039	51-4 Target Positive Sequence
1040	51-4 Target 67 Positive Sequence
1041	51-4 Target Unbalance
1042	51-5 Target 67 Unbalance
1059	51-5 Target A
1060	51-5 Target B
1061	51-5 Target C
1062	51-5 Target Negative Sequence
1063	51-5 Target Residual
1064	51-5 Target Independent Ground
1065	51-5 Target 67 A
1066	51-5 Target 67 B
1067	51-5 Target 67 C

Index	Description
1068	51-5 Target 67 Negative Sequence
1069	51-5 Target 67 Residual
1070	51-5 Target 67 Independent Ground
1071	51-5 Target Positive Sequence
1072	51-5 Target 67 Positive Sequence
1073	51-5 Target Unbalance
1074	51-5 Target 67 Unbalance
1091	51-6 Target A
1092	51-6 Target B
1093	51-6 Target C
1094	51-6 Target Negative Sequence
1095	51-6 Target Residual
1096	51-6 Target Independent Ground
1097	51-6 Target 67 A
1098	51-6 Target 67 B
1099	51-6 Target 67 C
1100	51-6 Target 67 Negative Sequence
1101	51-6 Target 67 Residual
1102	51-6 Target 67 Independent Ground
1103	51-6 Target Positive Sequence
1104	51-6 Target 67 Positive Sequence
1105	51-6 Target Unbalance
1106	51-6 Target 67 Unbalance
1123	51-7 Target A
1124	51-7 Target B
1125	51-7 Target C
1126	51-7 Target Negative Sequence
1127	51-7 Target Residual
1128	51-7 Target Independent Ground
1129	51-7 Target 67 A
1130	51-7 Target 67 B
1131	51-7 Target 67 C
1132	51-7 Target 67 Negative Sequence
1133	51-7 Target 67 Residual
1134	51-7 Target 67 Independent Ground
1135	51-7 Target Positive Sequence
1136	51-7 Target 67 Positive Sequence
1137	51-7 Target Unbalance
1138	51-7 Target 67 Unbalance
1155	32-1 Target A Over
1156	31-1 Target B Over
1157	32-1 Target C Over
1158	32-1 Target Total Over
1159	32-1 Target A Under

Index	Description
1160	32-1 Target B Under
1161	32-1 Target C Under
1162	32-1 Target Total Under
1165	32-1 Target Over
1166	32-1 Target Under
1187	32-2 Target A Over
1188	31-2 Target B Over
1189	32-2 Target C Over
1190	32-2 Target Total Over
1191	32-2 Target A Under
1192	32-2 Target B Under
1193	32-2 Target C Under
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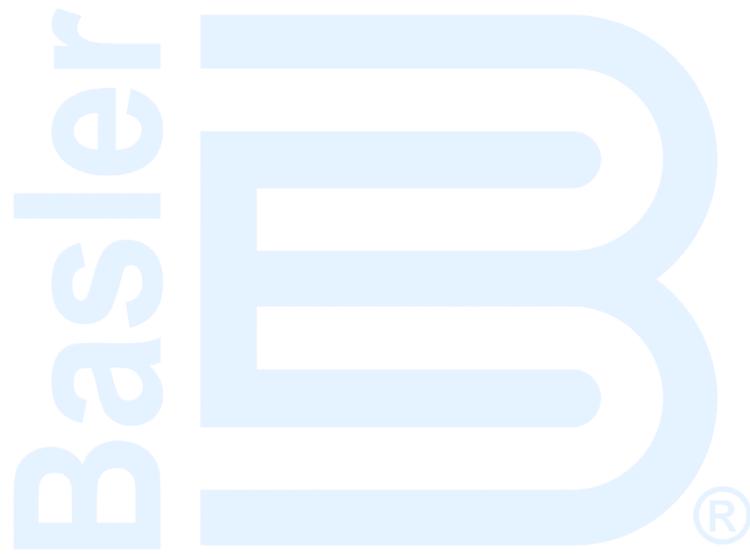
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5511	User Programmable Target 8
5512	User Programmable Target 9
5513	User Programmable Target 10
5514	User Programmable Target 11
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5569	Any Phase B Target
5570	Any Phase C Target
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5572	Any Neutral Target
5573	Any Ground/Neutral Target
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5731	Input V3, No IT-D Connection
5733	Input I1, IT-D Frequency Mismatch
5734	Input V1, IT-D Frequency Mismatch
5735	Input V2, IT-D Frequency Mismatch
5736	Input V3, IT-D Frequency Mismatch
5763	76-1 Target
5795	76-2 Target
5827	76-3 Target
5859	76-4 Target
5891	76-5 Target
5923	76-6 Target

Index	Description
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5987	76-8 Target
6019	76-9 Target
6051	76-10 Target
6083	76-11 Target
6115	76-12 Target
6147	76-13 Target
6183	Rate of Rise 1 Target
6215	Rate of Rise 2 Target
6240	82 Fail
6244	82 Lockout
6273	72 Alarm
6307	49 Alarm
6310	49 Target
6368	I1 Positive Demand Alarm
6369	I1 Negative Demand Alarm
6370	P1 Positive Demand Alarm
6371	P1 Negative Demand Alarm



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